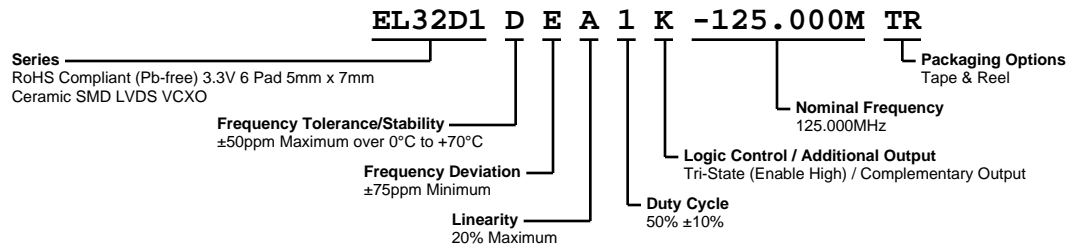


EL32D1DEA1K-125.000M TR



ELECTRICAL SPECIFICATIONS

Nominal Frequency	125.000MHz
Frequency Tolerance/Stability	±50ppm Maximum over 0°C to +70°C (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, 1st Year Aging at 25°C, Shock, and Vibration.)
Supply Voltage	3.3Vdc ±5%
Input Current	80mA Maximum
Output Voltage Logic High (Voh)	1.43Vdc Typical, 1.6Vdc Maximum
Output Voltage Logic Low (Vol)	1.1Vdc Typical, 0.9Vdc Minimum
Vdd Magnitude Change (dVod)	-50mV Minimum, +50mV Maximum
Differential Output Voltage (Vod)	247mV Minimum, 355mV Typical, 454mV Maximum
Offset Voltage (Vos)	1.125V Minimum, 1.20V Typical, 1.375V Maximum
Rise/Fall Time	1nSec Maximum (Measured over 20% to 80% of waveform)
Duty Cycle	50% ±10% (Measured at 50% of waveform)
Offset Voltage Magnitude Change (dVos)	25mV Maximum
Load Drive Capability	100 Ohms and 10pF (Between Output and Complementary Output)
Output Logic Type	LVDS
Control Voltage	1.65Vdc ±1.65Vdc (Test Conditions for Frequency Deviation)
Control Voltage Range	0.0Vdc to Vcc +0.5Vdc
Frequency Deviation	±75ppm Minimum (Inclusive of Operating Temperature Range, Supply Voltage, and Load)
Linearity	20% Maximum
Transfer Function	Positive Transfer Characteristic
Modulation Bandwidth	10kHz Minimum (Measured at -3dB with Control Voltage of +1.65Vdc)
Input Impedance	50kOhms Typical
Phase Noise	-55dBc/Hz at 10Hz offset, -90dBc/Hz at 100Hz offset, -120dBc/Hz at 1kHz offset, -140dBc/Hz at 10kHz offset, -145dBc/Hz at 100kHz offset, -148dBc/Hz at 1MHz offset (Typical Values at Fo = 155.520MHz)
Logic Control / Additional Output	Tri-State (Enable High) / Complementary Output
Tri-State Input Voltage (Vih and Vil)	Vih of 70% of Vcc Minimum or No Connect to Enable Outputs, Vil of 30% of Vcc Maximum to Disable Outputs (High Impedance)
RMS Phase Jitter	0.4pSec Typical, 1pSec Maximum (Fj=12kHz to 20MHz)
Accumulated Period Jitter (tacc)	4pSec Typical, 5pSec Maximum Sigma of Total Jitter Distribution
Period Jitter (trj)	3pSec Typical, 5pSec Maximum Sigma of Random Jitter
Period Jitter (trms)	3pSec Typical, 5pSec Maximum Sigma of Total Jitter Distribution
Period Jitter (tdj)	4pSec Typical, 10pSec Maximum Deterministic Jitter
Period Jitter (tp-p)	27pSec Typical, 40pSec Maximum Peak to Peak of Jitter Distribution
Start Up Time	10mSec Maximum
Storage Temperature Range	-55°C to +125°C

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

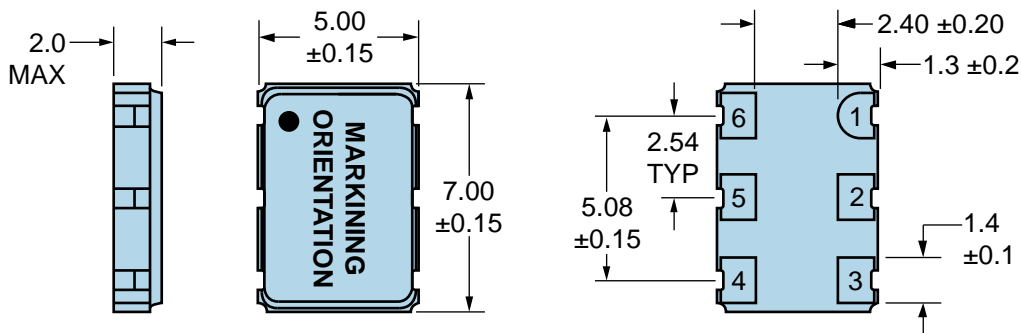
Fine Leak Test	MIL-STD-883, Method 1014 Condition A
Gross Leak Test	MIL-STD-883, Method 1014 Condition C
Mechanical Shock	MIL-STD-202, Method 213 Condition C

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ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

Resistance to Soldering Heat	MIL-STD-202, Method 210
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003
Temperature Cycling	MIL-STD-883, Method 1010
Vibration	MIL-STD-883, Method 2007 Condition A

MECHANICAL DIMENSIONS (all dimensions in millimeters)

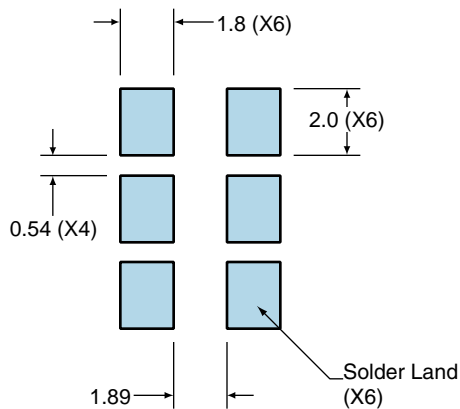


PIN	CONNECTION
1	Voltage Control
2	Tri-State
3	Case/Ground
4	Output
5	Complementary Output
6	Supply Voltage

LINE	MARKING
1	ECLIPTEK
2	125.00M
3	XXYZZ XX=Ecliptek Manufacturing Code Y=Last Digit of the Year ZZ=Week of the Year

Suggested Solder Pad Layout

All Dimensions in Millimeters

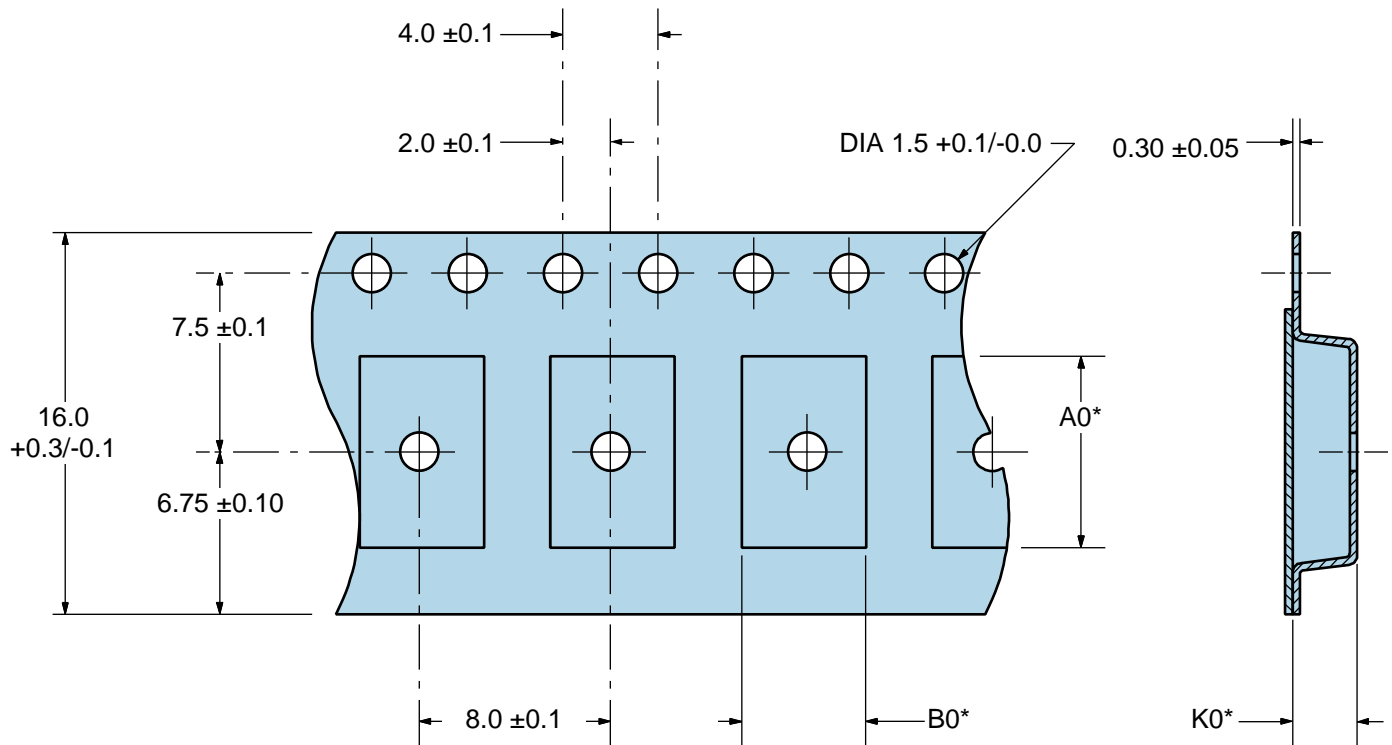


All Tolerances are ±0.1

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Tape & Reel Dimensions

Quantity Per Reel: 1,000 units



*Compliant to EIA 481A



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Recommended Solder Reflow Methods



High Temperature Infrared/Convection

T_s MAX to T_L (Ramp-up Rate)	3°C/second Maximum
Preheat	
- Temperature Minimum (T _s MIN)	150°C
- Temperature Typical (T _s TYP)	175°C
- Temperature Maximum (T _s MAX)	200°C
- Time (t _s MIN)	60 - 180 Seconds
Ramp-up Rate (T_L to T_p)	3°C/second Maximum
Time Maintained Above:	
- Temperature (T _L)	217°C
- Time (t _L)	60 - 150 Seconds
Peak Temperature (T_p)	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (T_p Target)	250°C +0/-5°C
Time within 5°C of actual peak (t_p)	20 - 40 seconds
Ramp-down Rate	6°C/second Maximum
Time 25°C to Peak Temperature (t)	8 minutes Maximum
Moisture Sensitivity Level	Level 1

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Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

T_s MAX to T_L (Ramp-up Rate)	5°C/second Maximum
Preheat	
- Temperature Minimum (T _s MIN)	N/A
- Temperature Typical (T _s TYP)	150°C
- Temperature Maximum (T _s MAX)	N/A
- Time (t _s MIN)	60 - 120 Seconds
Ramp-up Rate (T_L to T_p)	5°C/second Maximum
Time Maintained Above:	
- Temperature (T _L)	150°C
- Time (t _L)	200 Seconds Maximum
Peak Temperature (T_p)	240°C Maximum
Target Peak Temperature (T_p Target)	240°C Maximum 1 Time / 230°C Maximum 2 Times
Time within 5°C of actual peak (t_p)	10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time
Ramp-down Rate	5°C/second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1

Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum.

High Temperature Manual Soldering

260°C Maximum for 5 seconds Maximum, 2 times Maximum.