TOSHIBA Photocoupler GaAs IRED & Photo-Transistor

TLP627, TLP627-2, TLP627-4

Programmable Controllers
DC-output Module
Telecommunication

The TOSHIBA TLP627,-2 and -4 consist of a gallium arsenide infrared emitting diode optically coupled to a Darlington connected phototransistor which has an integral base-emitter resistor to optimize switching speed and elevated temperature characteristics.

The TLP627-2 offers two isolated channels in eight lead plastic DIP, while the TLP627-4 provide four isolated channels per package.

Collector-Emitter Voltage : 300 V (min)
 Current Transfer Ratio : 1000 % (min)
 UL recognized : UL1577, File No. E67349

cUL recognized : CSA Component Acceptance Service No. 5A

File No.E67349

SEMKO approved :9808210/01-03

Pin Configuration (top view)

EN60065, EN60950

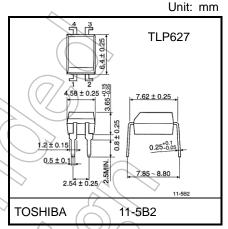
(approved TLP627 and TLP627-2)

Option (D4) type

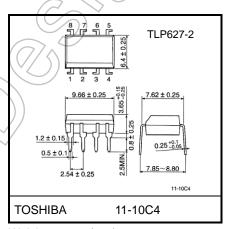
VDE approved : EN60747-5-5

Maximum operating insulation voltage: 890VPK Highest permissible over voltage: 8000VPK

Note: When an EN 60747-5-5 approved type is needed, please designate the "Option(D4)".



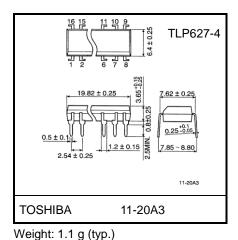
Weight: 0.26 g (typ.)



Weight: 0.54 g (typ.)

TLP627 TLP627-2 TLP627-4 TLP627-4

1,3,5,7 : ANODE 2,4,6,8 : CATHODE 9,11,13,15 : EMITTER 10,12,14,16 : COLLECTOR



Start of commercial production 1984-08



Absolute Maximum Ratings (Ta=25°C)

Characteristics			Ratin			
		Symbol	TLP627	TLP627-2 TLP627-4	Unit	
	Forward Current		lF	60 50		mA
	Forward Current Derating		ΔI _F /°C	-0.7(Ta≥39°C)	-0.5(Ta≥25°C)	mA /°C
Ω	Pulse Forward Current		IFP	1 (100µs puls	Α	
LED	Reverse Voltage		V _R	(5	V	
	Diode Power Dissipation	(1 Circuit)	PD	100	70	mW
	Diode Power Dissipation Derating	(1 Circuit)	ΔP _D /°C	-1.2 (Ta≥39°C)	-0.7 (Ta≥25°C)	mW /°C
	Collector-Emitter Voltage Emitter -Collector Voltage		VCEO	300	V	
ō			VECO	0.3	V	
Detector	Collector Current		Ic	150		mA
ă	Collector Power Dissipation	(1 Circuit)	P _C	150(300(Note 1))	100	mW
	Collector Power Dissipation Derating	(Ta≥25°C,1 Circuit)	ΔPc/°C	-1.5(-3.5(Note 1))	-1.0	mW /°C
Оре	erating Temperature Range		Topr	♦ -55 to	100	°C
Sto	rage Temperature Range		T _{stg}	-55 to	125	°C
Lead Soldering Temperature		T _{sol}	260(10s)		°C	
Total Package Power Dissipation (1 Circuit)		PT	250(320(Note 1))	150	mW	
Tota	Total Package Power Dissipation Derating (Ta≥25°C,1 Circuit)		∆ P _T /°C	-2.5(-3.2(Note 1))	-1.5	mW /°C
Isola	ation Voltage (AC,60 s, R.H. ≤ 60	%) (Note 2)	BVs	5000)	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: I_F=20mA Max

Note 2: Device considered a two terminal device : LED side pins Shorted together and DETECTOR side pins shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply Voltage	Vcc	_	_	200	٧
Forward Current	(NE)	_	16	25	mA
Collector Current	Ţ	_	_	120	mA
Operating Temperature	Topr	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.



Electrical Characteristics (Ta=25°C)

	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward Voltage	VF	VF		1.15	1.3	V
E	Reverse Current	IR	I _R V _R = 5 V		_	10	μΑ
	Capacitance	Ст	V = 0 V, f=1MHz	_ <	30	_	pF
	Collector-Emitter Breakdown Voltage	V _(BR) CEO	I _C = 0.1mA	300		\ <u>\</u>	V
tor	Emitter-Collector Breakdown Voltage	V(BR)ECO	I _E = 0.1mA	0.3	$\geq / ($) —	٧
Detector	Collector Dark Current	loso	VCE = 200V	/ [V])10	200	nA
	Collector Dark Current	ICEO	VCE = 200V, Ta = 85°C			20	μΑ
	Capacitance Collector to Emitter	CCE	V=0 V, f=1MHz		10		pF

Coupled Electrical Characteristics (Ta=25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Current Transfer Ratio	I _C /I _F	I _F =1mA, V _{CE} =1V	1000	4000	_	%
Saturated CTR	I _C /I _F (sat)	I _F =10mA, V _{CE} =1V	500		_	%
Collector-Emitter	\/o=(oot)	I _C =10mA, I _F =1mA	(\vee)) —	1.0	V
Saturation Voltage	V _{CE} (sat)	I _C =100mA, I _F =10mA	0.3	1	1.2	V

Isolation Electrical Characteristics (Ta=25°C)

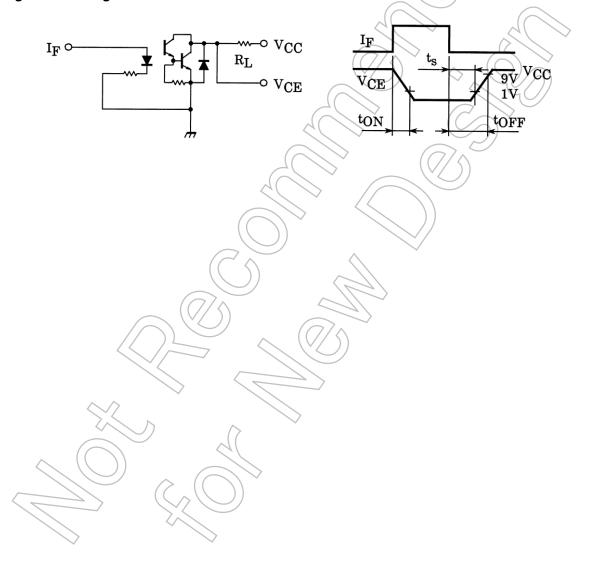
Characteristics	Sýmbol	Test Condition	Min	Тур.	Max	Unit
Capacitance Input to Output	Cs	V _S =0 V, f=1MHz	_	0.8	_	pF
Isolation Resistance	Rs	Vs=500V, R.H.≤60%	5×10 ¹⁰	10 ¹⁴	_	Ω
		AC, 60 s	5000	_		Vrms
Isolation Voltage	BVs	AC, 1s, in oil	_	10000	_	VIIIIS
	^	DC, 60 s, in oil	_	10000	_	Vdc

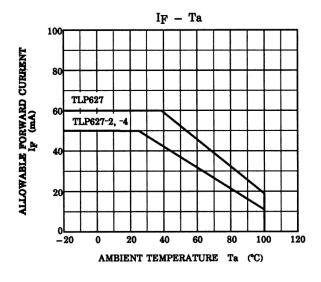


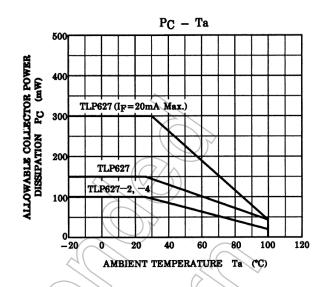
Switching Characteristics (Ta=25°C)

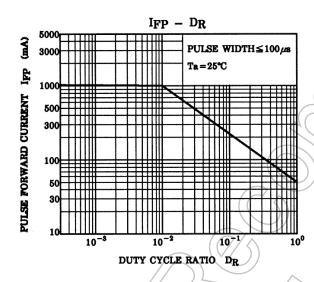
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise Time	tr		_	40	_	
Fall Time	tf	$V_{CC}=10V$ $I_{C}=10$ mA $R_{L}=100\Omega$	_	15	_	
Turn-on Time	t _{on}		_ <	50	_	
Turn-off Time	toff		_	15		μs
Turn-on Time	ton	R _L =180Ω (Fig.1) V _{CC} =10V, I _F =16mA	_	5	1)/_	
Strage Time	ts		10	40	_	
Turn-off Time	toff		//	80	_	

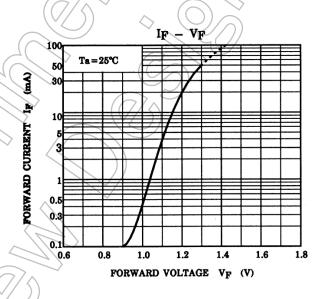
Fig.1 Switching Time Test Circuit

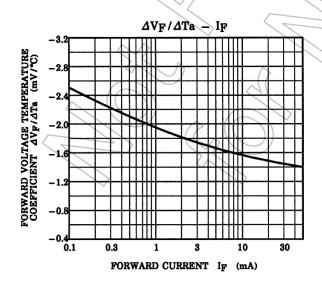


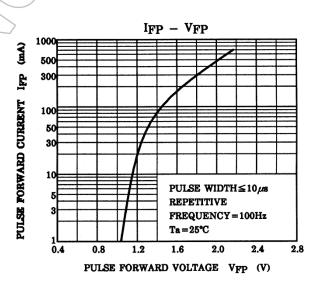


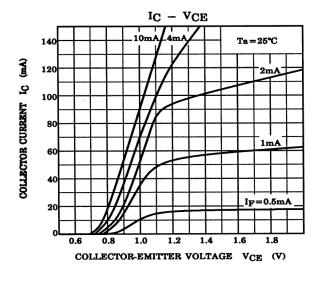


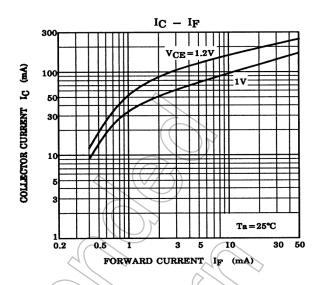


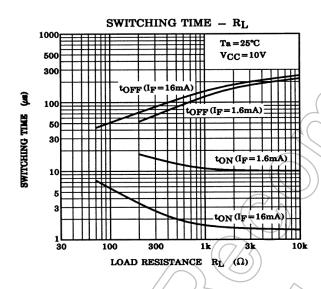


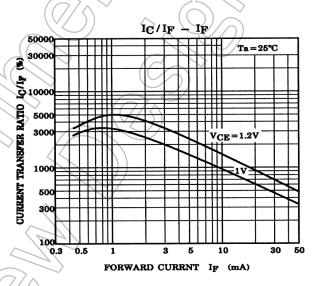


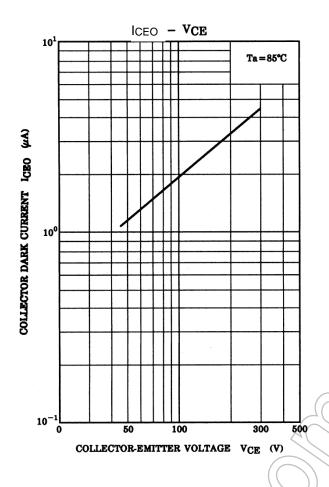


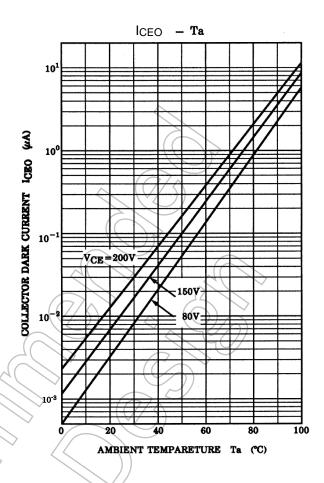


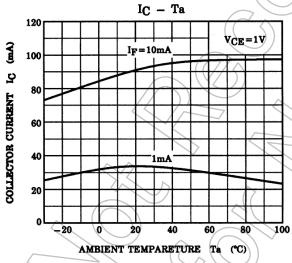


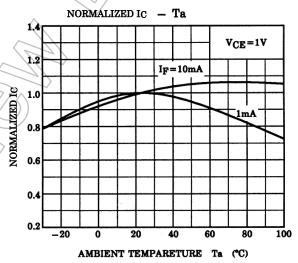












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