TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

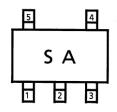
TA75S01F

Single Operational Amplifier

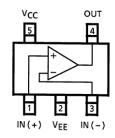
Features

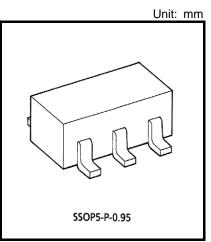
- In the linear mode the input common mode voltage range includes ground.
- The internally compensated Operational Amplifier is small package.
- Low power dissipation and power drain suitable for battery operation.
- Differential input voltage range equal to the power supply voltage.
- Large output voltage swing: 0VDC to 3.4VDC (VDC = 5V)
- Wide power supply voltage range and single power supply is possible.
- Single supply 3V_{DC} to 12V_{DC} or dual supplies ±1.5V_{DC} to ±6V_{DC}.

Marking (Top View)



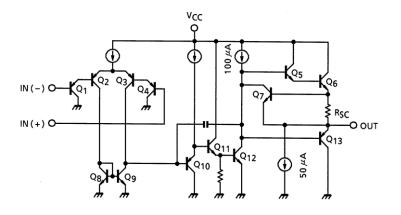
Pin Connection (Top View)





Weight:0.014g (typ.)

Equivalent Circuit



Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V_{CC}, V_{EE}	±6 or 12	V
Differential input voltage	DVIN	±12	V
Input voltage	VIN	-0.3 to V _{CC}	V
Power dissipation	PD	200	mW
Operating temperature	T _{opr}	-40 to 85	°C
Storage temperature	T _{stg}	-55 to 125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

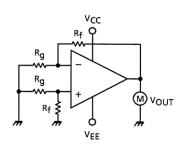
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Test Symbol **Test Condition** Unit Characteristic Min Тур. Max Circuit 1 Rg≤10kΩ 2 7 Input offset voltage Vio m٧ Input offset current 2 5 50 lio nA lj. 2 45 250 Input bias current nA _ _ CMVIN Common mode input voltage 3 0 Vcc-1.5 V Supply current Icc 4 ____ 0.4 0.8 mΑ _ Gv RL≥2kΩ 86 100 dB Voltage gain ____ _ Maximum output voltage swing V_{op-p} 5 $RL = 2k\Omega$ 0 _ 3.4 ٧ CMRR 3 dB Common mode rejection ratio 65 85 _ ____ SVRR Supply voltage rejection ratio ____ $Rg = 10k\Omega$ 65 100 dB _ IN (-) = 0V, IN (+) = 1V Source current 6 20 40 mΑ Isource ____ Isink 7 IN (-) = 1V, IN (+) = 0V Sink current 10 20 mΑ _ Unity gain cross frequency fT 0.3 MHz

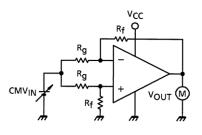
Electrical Characteristics (Vcc = 5V, VEE = GND, Ta = 25°C)

Test Circuit

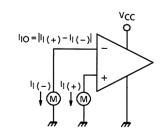
(1) Vio



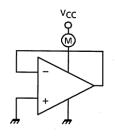
(3) CMVIN, CMRR



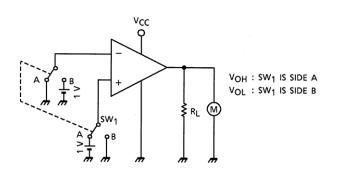
(2) II, IIO





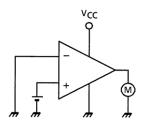


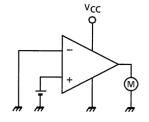
(5) Vop-p

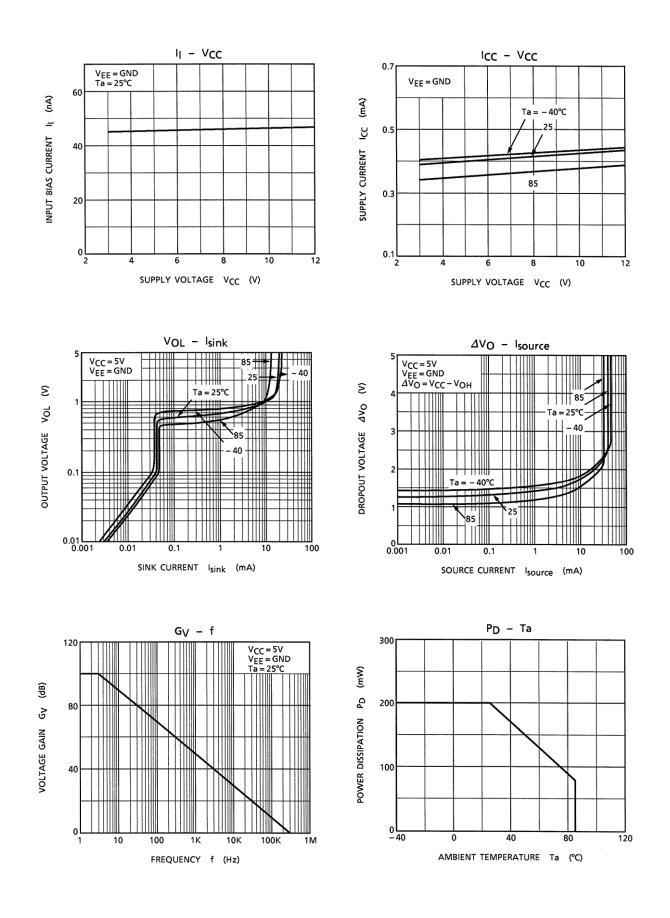


(6) Isource





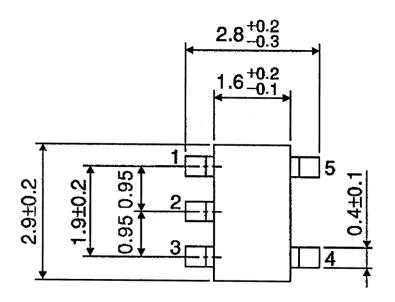


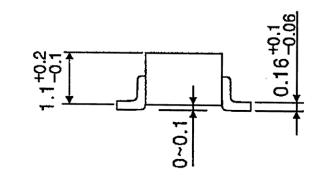


Package Dimensions

SSOP5-P-0.95

Unit: mm





Weight: 0.014g (typ.)

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