TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSVII)

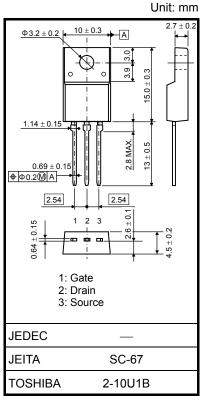
# TK9A45D

## **Switching Regulator Applications**

- Low drain-source ON-resistance: RDS (ON) = 0.63  $\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 4.8 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 450 \text{ V)}$
- Enhancement-mode:  $V_{th} = 2.0 \text{ to } 4.0 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$

#### Absolute Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	450	V
Gate-source voltage		$V_{GSS}$	±30	V
Drain current	DC (Note 1)	I <sub>D</sub>	9	Α
	Pulse (Note 1)	$I_{DP}$	36	
Drain power dissipation	on (Tc = 25°C)	P <sub>D</sub>	40	W
Single pulse avalanch	ne energy (Note 2)	E <sub>AS</sub>	170	mJ
Avalanche current		I <sub>AR</sub>	9	Α
Repetitive avalanche	energy (Note 3)	E <sub>AR</sub>	4.0	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature	range	T <sub>stg</sub>	-55 to 150	°C



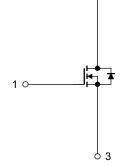
Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	3.125	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Internal Connection



Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 3.5 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 9 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.

Start of commercial production 2009-08

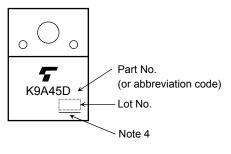
### **Electrical Characteristics (Ta = 25°C)**

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 450 V, V <sub>GS</sub> = 0 V	_	_	10	μА
Drain-source brea	rain-source breakdown voltage		I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	450	_	_	V
Gate threshold voltage		V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source ON resistance		R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4.5 A	_	0.63	0.77	Ω
Forward transfer admittance		Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 4.5 A	1.0	4.8	_	S
Input capacitance		C <sub>iss</sub>		_	800	_	pF
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	4	_	
Output capacitance		C <sub>oss</sub>			100	_	
Switching time	Rise time	t <sub>r</sub>	$\begin{array}{c c} 10 \text{ V} & \text{ID} = 4.5 \text{ A} & \text{Vout} \\ \hline V_{GS} & \text{O} & \text{V} & \text{RL} = 44 \Omega \end{array}$	_	20	_	ns
	Turn-on time	t <sub>on</sub>			40	_	
	Fall time	t <sub>f</sub>			12		
	Turn-off time	t <sub>off</sub>	Duty ≤ 1%, t <sub>W</sub> = 10 μs	_	60	_	
Total gate charge		Qg		_	16	_	
Gate-source charge		Qgs	$V_{DD} \approx 360 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 9\text{A}$	_	10	_	nC
Gate-drain charge		Q <sub>gd</sub>		_	6	_	

## **Source-Drain Ratings and Characteristics (Ta = 25°C)**

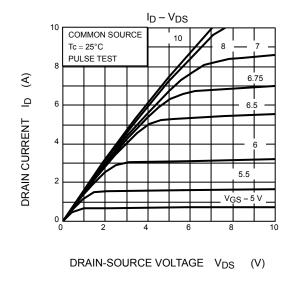
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current	(Note 1)	I <sub>DR</sub>	_	_	_	9	Α
Pulse drain reverse current	(Note 1)	I <sub>DRP</sub>	_	_	_	36	Α
Forward voltage (diode)		$V_{DSF}$	I <sub>DR</sub> = 9 A, V <sub>GS</sub> = 0 V	_	_	-1.7	٧
Reverse recovery time		t <sub>rr</sub>	I <sub>DR</sub> = 9 A, V <sub>GS</sub> = 0 V,	_	1200	_	ns
Reverse recovery charge		Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs	_	10	_	μС

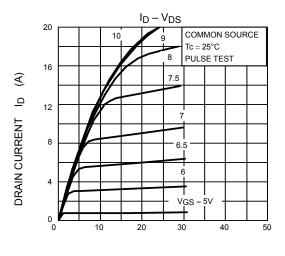
## Marking



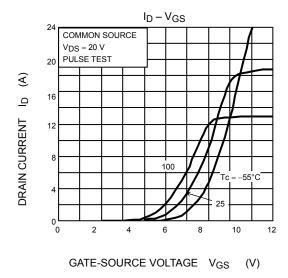
Note 4 : A line under a Lot No. identifies the indication of product Labels  $\hbox{[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]}$ 

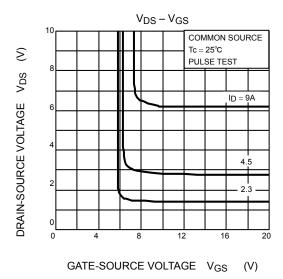
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

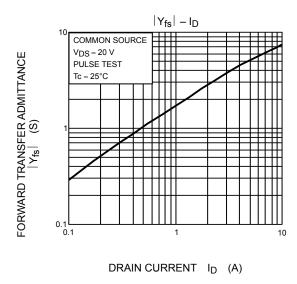


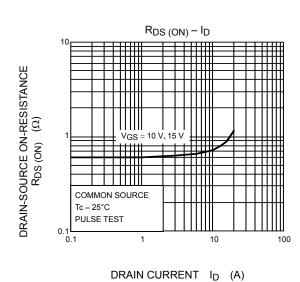


DRAIN-SOURCE VOLTAGE V<sub>DS</sub> (V)



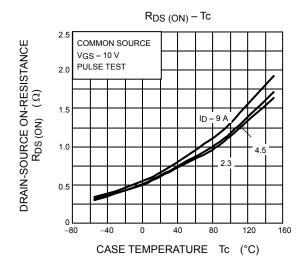


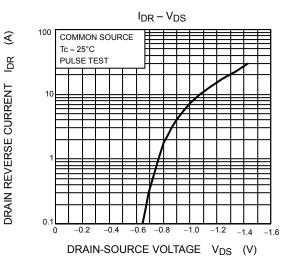


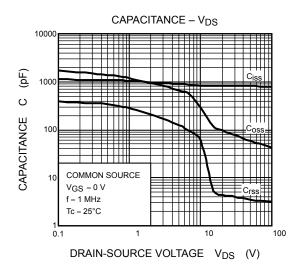


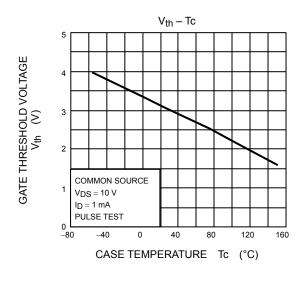
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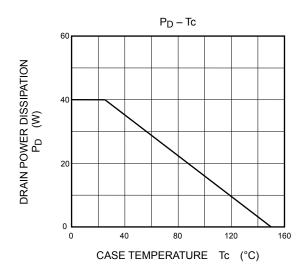
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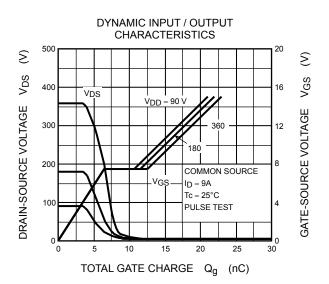


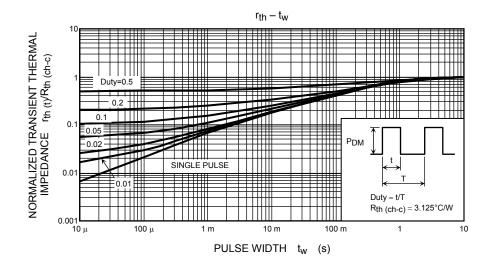


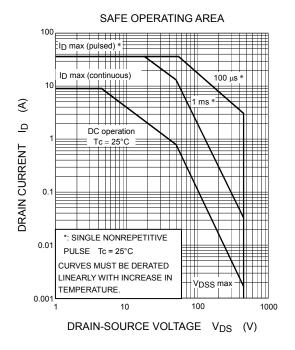


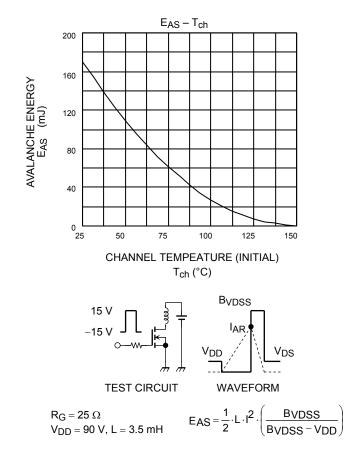












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