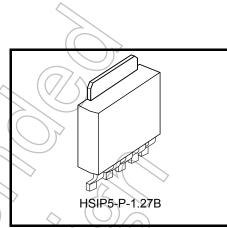
TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA4800AF

1 A Output Current Low Dropout Voltage Regulator

The TA4800AF consists of small-surface mount type low-dropout regulators with an output current of 1 A (maximum). The output voltage can be arbitrarily set by external resistance.



Weight : 0.36 g (typ.)

Features

• Maximum output current : 1 A

• Output voltage : V_{OUT} = 1.5 V to 9.0 V

Reference voltage accuracy : V_{REF} ± 2.5% (@Tj = 25°C)

• Low quiescent current : $850 \mu A \text{ (typ.)}$ (@ $V_{OUT} = 3.3 \text{ V}$, $I_{OUT} = 0 \text{ A}$)

• Low standby current (output OFF mode): 0.5 μA (typ.)

• Low-dropout voltage $V_D = 0.5 \text{ V (max)} (@V_{OUT} = 3.3 \text{ V}, I_{OUT} = 500 \text{ mA})$

Protection function : Overcurrent protection / overheating protection

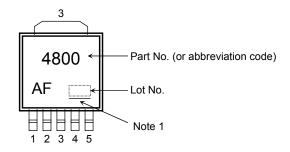
Package type : Surface-mount 5-pin New PW-Mold

Pin Assignment

Mark

(1) (2) (3) (4) (5) NC IN GND OUT ADJ

Marking



Note 1: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

The product(s) in this document ("Product") contain functions intended to protect the Product from temporary small overloads such as minor short-term overcurrent or overheating. The protective functions do not necessarily protect Product under all circumstances. When incorporating Product into your system, please design the system (1) to avoid such overloads upon the Product, and (2) to shut down or otherwise relieve the Product of such overload conditions immediately upon occurrence. For details, please refer to the notes appearing below in this document and other documents referenced in this document.

Pin Description

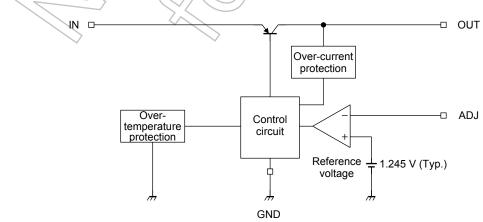
Pin No.	Symbol	Description
	,	
1	NC	Non-connection Non-connection
2	IN	Input terminal. Connected by capacitor (C _{IN}) to GND.
3	GND	Ground terminal
4	OUT	Output terminal. Connected by capacitor (COUT) to GND.
5	ADJ /	Output voltage feedback to regulator. It is connected to an error amplifier with VREF=1.245 V (typ.).

How to Order

Product No.	Package	Package Type and Capacity
TA4800AF (T6L1,Q)	5-pin New PW-Mold : Surface-mount	Tape (2000 pcs/reel)

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Absolute Maximum Rating (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Input voltage		V _{IN}	16	V	
Output current		Гоит	1	Α	
Operating junction	temperature	T _{jopr}	-40 to 150	°C	
Junction temperatu	ıre	Tj	150	°C	
Storage temperatu	re	T _{stg} –55 to 15		°C	
Power dissipation	Ta = 25°C	PD	1	w	
Power dissipation	Tc = 25°C	۲۵	10	VV	
Thermal	junction-ambient	R _{th(j-a)}	125	°C/W	
resistance	junction-case	R _{th(j-c)}	12.5	C/VV	

Note 2: Do not apply current and voltage (including reverse polarity) to any pin that is not specified.

Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Input Voltage Range

Characteristic	Symbol	Min	Тур	Max	Unit
Input voltage	V _{IN}	2.5 (Note 4)	<u></u>	16.0	٧

Note 4: This is the voltage at which the IC begins operating. V_D must be considered when determining the best input voltage for the application.

Output Voltage Range

Characteristic	Symbol	/>	Min	Тур.	Max	Unit
Output voltage	Vout		1.5	>	9.0	V

Protection Function (Reference)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Thermal shutdown	T _{SD}	V _{IN} = 4.3 V	150	170	_	°C	
Thermal shutdown hysteresis width	T _{SD(hys)}	V IN - 4.3 V	_	15	_	°C	
Peak circuit current	I _{PEAK}	$V_{IN} = 5.3 \text{ V}, T_j = 25^{\circ}\text{C}$	_	1.7	_	Α	
r can circuit current	PEAK	$V_{IN} = 8.3 \text{ V}, T_j = 25^{\circ}\text{C}$		2.0	_		
Short circuit current	loo	$V_{IN} = 5.3 \text{ V}, T_j = 25^{\circ}\text{C}$		1.1		Α	
Short diredit current	I _{SC}	$V_{IN} = 16V$, $T_j = 25^{\circ}C$	_	0.7	_	^	

Note 5: Ensure that the devices operate within the limits of the maximum rating when in actual use.

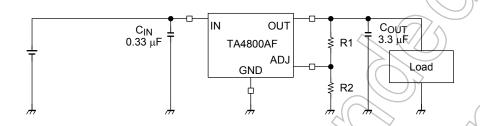
Electrical Characteristics (Unless otherwise specified, $V_{EN}=V_{IN},\,V_{OUT}=3.3$ V, $C_{IN}=0.33$ $\mu F,\,C_{OUT}=3.3$ $\mu F,\,T_j=25^{\circ}C)$

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Reference voltage	V _{REF}	V _{IN} = 4.3 V	1.214	1.245	1.276	V
Line regulation	Reg·line	$ \begin{array}{l} 4.3 \text{ V} \leq \text{V}_{IN} \leq 8.3 \text{ V}, \\ \text{I}_{OUT} = 500 \text{ mA} \end{array} $	1	8	24	mV
Load regulation	Reg·load	$V_{IN} = 4.3 \text{ V}, 5 \text{ mA} \le I_{OUT} \le 1 \text{ A}$		5	20	mV
Quiescent current	I_	$ \begin{array}{l} 4.3 \text{ V} \leq \text{V}_{IN} \leq 8.3 \text{ V}, \\ \text{I}_{OUT} = 0 \text{ A} \end{array} $	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	0.85	1.70	mA
Quiescent current	I _B	$4.3 \text{ V} \le \text{V}_{\text{IN}} \le 8.3 \text{ V},$ $\text{I}_{\text{OUT}} = 1 \text{ A}$	9)	10	20	IIIA
Starting quiescent current	la	V _{IN} = 2.1 V, I _{OUT} = 0 A	> —	3.3	4.0	mA
Starting quiescent current	l _{Bstart}	V _{IN} = 3.5 V, I _{OUT} = 1 A	_	17.0	28.5	ША
Output noise voltage	V _{NO}	$V_{IN} = 5.3 \text{ V}, I_{OUT} = 50 \text{ mA}, \\ 10 \text{ Hz} \le \text{f} \le 100 \text{ kHz}$	_	100	\rightarrow	μV_{rms}
Ripple rejection	R.R.	V _{IN} = 5.3 V, I _{OUT} = 50 mA, f = 120 Hz	-((63	<u> </u>	dB
Dranaut voltage	V _D	I _{OUT} = 500 mA	4	0.32	0.50	V
Dropout voltage		I _{OUT} = 1 A	7	0.69	_	V
Average temperature coefficient of output voltage	T _{CVO}	$V_{IN} = 5.3 \text{ V, } I_{OUT} = 5 \text{ mA},$ $0^{\circ}\text{C} \le T_{j} \le 125^{\circ}\text{C}$	2	0.3	_	mV/°C

Electrical Characteristics Common to All Products

• $T_j = 25^{\circ}\mathrm{C}$ in the measurement conditions of each item is the standard condition when a pulse test is carried out, and any drift in the electrical characteristic due to a rise in the junction temperature of the chip may be disregarded.

Standard Application Circuit



• Be sure to connect a capacitor near the input terminal and output terminal between both terminals and GND. The use of a monolithic ceramic capacitor (B Characteristic or X7R) of low ESR (equivalent series resistance) is recommended. The IC may oscillate due to external conditions (output current, temperature, or the type of the capacitor used). The type of capacitor required must be determined by the actual application circuit in which the IC is used.

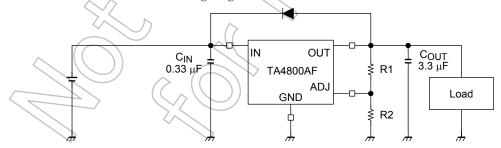
Setting Output Voltage

• The output voltage is determined by the equation shown below. When you control the output voltage with R1, a recommended value to use for R2 is $5 \text{ k}\Omega$. R1 and R2must be placed as close as possible to each other, and the board trace to the ADJ terminal must be kept as short as possible.

$$V_{\text{OUT}} = V_{\text{REF}} \times \left(1 + \frac{R1}{R2} \right)$$

Usage Precautions

• The IC might be destroyed if a voltage greater than the input terminal voltage is applied to the output terminal, or if the input terminal is connected to GND during operation. To prevent such an occurrence, connect a diode as in the following diagram.



- There is a possibility that internal parasitic devices may be generated when momentary transients cause a terminal's potential to fall below that of the GND terminal. In such case, that the device could be destroyed. The voltage of each terminal and any state must therefore never fall below the GND potential.
- Low voltage

Do not apply voltage to the Product that is lower than the minimum operating voltage, or the Product's protective functions will not operate properly and the Product may be permanently damaged.

• Overcurrent Protection

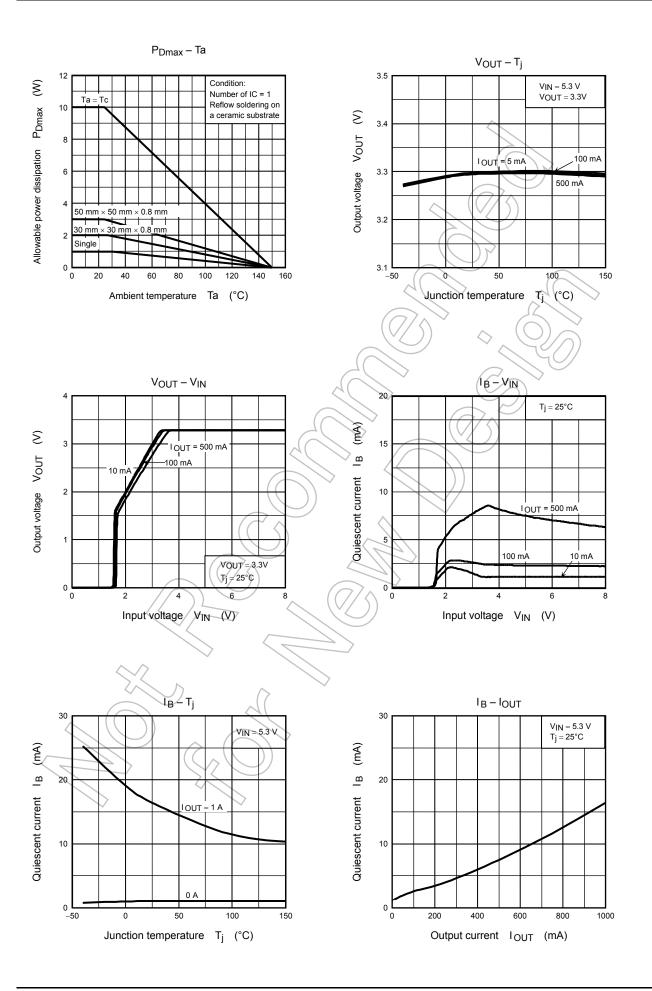
The overcurrent protection circuits in the Product are designed to temporarily protect Product from minor overcurrent of brief duration. When the overcurrent protective function in the Product activates, immediately cease application of overcurrent to Product. Improper usage of Product, such as application of current to Product exceeding the absolute maximum ratings, could cause the overcurrent protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

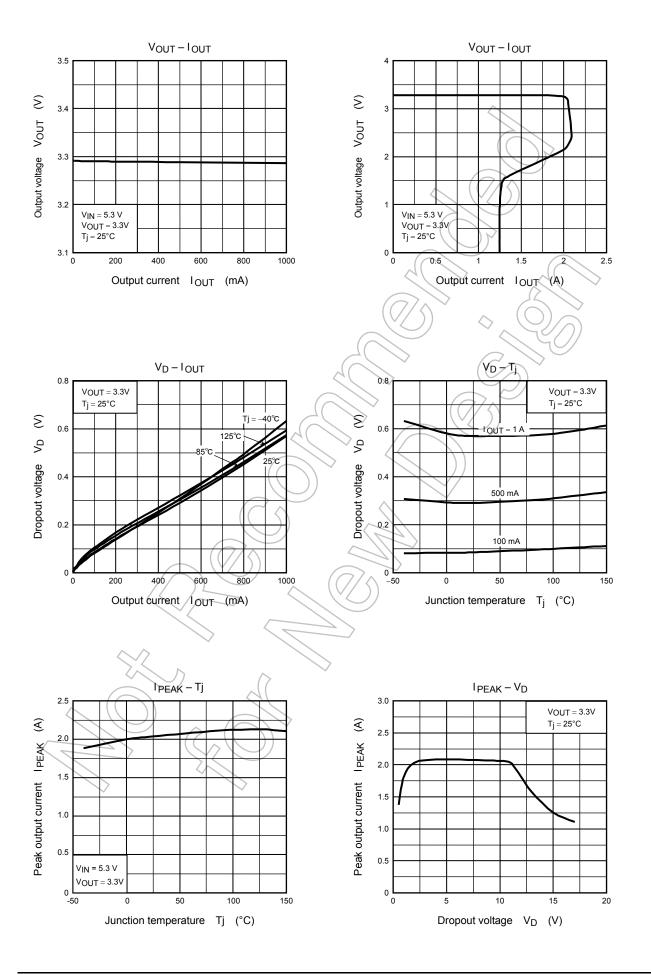
• Overheating Protection

The thermal shutdown circuits in the Product are designed to temporarily protect Product from minor overheating of brief duration. When the overheating protective function in the Product activates, immediately correct the overheating situation. Improper usage of Product, such as the application of heat to Product exceeding the absolute maximum ratings, could cause the overheating protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.



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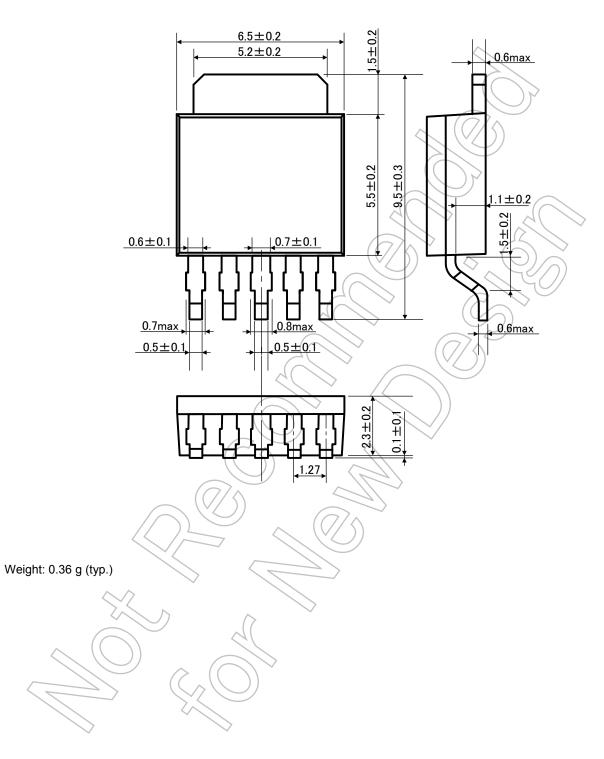






Package Dimensions

HSIP5-P-1.27B Unit: mm



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