



MMIC VCO w/ HALF FREQUENCY OUTPUT & DIVIDE-BY-16, 23.8 - 26.8 GHz

Typical Applications

The HMC739LP4(E) is ideal for:

- Point-to-Point Radios
- Point-to-Multi-Point Radios / LMDS
- VSAT

Features

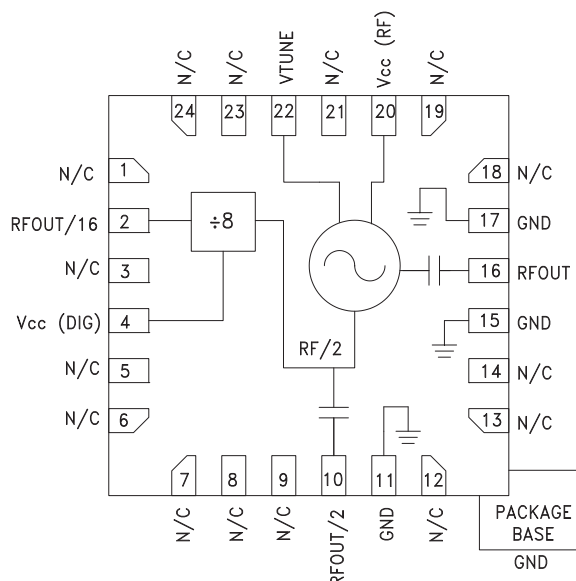
Pout: +8 dBm

Phase Noise: -93 dBc/Hz @ 100 kHz Typ.

No External Resonator Needed

24 Lead 4x4mm SMT Package: 16mm²

Functional Diagram



General Description

The HMC739LP4(E) is a GaAs InGaP Heterojunction Bipolar Transistor (HBT) MMIC VCO. The HMC739LP4(E) integrates a resonator, negative resistance device, varactor diode and divide-by-16 prescaler. The VCO's phase noise performance is excellent over temperature, shock, and process due to the oscillator's monolithic structure. Power output is +8 dBm typical from a 5V supply voltage. The voltage controlled oscillator is packaged in a low cost leadless QFN 4x4 mm surface mount package

Electrical Specifications, $T_A = +25^\circ\text{C}$, $V_{cc}(\text{RF})$, $V_{cc}(\text{DIG}) = +5\text{V}$

Parameter	Min.	Typ.	Max.	Units
Frequency Range	23.8 - 26.8			GHz
Power Output	3		14	dBm
RF OUT	-3		5	dBm
RF OUT/2	-7		-1	dBm
SSB Phase Noise @ 100 kHz Offset, $V_{tune} = +5\text{V}$ @ RF Output		-93		dBc/Hz
Tune Voltage	1		13	V
Supply Current	160	200	220	mA
Tune Port Leakage Current ($V_{tune} = 13\text{V}$)			10	μA
Output Return Loss		3		dB
Harmonics/Subharmonics	1/2	-20		dBc
	3/2	-30		dBc
Pulling (into a 2.0:1 VSWR)		30		MHz pp
Pushing @ $V_{tune} = 5\text{V}$		-65		MHz/V
Frequency Drift Rate		4		MHz/ $^\circ\text{C}$

HMC739* PRODUCT PAGE QUICK LINKS

Last Content Update: 11/29/2017

COMPARABLE PARTS

View a parametric search of comparable parts.

EVALUATION KITS

- HMC739LP4 Evaluation Board

DOCUMENTATION

Data Sheet

- HMC739 Data Sheet

REFERENCE MATERIALS

Product Selection Guide

- RF, Microwave, and Millimeter Wave IC Selection Guide 2017

Quality Documentation

- Package/Assembly Qualification Test Report: LP4, LP4B, LP4C, LP4K (QTR: 2013-00487 REV: 04)
- Package/Assembly Qualification Test Report: Plastic Encapsulated QFN (QTR: 05006 REV: 02)
- Semiconductor Qualification Test Report: GaAs HBT-A (QTR: 2013-00228)

DESIGN RESOURCES

- HMC739 Material Declaration
- PCN-PDN Information
- Quality And Reliability
- Symbols and Footprints

DISCUSSIONS

View all HMC739 EngineerZone Discussions.

SAMPLE AND BUY

Visit the product page to see pricing options.

TECHNICAL SUPPORT

Submit a technical question or find your regional support number.

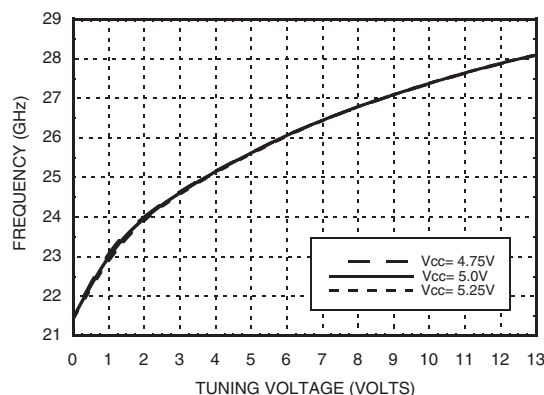
DOCUMENT FEEDBACK

Submit feedback for this data sheet.

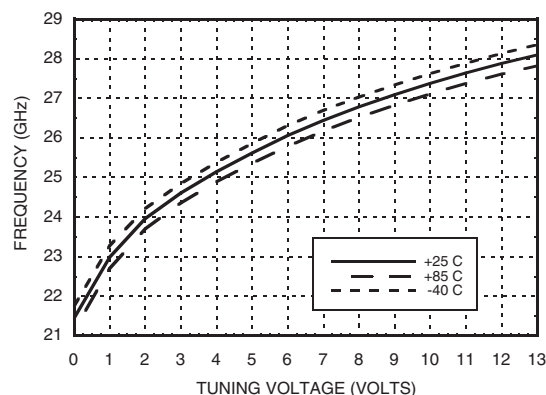


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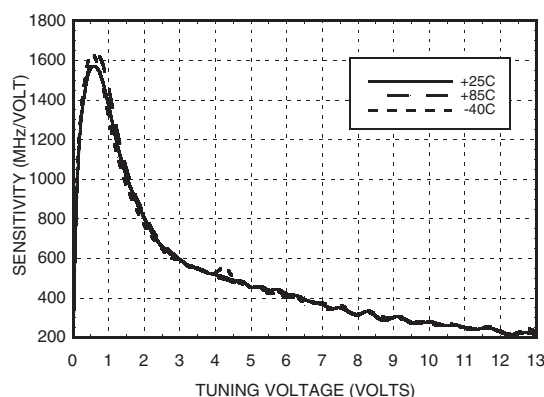
Frequency vs. Tuning Voltage, $T = 25^{\circ}\text{C}$



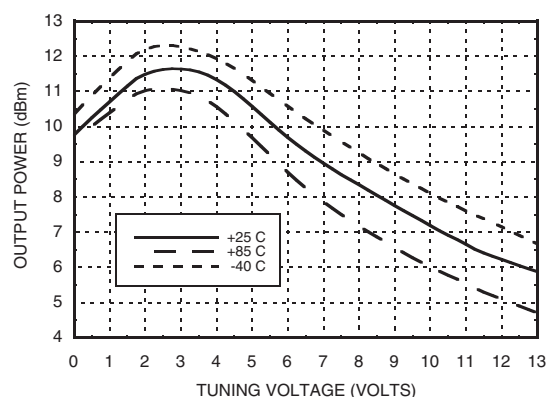
Frequency vs. Tuning Voltage, $V_{CC} = +5\text{V}$



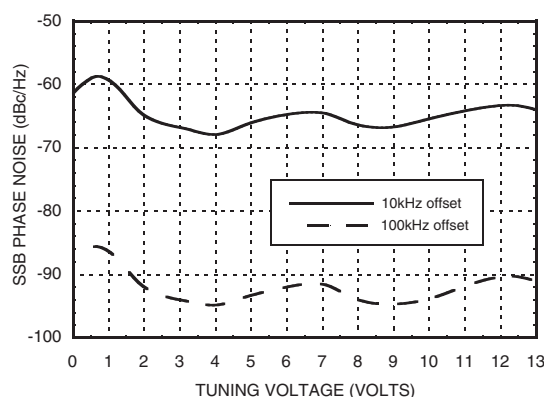
Sensitivity vs. Tuning Voltage, $V_{CC} = +5\text{V}$



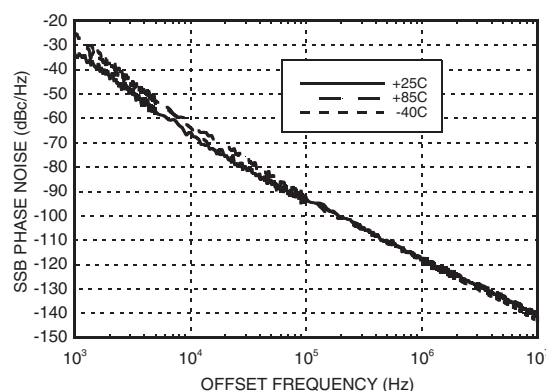
Output Power
vs. Tuning Voltage, $V_{CC} = +5\text{V}$



SSB Phase Noise vs. Tuning Voltage



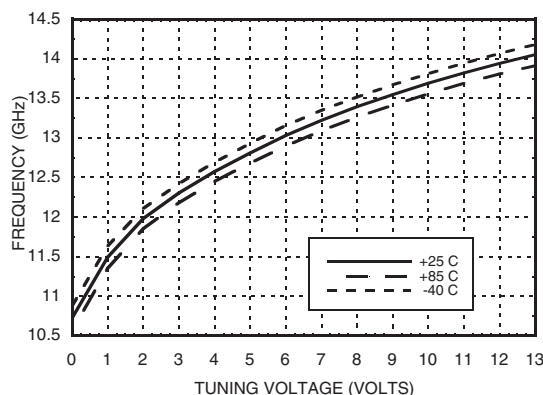
SSB Phase Noise @ $V_{\text{tune}} = 5\text{V}$



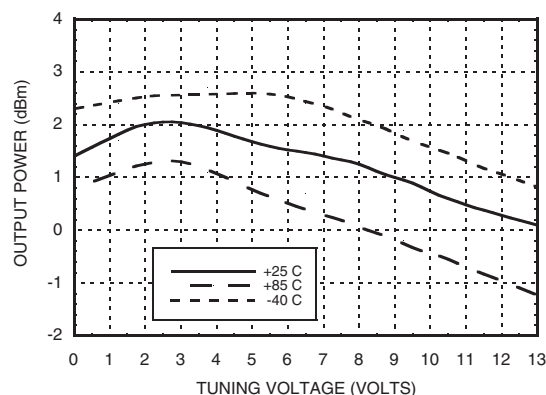


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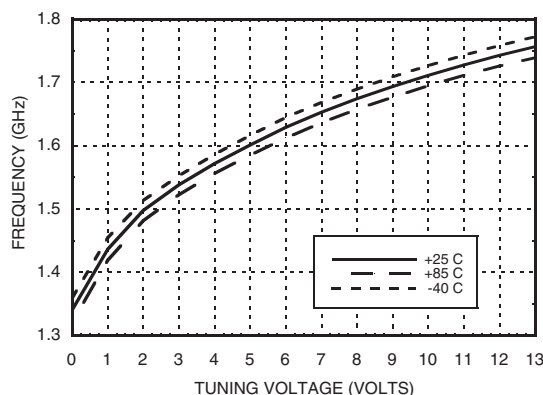
**RFOUT/2 Frequency
vs. Tuning Voltage, Vcc= +5V**



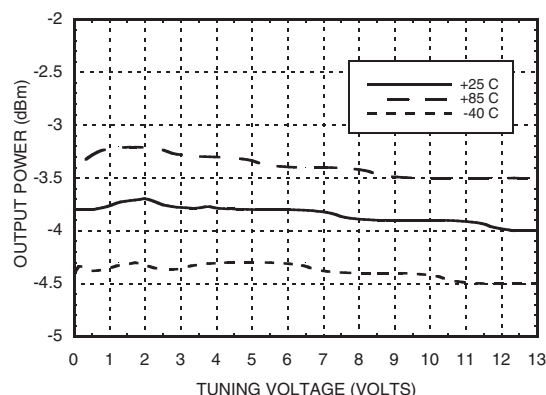
**RFOUT/2 Output Power
vs. Tuning Voltage, Vcc= +5V**



**Divide-by-16 Frequency
vs. Tuning Voltage, Vcc = +5V**



**Divide-by-16 Output Power
vs. Tuning Voltage, Vcc = +5V**



Absolute Maximum Ratings

Vcc (RF), Vcc (DIG)	+5.5V
Vtune	0 to +15V
Junction Temperature	135° C
Continuous P _{diss} (T= 85 °C) (derate 23.3 mW/° above 85 °C)	1.2 W
Thermal Resistance (junction to ground paddle)	43 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C

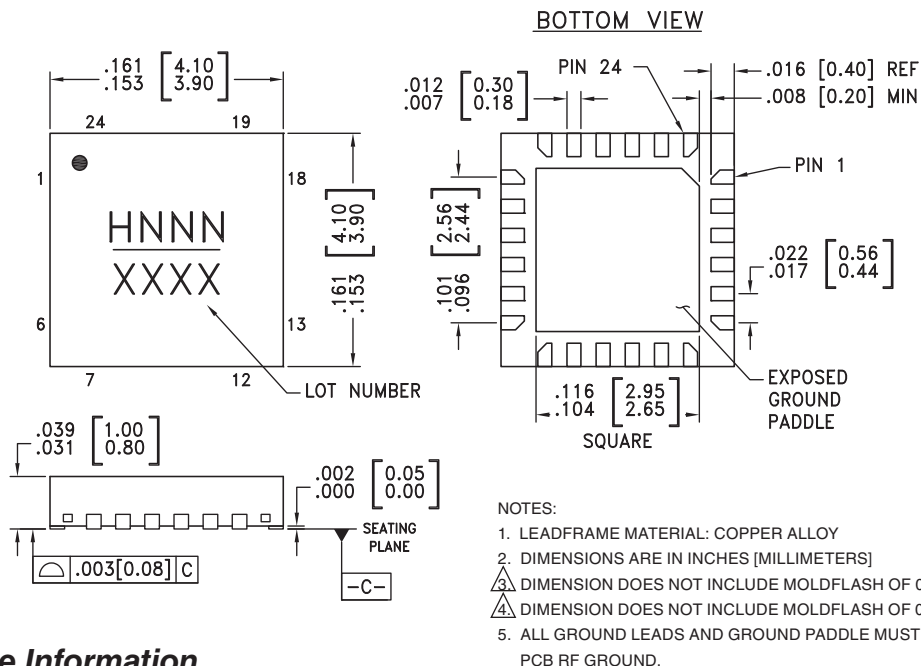
Typical Supply Current vs. Vcc

Vcc(RF), Vcc DIG) (V)	I _{cc} (mA)
4.75	172
5.0	192
5.25	212

Note: VCO will operate over full voltage range shown above.



**ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS**


**MMIC VCO w/ HALF FREQUENCY OUTPUT
& DIVIDE-BY-16, 23.8 - 26.8 GHz**
Outline Drawing

Package Information

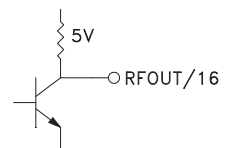
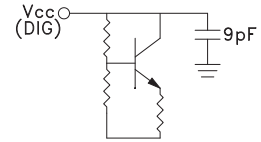
Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[3]
HMC739LP4	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 ^[1]	H739 XXXX
HMC739LP4E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	H739 XXXX

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

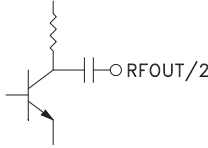

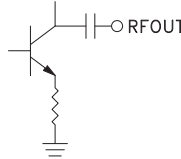
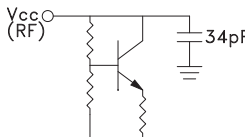
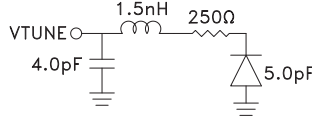
Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 3, 5, 6, 7, 8, 9, 12, 13, 14, 18, 19, 21, 23, 24	N/C	No Connection required. These pins may be connected to RF/DC ground without affecting performance.	
2	RFOUT/16	RF/16 Divided Output. Requires DC Block.	
4	Vcc (DIG)	Supply voltage for prescaler. Can be omitted if prescaler is not needed to conserve approximately 100 mA	

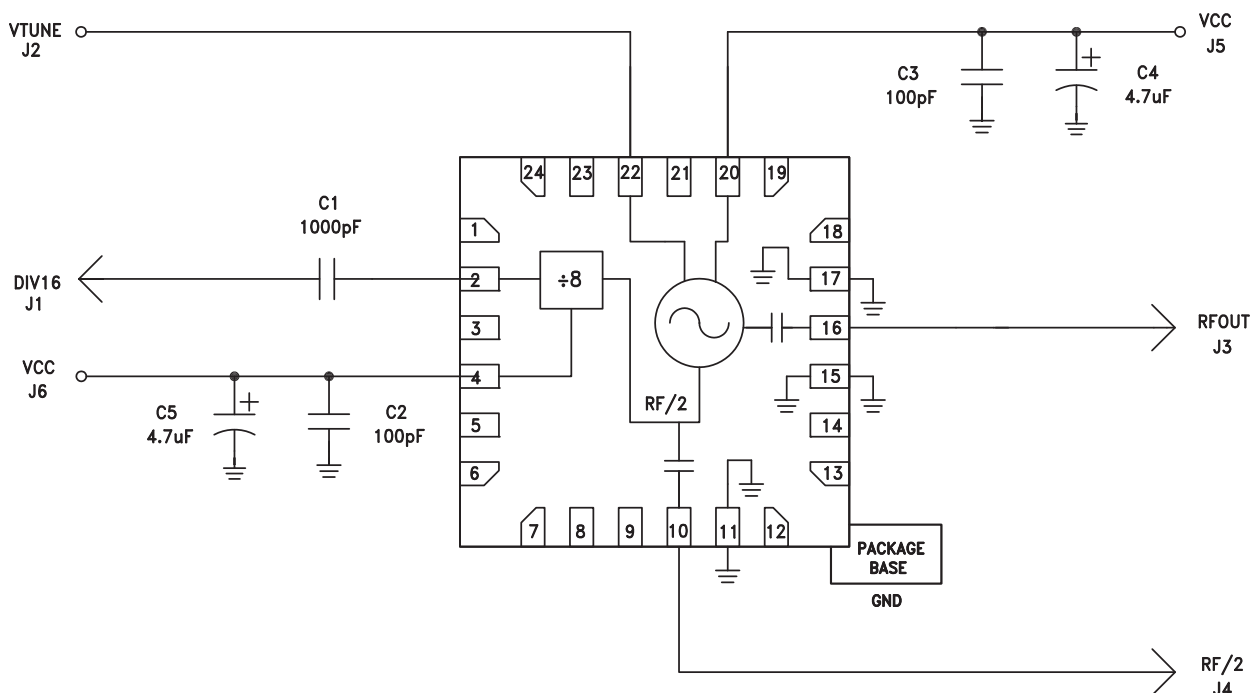


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Pin Descriptions (Continued)

Pin Number	Function	Description	Interface Schematic
10	RFOUT/2	Half frequency output (AC coupled)	
11, 15, 17	GND	Package bottom has an exposed metal paddle that must be RF & DC grounded.	
16	RFOUT	RF output (AC coupled).	
20	Vcc (RF)	Supply Voltage	
22	VTUNE	Control Voltage Input. Modulation port bandwidth dependent on drive source impedance.	

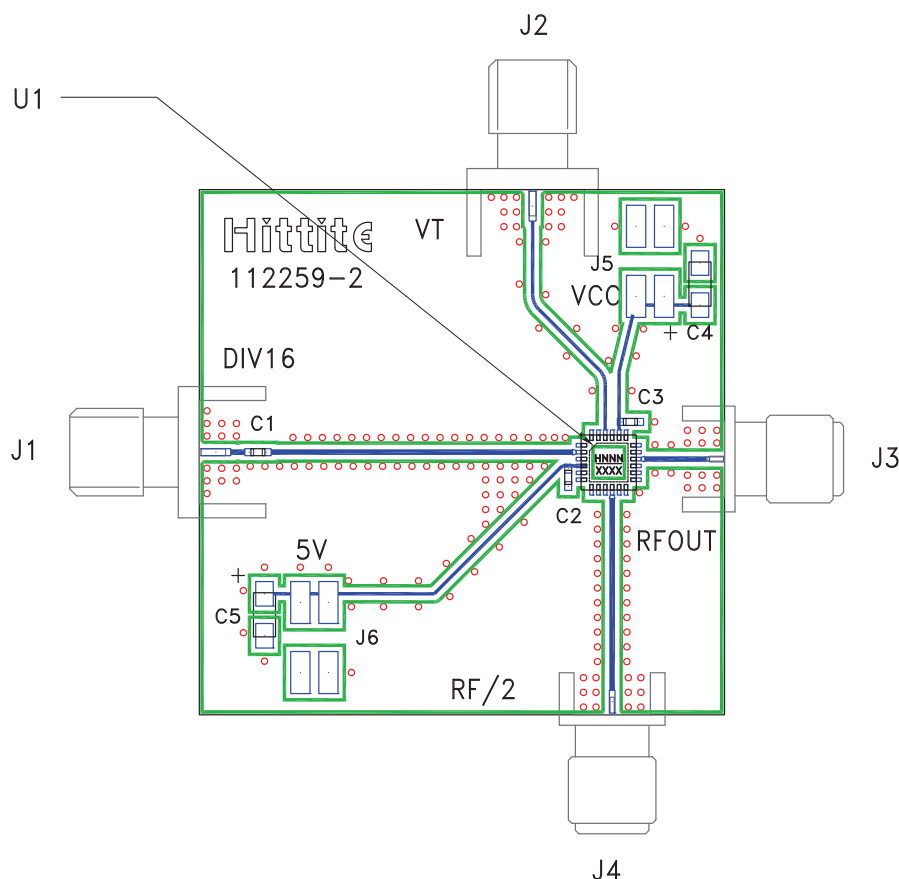
Typical Application Circuit





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Evaluation PCB



List of Materials for Evaluation PCB 112261 ^[1]

Item	Description
J1 - J2	PCB Mount SMA Connector
J3	PCB Mount K-Connector
J4	PCB Mount SRI SMA Connector
J5, J6	2mm SMT 8 Pin Molex Header
C1	1000 pF, 0402 Pkg.
C2, C3	100 pF, 0402 Pkg.
C4, C5	4.7 μF Tantalum Capacitors Case A
U1	HMC739LP4(E) VCO
PCB [2]	112259 Eval Board

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and backside ground slug should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.