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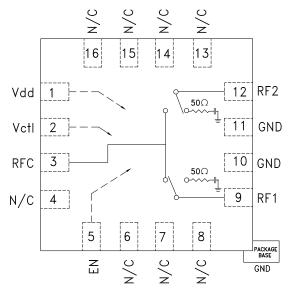
# HMC849ALP4CE

#### Typical Applications

The HMC849ALP4CE is ideal for:

- Cellular/4G Infrastructure
- WiMAX, WiBro & Fixed Wireless
- Automotive Telematics
- Mobile Radio
- Test Equipment

#### Functional Diagram



#### HIGH ISOLATION SPDT NON-REFLECTIVE SWITCH, DC - 6 GHz

#### Features

High Isolation: up to 60 dB Single Positive Control: 0/+3V to +5V High Input IP3: +52 dBm Non-Reflective Design "All Off" State 16 Lead 4x4 mm QFN Package: 16 mm<sup>2</sup>

#### **General Description**

The HMC849ALP4CE is a high isolation non-reflective DC to 6 GHz GaAs pHEMT SPDT switch in a low cost leadless surface mount package. The switch is ideal for cellular/WiMAX/4G Infrastructure applications yielding up to 60 dB isolation, low 0.8 dB insertion loss and +52 dBm input IP3. Power handling is excellent up through the 5 - 6 GHz WiMAX band with the switch offering a P1dB compression point of +31 dBm. On-chip circuitry allows a single positive voltage control of 0/+3V or 0/+5V at very low DC currents. An enable input (EN) set to logic high will put the switch in an "all off" state.

#### Electrical Specifications, $T_{A} = +25$ °C, VctI = 0/Vdd, Vdd = +3V to +5V, 50 Ohm System

Parameter	Frequency	Min.	Тур.	Max.	Units
Insertion Loss	DC - 2.0 GHz 2.0 - 4.0 GHz 4.0 - 6.0 GHz		0.9 1.0 1.8	1.3 1.5 2.5	dB dB
Isolation (RFC to RF1/RF2)	DC - 2.0 GHz 2.0 - 4.0 GHz 4.0 - 6.0 GHz	53 48 35	60 56 50		dB dB
Return Loss (On State)	DC - 4.0 GHz 4.0 - 6.0 GHz		17 14		dB dB
Return Loss (Off State)	DC - 6.0 GHz		15		dB
Input Power for 1 dB Compression +3V +5V	0.35 - 6.0 GHz	24 30	27 33		dBm dBm
Input Third Order Intercept (Two-Tone Input Power = +10 dBm Each Tone)	DC - 6.0 GHz		52		dBm
Switching Speed tRISE, tFALL (10/90% RF) tON, tOFF (50% CTL to 10/90% RF)	DC - 6.0 GHz		60 150	600	ns ns

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# HMC849A\* PRODUCT PAGE QUICK LINKS

Last Content Update: 12/18/2017

#### COMPARABLE PARTS

View a parametric search of comparable parts.

#### EVALUATION KITS

HMC849ALP4C Evaluation Board

#### **DOCUMENTATION**

#### Data Sheet

HMC849A Data Sheet

#### TOOLS AND SIMULATIONS $\square$

• HMC849A S-Parameters

#### REFERENCE MATERIALS

#### **Product Selection Guide**

RF, Microwave, and Millimeter Wave IC Selection Guide 2017

#### **Quality Documentation**

- HMC Legacy PDN: PCN130060
- Semiconductor Qualification Test Report: PHEMT-J (QTR: 2013-00285)

#### DESIGN RESOURCES

- HMC849A Material Declaration
- PCN-PDN Information
- Quality And Reliability
- Symbols and Footprints

#### DISCUSSIONS

View all HMC849A EngineerZone Discussions.

#### SAMPLE AND BUY

Visit the product page to see pricing options.

#### TECHNICAL SUPPORT

Submit a technical question or find your regional support number.

#### DOCUMENT FEEDBACK

Submit feedback for this data sheet.



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#### HIGH ISOLATION SPDT NON-REFLECTIVE SWITCH, DC - 6 GHz

Return Loss [1]

-10

-15

-20

-25

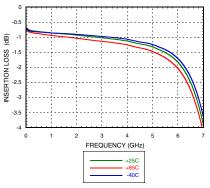
-30

-35

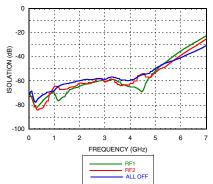
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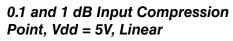
RETURN LOSS (dB)

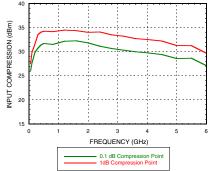
#### **Insertion Loss**



Isolation Between Ports RFC and RF1 / RF2







#### [1] RFC is reflective in "all off" state.

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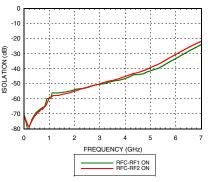
Isolation Between Ports RF1 and RF2

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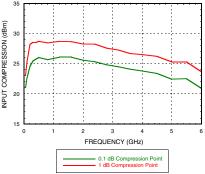
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FREQUENCY (GHz)

RF1, RF2 OFF RF1, RF2 ON



0.1 and 1 dB Input Compression Point, Vdd = 3V, Linear

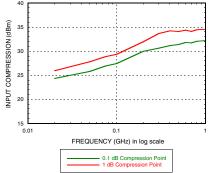




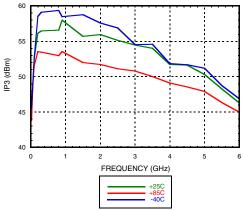
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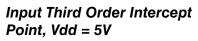


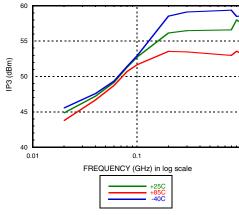




Input Third Order Intercept Point, Vdd = 5V, Linear

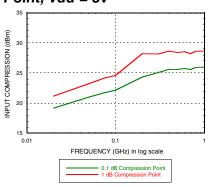


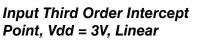


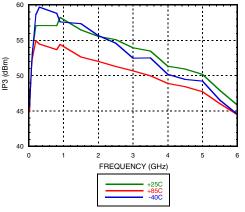


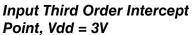
#### HIGH ISOLATION SPDT NON-REFLECTIVE SWITCH, DC - 6 GHz

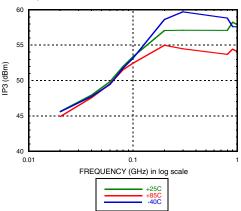
0.1 and 1 dB Input Compression Point, Vdd = 3V











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3





#### HIGH ISOLATION SPDT NON-REFLECTIVE SWITCH, DC - 6 GHz

#### Absolute Maximum Ratings

Bias Voltage (Vdd)	7V
Control Voltage (Vctl, EN)	-1V to Vdd +1V
RF Input Power *	
Through Path 3V/5V Termination Path 3V/5V	31 / 33 dBm 26.5 dBm
Channel Temperature	150 °C
Continuous Pdiss (T = 85 °C) (derate 14.9 mW/°C for through path, and 6.9 mW/°C for termination path above 85 °C) Through Path Termination Path	0.969 W 0.451 W
Thermal Resistance (channel to package bottom) Through Path Termination Path	67.1 °C/W 144.2°C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 1A

v00.1013

\* The RF input power is quite lower than the breakdown power levels. Hence, the only concern with this product is the thermal limit.



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

#### **Bias Voltage & Current**

Vdd (V)	ldd (Typ.) (mA)
3	1.2
5	1.3

#### **Digital Control Voltages**

State	Bias Condition	
Low	0 to +0.8 Vdc @ <1 µA Typical	
High	+2.0 to +5.0 Vdc @ 40 µA Typical	

#### Truth Table

Control Input		Signal Path State		
Vctl	EN	RFC - RF1	RFC - RF2	
Low	Low	OFF	ON	
High	Low	ON	OFF	
Low	High	OFF	OFF	
High	High	OFF	OFF	

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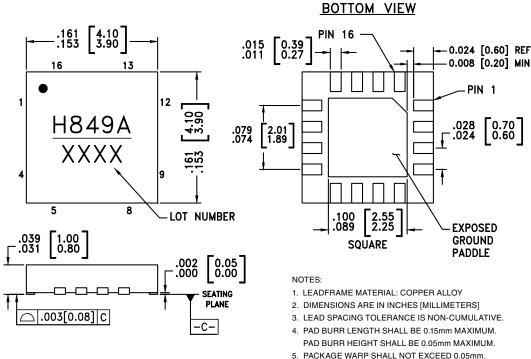


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#### HIGH ISOLATION SPDT NON-REFLECTIVE SWITCH, DC - 6 GHz

#### **Outline Drawing**



6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.

7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED LAND PATTERN.

#### Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking <sup>[1]</sup>
HMC849ALP4CE	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 <sup>[2]</sup>	<u>H849A</u> XXXX

[1] 4-Digit lot number XXXX

[2] Max peak reflow temperature of 260 °C

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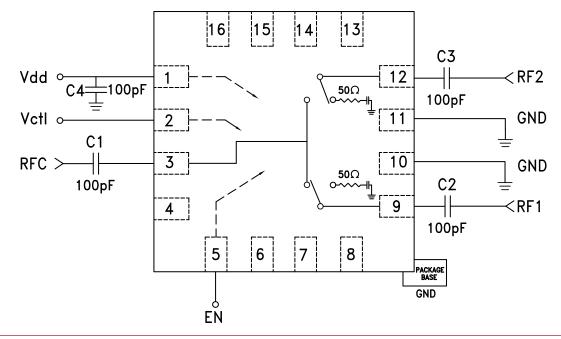


### HIGH ISOLATION SPDT NON-REFLECTIVE SWITCH, DC - 6 GHz

#### **Pin Descriptions**

Pin Number	Function	Description	Interface Schematic
1	Vdd	Supply Voltage.	
2	Vctl	Control input. See truth and control voltage tables.	
3, 9, 12	RFC, RF1, RF2	These pins are DC coupled and matched to 50 Ohms. Blocking capacitors are required.	
4, 6, 7, 8, 13, 14, 15, 16	N/C	The pins are not connected internally; however, all data shown herein was measured with these pins connected to RF/DC ground externally.	
5	EN	Enable. See truth and control voltage tables.	
10, 11	GND	Package bottom must also be connected to PCB RF ground.	

**Application Circuit** 



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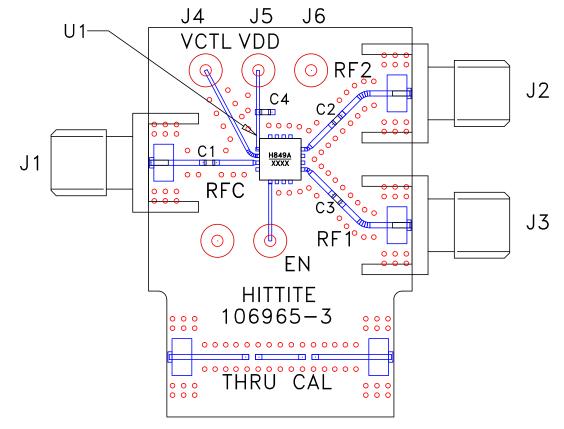


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#### HIGH ISOLATION SPDT NON-REFLECTIVE SWITCH, DC - 6 GHz

#### **Evaluation PCB**



#### List of Materials for Evaluation PCB EV1HMC849ALP4C [1]

Item	Description	
J1 - J3	PC Mount SMA RF Connector	
J4 - J8	DC Pin	
C1 - C4	100 pF Capacitor, 0402 Pkg.	
U1	HMC849ALP4CE SPDT Switch	
PCB [2]	106965 Evaluation PCB	

Reference this number when ordering complete evaluation PCB
Circuit Board Material: Rogers 4350 or Arlon 25FR

The circuit board used in the final application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 Ohm impedance and the package ground leads and backside ground slug should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Hittite Microwave Corporation upon request.

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Notes:

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