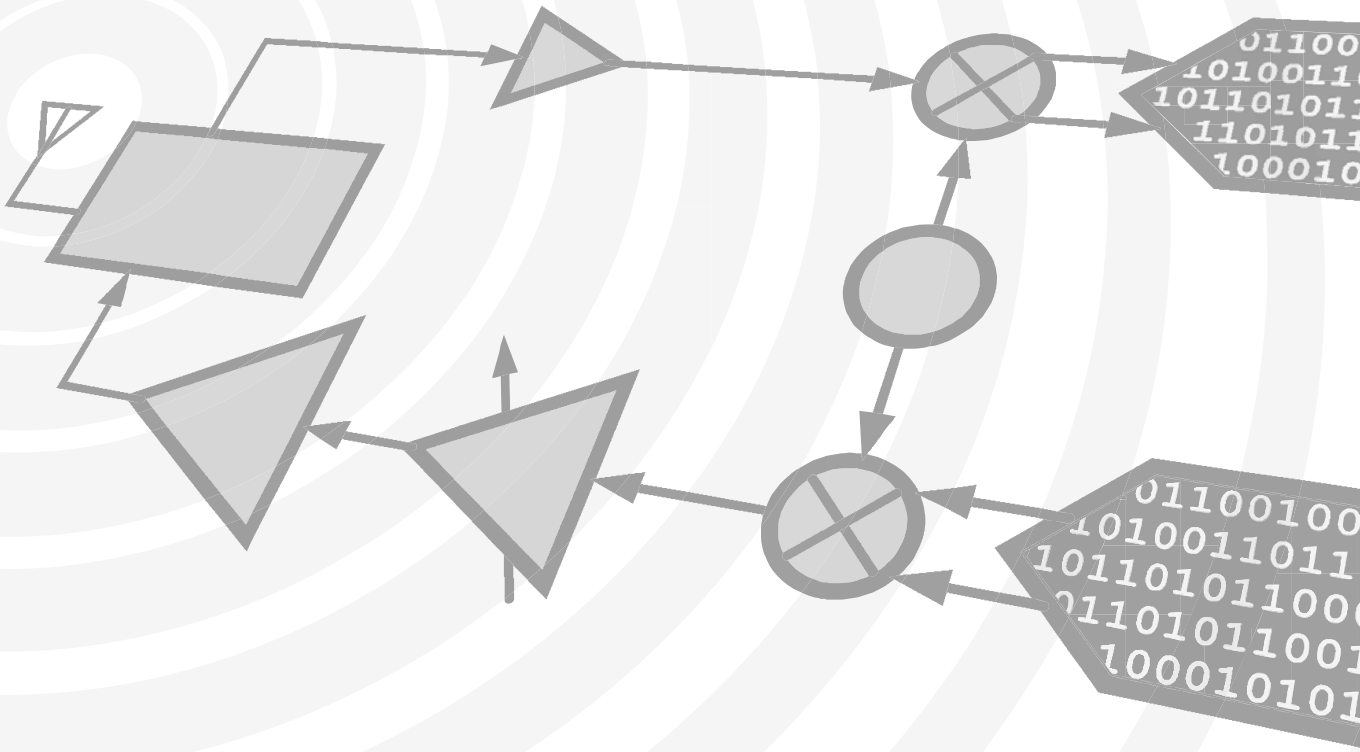
ANALOG DEVICES

Hittite MICROWAVE PRODUCTS FROM ANALOG DEVICES

Analog Devices Welcomes Hittite Microwave Corporation

NO CONTENT ON THE ATTACHED DOCUMENT HAS CHANGED

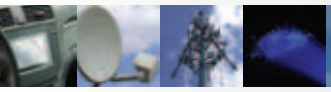


www.analog.com

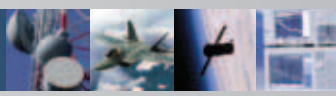
www.hittite.com

THIS PAGE INTENTIONALLY LEFT BLANK

Fractional-N PLL Chipset Evaluation Kit



Analog, Digital & Mixed-Signal ICs, Modules, Subsystems & Instrumentation



User Manual

Software & Hardware Installation

Hittite MICROWAVE CORPORATION

Order On-Line at: www.hittite.com
Receive the latest product releases - click on "My Subscription"

20 Alpha Road Chelmsford, MA 01824
Phone: 978-250-3343 • Fax: 978-250-3373 • sales@hittite.com

Part #
140-00055-00
Rev. A - v00.0811
ECN# CP111380

Table of Contents

1. Introduction

This document describes the functionality of the HMC983LP5E & HMC984LP4E Evaluation Kit, and provides high level instructions for using and configuring the Kit. The Kit includes PC compatible software with a Graphical User Interface (GUI) that allows the user to read from and write to all device registers and observe full functionality and performance of either the HMC983LP5E and/or the HMC984LP4E.

The HMC983LP5E & HMC984LP4E evaluation board included in the Kit is configured by default to form a complete frequency synthesizer. Hardware board changes are required to configure the HMC983LP5E & HMC984LP4E evaluation board to evaluate the HMC983LP5E (Fractional Divider), or the HMC984LP4E (Phase Detector) separately. Hardware configuration requirements are described in section 4.3.

Note: For most up-to-date software download and information please visit www.hittite.com.

2. Package Contents

2.1 Hardware

Verify that all the items listed in Table 1 are included in the shipment.

Item	Quantity
HMC983LP5E & HMC984LP4E Evaluation Board	1
USB Interface Board	1
6' USB A Male to USB B Male Cable	1
CD ROM (Contains User Manual and software)	1

Table 1: Packing List

2.2 Software

The HMC983LP5E & HMC984LP4E Evaluation Software enables users to communicate with and control HMC983LP5E & HMC984LP4E Evaluation Board with their PC, and observe full functionality and performance of the HMC983LP5E & HMC984LP4E.

3. Operating Environment

This evaluation kit is designed for use in a laboratory setting at ambient room temperature (25 °C) and is not protected against moisture. The USB Interface Board has an ESD rating of +/-3000 V, however the HMC983LP5E & HMC984LP4E may have a lower rating (check the product's data sheet for its specific ESD rating). Use appropriate ESD procedures and precautionary measures when handling all electronic hardware.

4. Setup and Installation

4.1 User Provided Equipment

In addition to the items provided in the evaluation kit, the user must provide the following equipment to evaluate the HMC983LP5E & HMC984LP4E under test.

- DC Power Supply
- DC Cables
- Spectrum Analyzer
- Computer (PC) with Standard USB port

For detail specifications regarding operating system and software requirements please visit www.hittite.com

4.2 Software Installation

1. Double-click on the "Hittite Dual Chip Synth Eval Software Installer Vxxxx.exe" file available on the Evaluation Kit CD and/or downloaded from www.hittite.com, and follow the installation wizard.

4.3 Hardware Setup

The HMC983LP5E & HMC984LP4E evaluation board includes both the Phase Detector (HMC984LP4E) and the Fractional Divider (HMC983LP5E) that are configured by default to together form a complete frequency synthesizer. Hardware board changes are required to configure the HMC983LP5E & HMC984LP4E evaluation board to evaluate the HMC983LP5E (Fractional Divider), or the HMC984LP4E (Phase Detector) separately. Hardware test configurations for testing the HMC983LP5E and HMC984LP4E separately are discussed in sections 4.3.2 and 4.3.3 respectively.

The default evaluation board configuration is shown in the HMC983LP5E & HMC984LP4E evaluation board schematic that is included in the HMC983LP5E & HMC984LP4E Evaluation Kit. The schematic also includes optional configurations. All components that are part of optional configurations are not populated on the HMC983LP5E & HMC984LP4E evaluation board, and are labeled as 'DEPOP' in the HMC983LP5E & HMC984LP4E evaluation board schematic. Components required for optional configuration are not included in the HMC983LP5E & HMC984LP4E Evaluation Kit, and have to be supplied by the user.

4.3.1 Default Hardware Test Configuration

To evaluate both the HMC983LP5E and the HMC984LP4E together as a complete frequency synthesizer connect all hardware as shown in Figure 1.

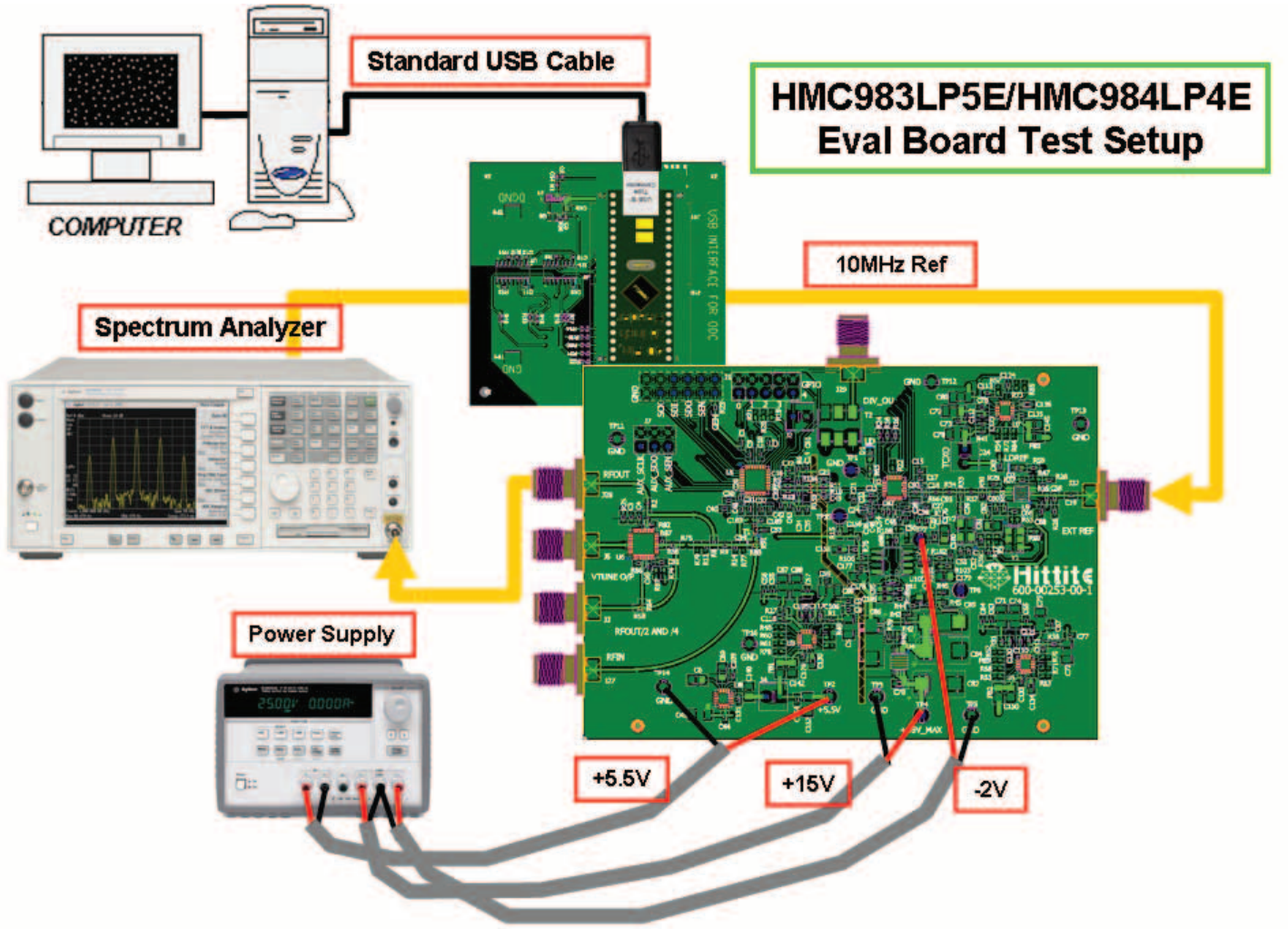


Figure 1. Default Evaluation Board Test Setup

1. Plug the USB Interface Board header connector into the evaluation board header connector.
2. Connect the USB Interface Board to the USB port of the PC through the USB Cable provided in the Kit. The PC should then detect new hardware called DLP2332M.
3. Setup the evaluation board
   - Ensure that Jumpers on J4 and J24 are installed.
   - Connect the J28 (RFOUT SMA connector) of the HMC983LP5E & HMC984LP4E evaluation board output to a test instrument such as a spectrum analyzer.
   - Connect the test instruments (Spectrum Analyzer's) 10 MHz Reference Output to J32 SMA connector (external reference input) of the HMC983LP5E & HMC984LP4E Evaluation Board.
   - Set the DC power supply to +5.5 V and connect to the evaluation board. This supply is used to power all of the components on the evaluation board except the AD797ARZ OpAmp used in the active loop filter configuration.
   - Set the DC power supply to +15 V and -2 V and connect to the evaluation board. This supply is used to power the AD797ARZ OpAmp used in the active loop filter configuration.

4.3.2 Loop Filter Configuration

The HMC983LP5E & HMC984LP4E evaluation board allows both active and passive loop filter configurations.

4.3.2.1 Default Loop Filter Configuration

The default configuration is an active loop filter using AD797ARZ OpAmp. Because the AD797ARZ is an inverting OpAmp, the provided default register setting files for the HMC984LP4E phase detector are configured for inverted phase polarity with offset current set in the up direction. It is recommended to change the HMC984LP4E phase detector polarity to non-inverting (Reg03h[4] = 0 of HMC984LP4E), and offset current direction to down (Reg04h[21] = 0 and Reg04h[22] = 1 of HMC984LP4E), when using non-inverting (active or passive) loop filter design.

4.3.2.2 User-Defined Loop Filter Configurations

Hittite PLL Design software that is included in the HMC983LP5E & HMC984LP4E evaluation kit and available for download from **www.hittite.com** is recommended to be used to design user-defined loop filter configurations, if needed. The Hittite PLL Design model files for HMC983LP5E & HMC984LP4E evaluation board are available upon request by contacting apps-support@hittite.com.

Hardware board changes are required to change the loop filter design or configuration. **Figure 2** shows all of the loop filter components present on the HMC983LP5E & HMC984LP4E evaluation board. More details are available in the HMC983LP5E & HMC984LP4E evaluation board schematic.

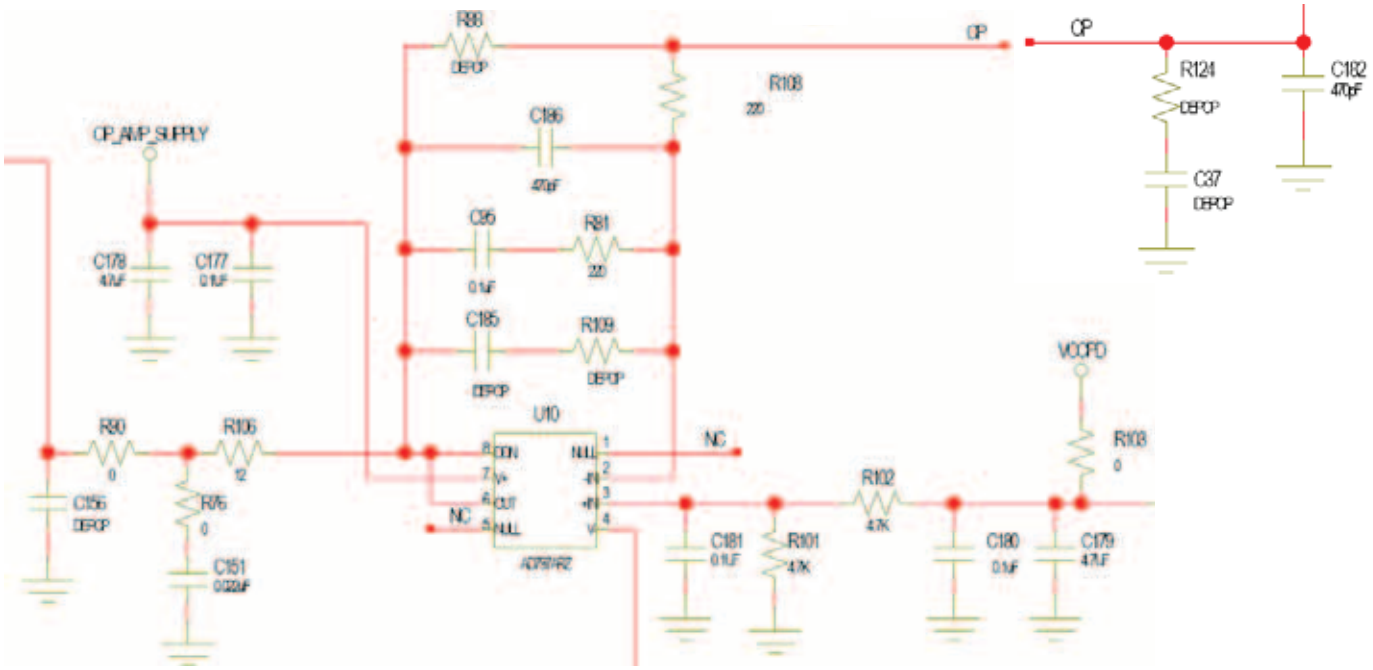


Figure 2. Dual Chip Synthesizer Loop Filter

4.3.2.3 Passive Loop Filter Configuration Example

To configure the HMC983LP5E & HMC984LP4E evaluation board for a third order passive loop filter design with loop filter bandwidth of 118 kHz and 76 degree phase margin, the following components need to be removed:

- R108, C186, C95, R81

+15 V and -2 V power supply needs to be disabled.

The following component values need to be set (component details are shown in **Figure 2** and available in the HMC983LP5E & HMC984LP4E evaluation board schematic that is included in the HMC983LP5E & HMC984LP4E Evaluation Kit):

- C182 = 120 pF
- C37 = 47 nF
- C151 = 390 pF
- R124 = 300 Ω
- R88 = 0 Ω
- R106 = 120 Ω
- R76 = 0 Ω

Finally, the HMC984LP4E phase detector polarity needs to be set to non-inverting (Reg03h[4] = 0 of HMC984LP4E), and offset current direction set to down (Reg04h[21] = 0 and Reg04h[22] = 1 of HMC984LP4E). Detailed register writes required for these changes are available in the HMC984LP4E data sheet.

4.3.3 Evaluating HMC983LP5E Fractional Divider as a Stand-Alone Part

Hardware board changes are required to configure the HMC983LP5E & HMC984LP4E evaluation board to test the HMC983LP5E as a stand-alone part. For the changes below please refer to the HMC983LP5E & HMC984LP4E evaluation board schematic.

The following components need to be depopulated:

- R10, R11, and R91

The following components (labeled 'DEPOP' in the HMC983LP5E & HMC984LP4E evaluation board schematic) need to be populated:

- R6 = R13 = 75 Ω
- R89 = 0 Ω
- C25 = C49 = 3300 pF
- J29 - SMA Connector
- T2 - Balun ADT2-1T

And jumpers J4 and J24 need to be removed.

The measurement test setup for evaluating standalone HMC983LP5E is shown in **Figure 3**.

1. Plug the USB Interface Board header connector into the evaluation board header connector.
2. Connect the USB Interface Board to the USB port of the PC through the USB Cable provided in the kit. The PC should then detect new hardware called DLP2332M.
3. Setup the evaluation board
   - Ensure that Jumpers on J4 and J24 are not installed.
   - Connect the J29 (DIV_OUT SMA connector) of the HMC983LP5E & HMC984LP4E evaluation board to a test instrument such as a spectrum analyzer.
   - Supply the input signal to be divided to the J27 (RFIN SMA connector) of the HMC983LP5E & HMC984LP4E evaluation board.
   - Set the DC power supply to +5.5 V and connect to the evaluation board. This supply is used to power all of the components on the evaluation board except the AD797ARZ OpAmp used in the active loop filter configuration.

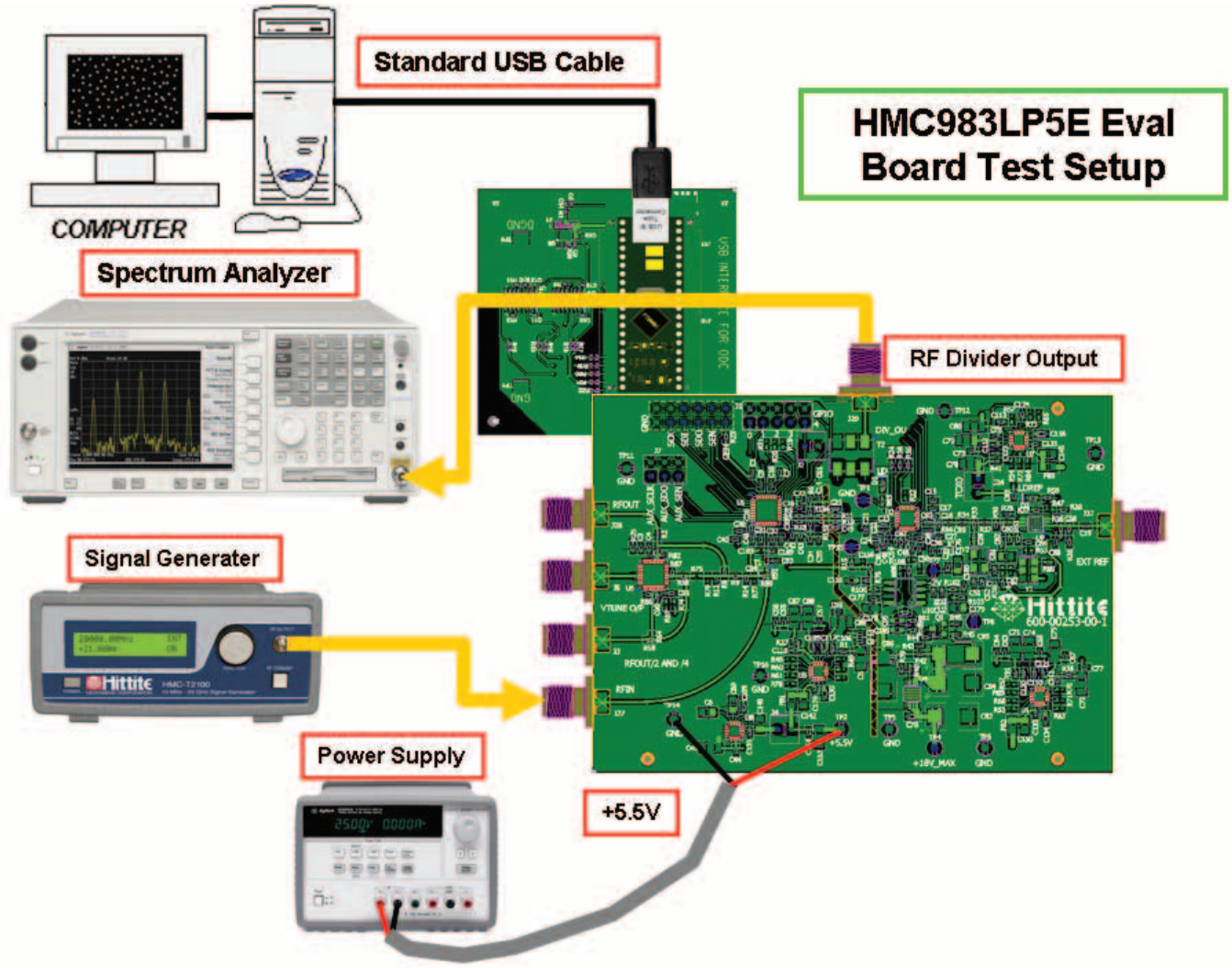


Figure 3. Evaluation Board Test Setup For Testing Stand-Alone HMC983LP5E

4.3.4 Evaluating HMC984LP4E Phase Detector as a Stand-alone Part

Hardware board changes are required to test the HMC984LP4E as a stand-alone part with a divider and/or a VCO other than the HMC983LP5E and the HMC507LP5E which are included in the HMC983LP5E & HMC984LP4E Evaluation Board.

To evaluate the HMC984LP4E as a stand-alone part, the following changes are required. Details are also available in the HMC983LP5E & HMC984LP4E evaluation board schematic.

The following components need to be depopulated:

- R133, and R134

The following components (labeled 'DEPOP' in the HMC983LP5E & HMC984LP4E evaluation board schematic) need to be populated:

- C25 = C49 = 3300 pF
- J29 - SMA Connector
- T2 - Balun ADT2-1T

Jumper J24 is required and jumper J4 has to be removed if on-board VCO HMC507LP5E is not needed. Using this setup the SMA connector J29 will be the input into the HMC984LP4E, and the output of the HMC984LP4E is available at SMA connector J5 (VTUNE O/P).

5. Using the HMC983LP5E & HMC984LP4E Evaluation Software

5.1 Launch Hittite Dual-Chip Synthesizer Software

Click on “Launch Hittite DualChip V1020.exe” from the Start, Program Files, Hittite Microwave Corp Windows menu.

5.2 Using the HMC983LP5E & HMC984LP4E Evaluation Software

- Hittite dual chip synthesizer product selection window shown in **Figure 4** will appear. From the drop down list, select HMC983LP5E & HMC984LP4E, as shown in **Figure 4**.

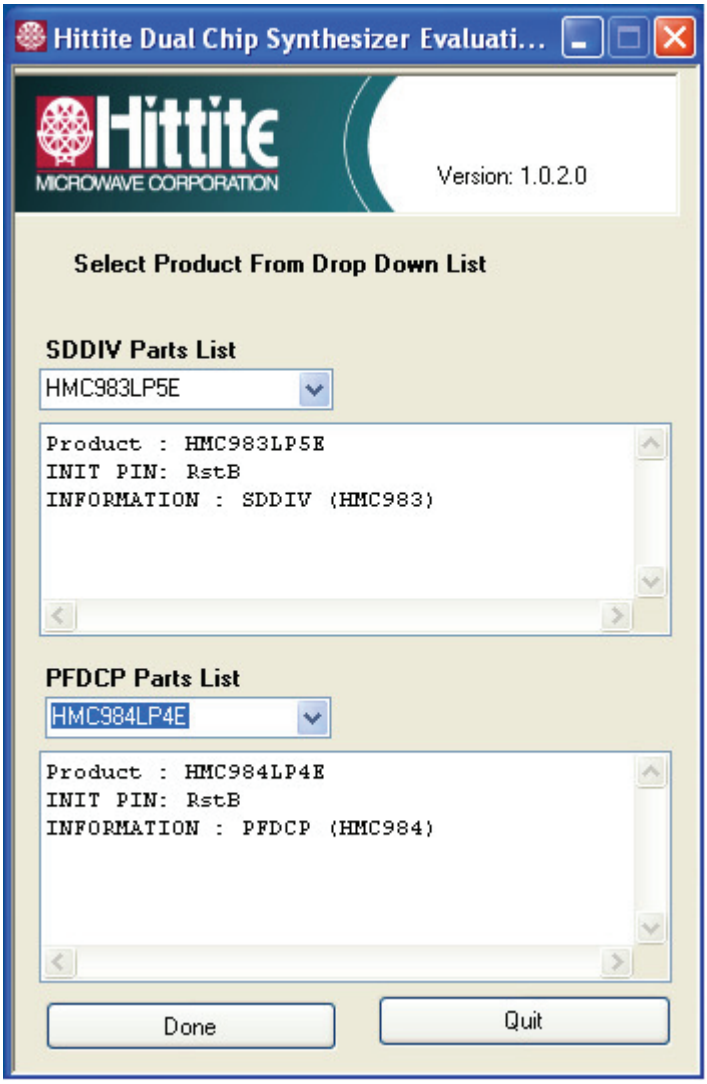


Figure 4. Hittite Dual Chip Synthesizer Product Selection Window

- Press “Done”, the HMC983LP5E & HMC984LP4E Main Control GUI shown in **Figure 5** will appear.

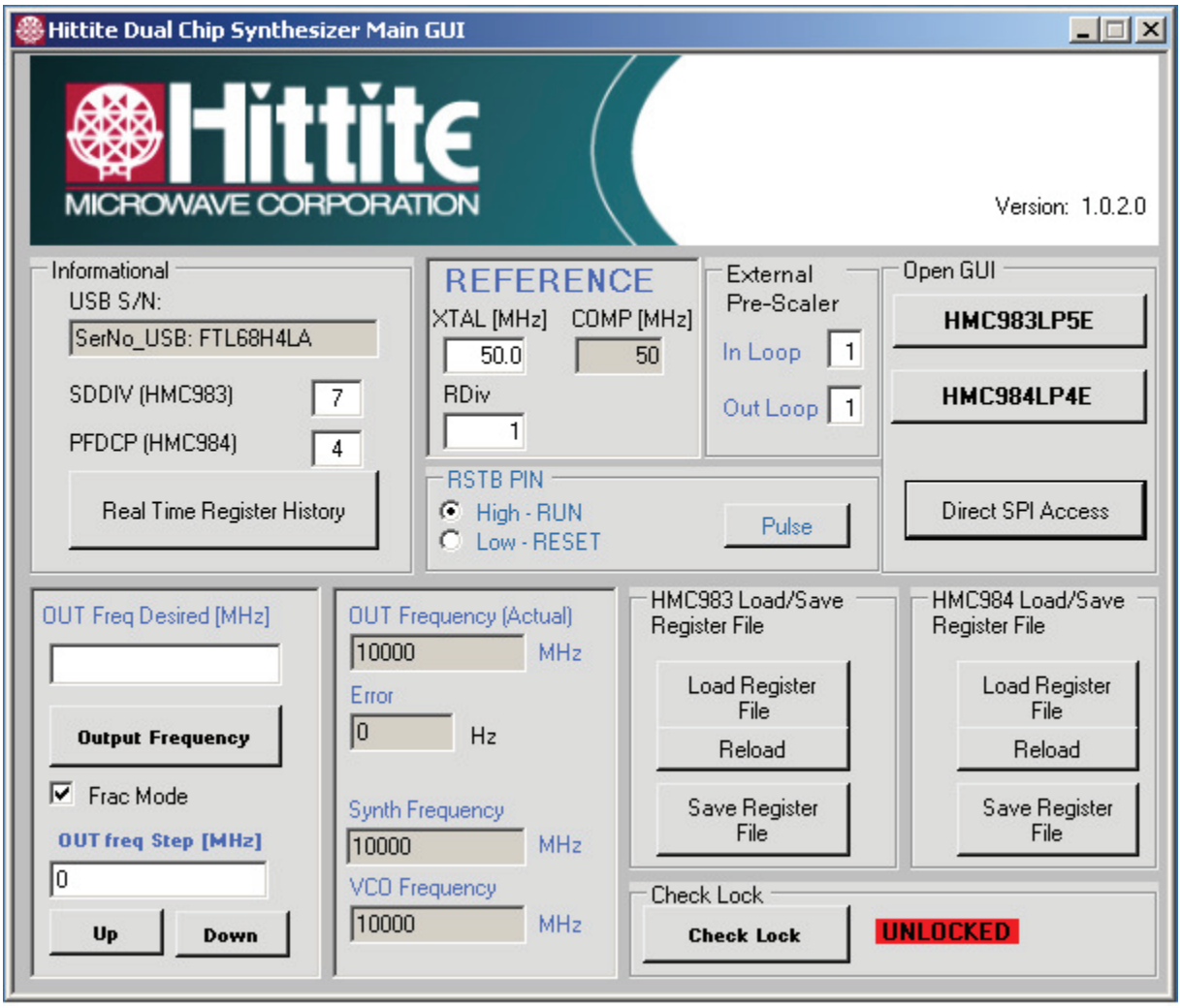


Figure 5. Hittite Dual Chip Synthesizer Main GUI Dialog

5.3 Load the register files.

- To load the default register files for the HMC983LP5E or HMC984LP4E, click on “Load Register File” button in the HMC983LP5E or HMC984LP4E Load/Save Register File frame which is located in the lower right corner of the display. Double-click on the Register Setting Files folder and select either the integer or fractional register file for the appropriate device (HMC983LP5E or HMC984LP4E) , and press “Load Register File” button.
- Press the “Check Lock” button; The Check Lock should now display the green ‘LOCKED’ display indicator as shown in **Figure 12**.

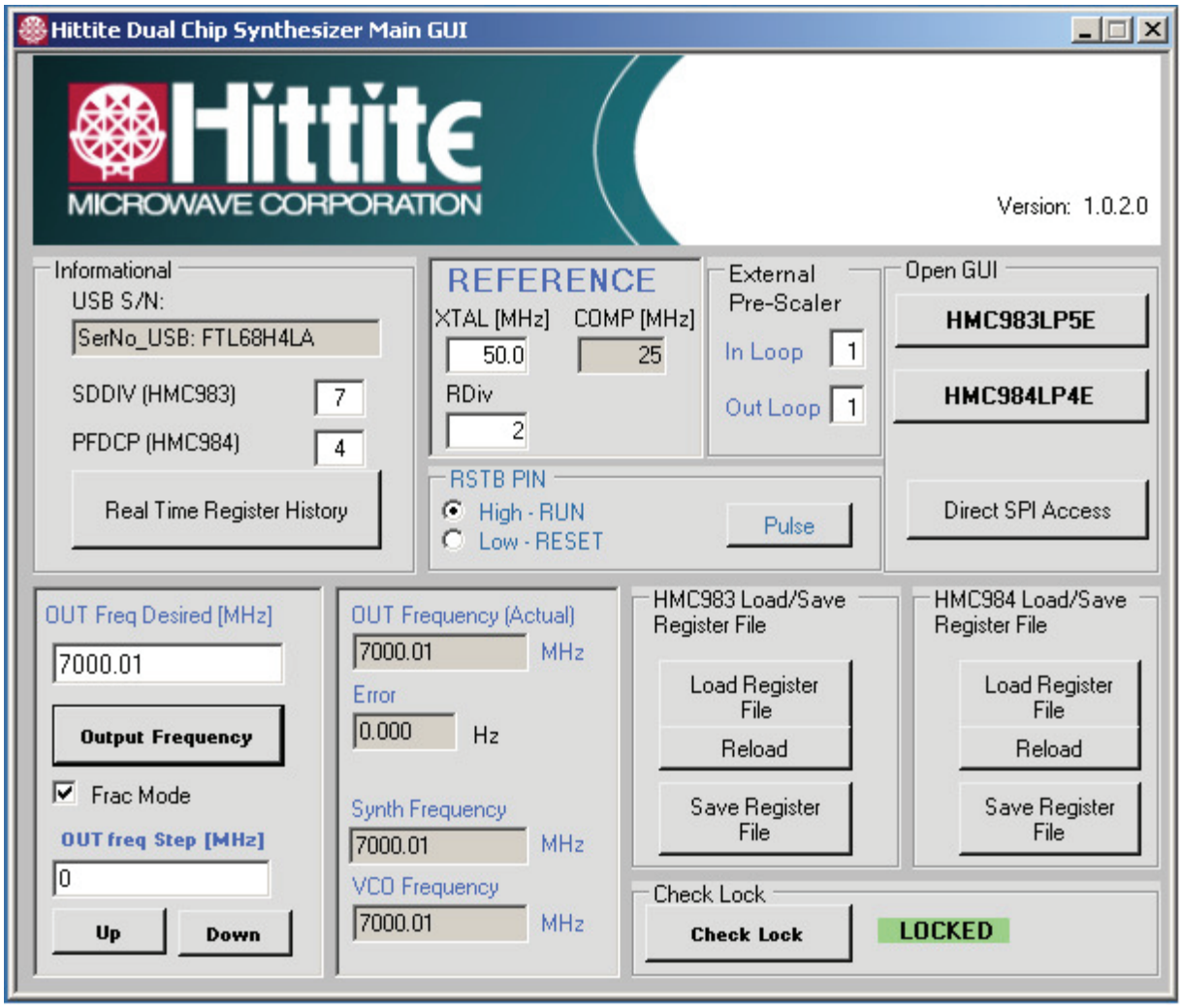


Figure 6. Hittite Dual Chip Synthesizer Main GUI Indicating Synthesizer Lock State

6. Synthesizer Programming

6.1 Frequency Selection

To program the synthesizer to a desired frequency, enter the desired frequency in the “OUT Freq Desired” box and press the “Output Frequency” button (Reference divider is only updated when the “Output Frequency” button is pressed). The “OUT Freq (Actual)” box displays the frequency that the synthesizer is generating. The “Error” box displays the frequency error between the desired and generated frequencies.

6.2 Frequency Step

To step up/down in frequency, enter the desired frequency step size in MHz in the “OUT freq Step” window and press the “Up”/”Down” button.

6.3 Fractional/Integer Mode

To operate in Fractional Mode, check the “Frac Mode” box, otherwise the synthesizer will operate in Integer Mode. The default register setting files included in the HMC983LP5E & HMC984LP4E evaluation software include both fractional and integer versions which are named correspondingly. If integer operation is desired, it is recommended to use integer register files, and vice-versa for fractional mode of operation.

6.4 Check Lock

The “LOCKED”/”UNLOCKED” indicator is updated every time the “Update Frequency” button is pressed. The locked state can be confirmed at any time by pressing the “Check Lock” button.

6.5 Register Read/Write

“Direct SPI Access” buttons provide direct register read/write capability in Hex and Decimal, enabling the user to configure each component in detail.

To observe all of the register states in detail simultaneously, click the appropriate HMC983LP5E or HMC984LP4E button in “Open GUI” frame of the “Hittite Dual Chip Synthesizer Main GUI” dialog (**Figure 6**). Depending on the selected part (HMC983LP5E or HMC984LP4E) **Figure 7** or **Figure 8** will appear. In the Detailed GUIs in **Figure 7** and **Figure 8** (for HMC983LP5E or HMC984LP4E respectively) each register is controlled by its own “Read” and “Write” button in its sub-panel. Changes made by clicking on any check box are only implemented after clicking that register’s “Write” button. Similarly the data is only updated after the “Read” button is clicked.

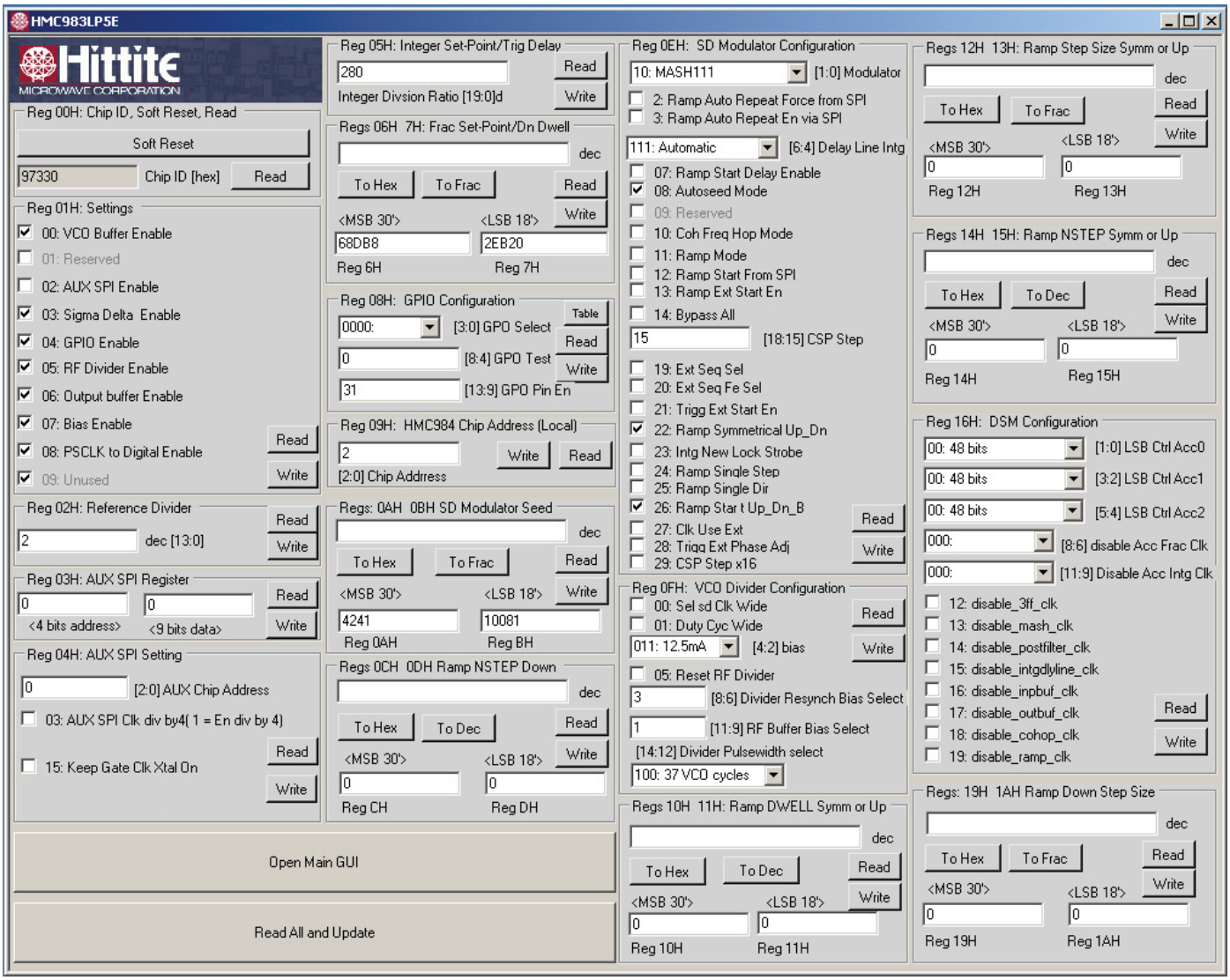


Figure 7. HMC983LP5E Detailed GUI

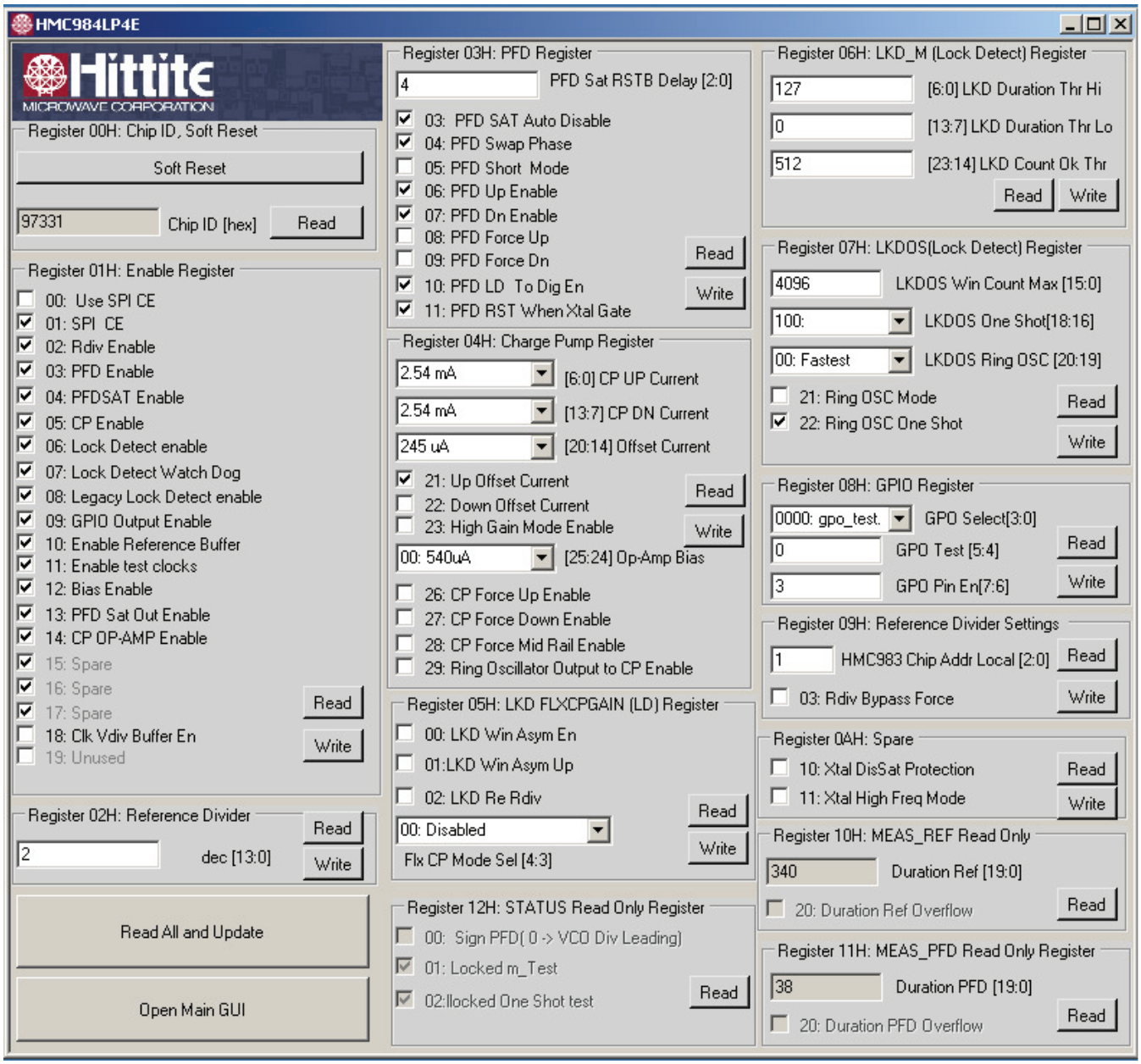


Figure 8. HMC983LP5E Detailed GUI

6.6 Synthesizer Configuration Save/Load

To save a synthesizer configuration to a file, use the “Save Reg File” button in the bottom right corner of the main GUI shown as **Figure 6**. This functionality allows users to save and recall the desired synthesizer configuration state by using the “Load Reg File”.

Technical Support

Please contact apps-support@hittite.com for any questions. Hittite Microwave provides local direct support in many areas around the world. Please see the "Contact Us" page at www.hittite.com.

20 Alpha Road Chelmsford, MA 01824
Phone: 978-250-3343 • Fax: 978-250-3373 • sales@hittite.com