

# Quick Start Guide

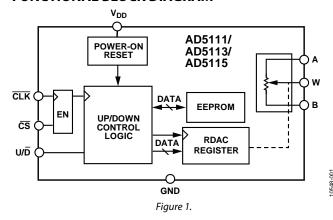
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# Overview of AD5111/AD5113/AD5115 Connections and Functionality

### **GENERAL DESCRIPTION**

This quick start guide provides a general overview of AD5111/AD5113/AD5115 connections and functionality and should be reviewed in conjunction with the AD5111/AD5113/AD5115 data sheet.

# **FUNCTIONAL BLOCK DIAGRAM**



# **PIN CONFIGURATION**

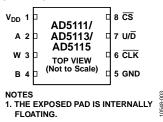


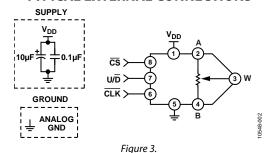
Figure 2.

### PIN FUNCTION DESCRIPTIONS

Table 1.

Pin No.	Mnemonic	Description
1	$V_{DD}$	Positive power supply.
2	Α	Terminal A of RDAC.
3	W	Wiper terminal of RDAC.
4	В	Terminal B of RDAC.
5	GND	Ground pin.
6	CLK	Clock input.
7	U/D	Up/down selection.
8	<u>cs</u>	Chip select.
	EPAD	Internally floating exposed pad.

# **TYPICAL EXTERNAL CONNECTIONS**

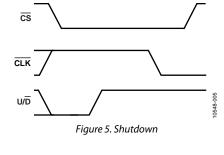


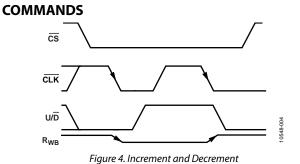
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#### **OPERATIONAL CONDITIONS**

**Table 2. Specifications** 

Parameter	Specification
V <sub>DD</sub> to GND	2.3 V to 5.5 V
$V_A$ , $V_W$ , $V_B$ to GND	GND to V <sub>DD</sub>
Maximum Continuous IA, IW, IB	
$R_{AW} = 5 \text{ k}\Omega$ and $10 \text{ k}\Omega$	±6 mA
$R_{AW} = 80 \text{ k}\Omega$	±1.5 mA
Maximum Clock (CLK)	50 MHz





CLK

U/D

DATA

NEW DATA

Figure 6. Store

#### **REVISION HISTORY**

3/12—Revision 0: Initial Version



#### **ESD Caution**

**ESD** (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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