## QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 326C

## DESCRIPTION

Demonstration circuit 326C is a monolithic step-down DC/DC switching converter featuring the LT1766EFE. The board is optimized for 5 V output at up to 1 A load current and a steady state input voltage range of 6 V to 60 V . With its wide input voltage range, 1.5A internal power switch, 200 kHz switching frequency and thermally enhanced package, the LT1766EFE is a very versatile and powerful IC for DC/DC converters that require compact space, high efficiency and tolerance to high input voltage transients.

The LT1766EFE 200kHz switching frequency allows all of the components to be small, surface mount devices. Synchronization with an external clock of up to 700 kHz is possible. The current-mode control topology creates fast transient response and good loop stability with a minimum number of external components. The low resistance internal power switch ( $0.2 \Omega$ ) achieves high efficiencies of up to $90 \%$. The SHDN pin can be used to
program undervoltage lockout or place the part in micropower shutdown, reducing supply current to $25 \mu \mathrm{~A}$ by driving the pin low.
The LT1766EFE datasheet gives a complete description of the part, operation and applications information. The datasheet must be read in conjunction with this Quick Start Guide for demonstration circuit 326C. In particular, the datasheet section on 'Thermal Calculations' is important for estimating whether a given application's combination of input voltage, load current and frequency will cause the LT1766EFE to exceed it's absolute maximum rated junction temperature. The LT1766EFE is assembled in a small 16-pin thermally enhanced package with exposed pad where proper board layout is essential for maximum thermal performance. See the datasheet section 'Layout Considerations'.
Design files for this circuit board are available. Call the LTC factory.

Table 1. Typical Performance Summary ( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ )

| PARAMETER | CONDITION | VALUE |
| :--- | :--- | :--- |
| Steady State Input Voltage Range | $V_{\text {OUT }}=5 \mathrm{~V}, \mathrm{I}_{\text {OUT }} \leq 1 \mathrm{~A}$ | $6-60 \mathrm{~V}$ |
| Maximum Transient Input Voltage |  | 60 V |
| $\mathrm{~V}_{\text {OUT }}$ | $\mathrm{V}_{\text {IN }}=6 \mathrm{~V}$ to $60 \mathrm{~V}, \mathrm{I}_{\text {OUT }} \leq 1 \mathrm{~A}$ | $5 \mathrm{~V} \pm 3 \%$ |
| Maximum Output Current | $\mathrm{V}_{\text {OUT }}=5 \mathrm{~V}$ | 1 A |
| Output Voltage Ripple | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=1 \mathrm{~A}, \mathrm{~V}_{\text {OUT }}=5 \mathrm{~V}$ | 20 mV PK-PK |
|  | $\mathrm{V}_{\text {IN }}=48 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=1 \mathrm{~A}, \mathrm{~V}_{\text {OUT }}=5 \mathrm{~V}$ | 32 mV PK-PK |
|  | $\mathrm{V}_{\text {IN }}=6 \mathrm{~V}$ to $60 \mathrm{~V}, \mathrm{I}_{\text {OUT }} \leq 1 \mathrm{~A}$ | 200 kHz |
| Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=1 \mathrm{~A}, \mathrm{~V}_{\text {OUT }}=5 \mathrm{~V}$ | $87 \%$ |
|  | $\mathrm{~V}_{\text {IN }}=48 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=1 \mathrm{~A}, \mathrm{~V}_{\text {OUT }}=5 \mathrm{~V}$ | $80 \%$ |

# QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 326C 1.5A, 200KHZ HIGH VOLTAGE BUCK CONVERTER 

## QUICK START PROCEDURE

Demonstration circuit 326C is easy to set up to evaluate the performance of the LT1766EFE. Refer to Figure 1 for proper measurement equipment setup and follow the procedure below:
nOTE: Make sure that the input voltage does not exceed 60 V .

NOTE: The synchronization and shutdown functions are optional and their pins can be left floating (disconnected) if their functions are not being used.

NOTE: Do not hot-plug the input voltage terminal VIN. The absolute maximum voltage on VIN is 60 V and hotplugging a power supply through wire leads to the demonstration circuit can cause the voltage on the extremely low-ESR ceramic input capacitor to ring to twice its DC value. This is due to high currents instantaneously generated in the inductive supply leads from an input voltage step on the low-ESR ceramic input capacitor. A bulky higher-ESR capacitor, and an additional inductive filter can be added to the circuit to dampen hot-plug transient ringing. See Application Note 88 for more details.

1. Connect the power supply (with power off), load and meters as shown in Figure 1.
2. After all connections are made, turn on input power and verify that the output voltage is 5 V .
NOTE: If the output voltage is too low, temporarily disconnect the load to make sure that the load is not set too high.
3. Once the proper output voltages are established, adjust the loads within the operating range and observe the output voltage regulation, ripple voltage, efficiency and other parameters.


Figure 1. Proper Measurement Equipment Setup

## CUSTOM OPTIONS

## OUTPUT VOLTAGE

The components assembled on the board are optimized for a wide input voltage range and a 5 V output. The feedback resistors (R2, R3) can be changed to adjust the output voltage according to the following equation:
$V_{\text {OUT }}=1.22(1+\mathrm{R} 2 / R 3)$
The Thevenin equivalent resistance seen at the FB pin should be less than 3.8 k to maximize frequency foldback during start-up and short-circuit.
For output voltages below 3V, the boost diode should be moved from D2 to D3 to provide at least the minimum boost voltage required for the internal power switch. Make sure that the boost capacitor (C4) has a voltage rating (1) greater than the output voltage (when the boost diode is placed in D2) or (2) greater than the input voltage (when the boost diode is placed in position D3).

For output voltages greater than 5 V , the optional 'blocking' zener diode D4 can be used to reduce the boost voltage across C4 to some lower voltage between 3V and 5 V . The diode transfers power dissipation from inside the LT1766EFE to the diode on the demonstration circuit, outside the LT1766EFE, allowing higher ambient temperature operation of the part. Maintaining boost voltage between 3 V and 5 V maximizes efficiency and

# QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 326C <br> 1.5A, 200KHZ HIGH VOLTAGE BUCK CONVERTER 

optimizes control of the power switch. It is recommended that a CMHZ5236B zener diode is used in D4 when $V_{\text {OUT }}=12 \mathrm{~V}$. To properly install $D 4$, the small trace shorting the anode to the cathode of D4 on the board must be opened (an Exacto knife works well) before D4 is soldered to the board. In the 'Thermal Calculations' section of the datasheet, the new value for boost voltage ( $\mathrm{V}_{\text {OUT }}-\mathrm{V}_{\mathrm{Z}}$ ) should be accounted for when calculating junction temperature.
$P_{\text {DISS(BOOST }}=\left(V_{\text {OUT }}-V_{Z}\right)^{*}\left(I_{\text {SWI }} / 36\right)^{*} V_{\text {OUT }} / V_{\text {IN }}$

## CERAMIC OUTPUT CAPACITOR

The output capacitor (C5) is a $100 \mu \mathrm{~F}$ tantalum 10 V AVX TPS series with $100 \mathrm{~m} \Omega$ ESR. This tantalum capacitor has low ESR, resulting in low output voltage ripple ( $20 \mathrm{mV} \mathrm{V}_{\text {PK-PK }}$ with $12 \mathrm{~V}_{\text {IN }}, 5 \mathrm{~V}_{\text {OUT }}, 1 \mathrm{~A}$ load current).
For even lower output voltage ripple, a ceramic output capacitor can be used because the ceramic output capacitor will have much lower ESR than the tantalum. A $47 \mu \mathrm{~F}, 6.3 \mathrm{~V}$ X5R or better ceramic capacitor can be used in place of C 5 for solutions with $5 \mathrm{~V}_{\text {OUT }}$ or below. R1, C1, and C2 (and C8) may need to be adjusted to optimize control loop stability for different types of output capacitors and different output voltages. The $3.3 \mathrm{~V}_{\text {OUT }}$ ceramic output capacitor solution in Figure 6 has 15 mV PK-PK output voltage ripple with $12 \mathrm{~V}_{\mathrm{IN}}$ and 1 A load current.

For more details, see the 'Output Capacitor' and 'Ceramic Capacitors' sections of the Applications Data in the datasheet.

## COMPENSATION

Demonstration circuit 326C has a frequency compensation network that is optimized for the tantalum output capacitor C 5 , the wide input voltage range 6 V to 60 V , and 5 V output. Improved loop bandwidth can be achieved for various output voltages, output capacitors, and input voltage ranges by adjusting R1, C1, and C2. Additional optional component locations for a feedforward capacitor (C8) and a resistor (R4 for short circuit feedback pin protection when C8 is used) are located in parallel with R2. For more information, see the 'Frequency Compensation' section in the Applications Information in the datasheet, Application Note 19, or Application Note 76.

## SOFT START

When the ratio of the input voltage divided by the output voltage (plus the forward voltage of the catch diode) is greater than ten
$V_{\text {IN }} /\left(V_{\text {OUT }}+V_{F}\right)>10$
the soft start circuit made up of components C9, R5, R6, and Q1 should be used to control inductor current during startup. The soft start circuit is also useful for controlling output voltage rise time and overshoot without affecting transient response performance during normal operation of the converter. The soft start circuit is covered in detail in 'Buck Converter with Adjustable Soft Start' in the datasheet.


Figure 2. DC326C Typical Efficiency ( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ )


Figure 3. DC326C Typical Step Load Response ( $\mathrm{I}_{\text {OUT }}=500 \mathrm{~mA}$ to $1 \mathrm{~A}, \mathrm{~V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{V}_{\text {OUT }}=5 \mathrm{~V}$ ) CH3 is $\mathrm{V}_{\text {OUT }}(\mathrm{AC})$


Figure 4. DC326C Typical Output Voltage Ripple
( $I_{\text {OUT }}=1 \mathrm{~A}, \mathrm{~V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ ) CH3 is $\mathrm{V}_{\text {OUT }}$ ripple, CH1 is $\mathrm{V}_{\text {SW }}$


Figure 5. Typical Output Voltage Ripple of Ceramic Output Capacitor Solution in Figure 6 ( $\mathrm{I}_{\mathrm{OUT}}=1 \mathrm{~A}, \mathrm{~V}_{\mathrm{IN}}=$ $12 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=3.3 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ ) CH1 is $\mathrm{Vsw}, \mathrm{CH} 3$ is $\mathrm{V}_{\text {OUT }}$ ripple

## QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 326C



Figure $6.8 V_{\text {IN }}$ to $40 V_{\text {IN }}, 3.3 V_{\text {OUT }}, 1 A_{\text {OUT }}$ All-Ceramic Capacitor Solution with 3.0 mm Maximum Height

## QUICK START GUIDE FOR DEMONSTRATION CIRCUIT 326C 1.5A, 200KHZ HIGH VOLTAGE BUCK CONVERTER




NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0603.

ALL CAPACITORS ARE IN MICROFARADS, 0603.
2. ALL OPTIONAL COMPONENTS WILL HAVE NO COMPONENT OUTLINE ON BOARD EXCEP Q1, R5, R6, AND C9.


Linear Technology Corporation
LT1766EFE

### 1.5A, 200kHz HIGH VOLTAGE BUCK CONVERTER

ENG: KEITH SZOLUSHA

BILL OF MATERIALS

| Item | Qty | Reference | Part Description | Manufacture / Part \# |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |
|  |  |  |  |  |  |
|  |  |  |  |  |  |
| 1 | 1 | C1 | CAP, NPO 220pF 25V 10\% | AVX, 06033A221KAT1A |  |
| 2 | 1 | C2 | CAP, X7R 0.022uF 16V 10\% | AVX, 0603YC223KAT1A |  |
| 3 | 1 | C3 | CAP, X7R 2.2uF 100V, 80\%, 1812 | TDK, C4532X7R2A225K |  |
| 4 | 1 | C4 | CAP, X5R 0.33uF 10V 10\% | AVX, 0603ZD334KAT1A |  |
| 5 | 1 | C5 | CAP, TANT. 100uF 10V 20\%, 7343 | AVX, TPSD107M010R0100 |  |
| 6 | 0 | C6 | CAP, Y5V 0.47uF 25V, 80\% | OPT |  |
| 7 | 0 | C7 | CAP, C SIZE, 6032 | OPT |  |
| 8 | 0 | C8, C9 | CAP, 0603 | OPT |  |
| 9 | 1 | D1 | DIODE SCHOTTKY,1.5A, 60V,SMA-DIODE | IR, 10MQ060N |  |
| 10 | 1 | D2 | DIODE, 200mA, 100V, SOD123 | DIODES INC., 1N4148W |  |
| 11 | 0 | D3 | DIODE, 200mA, 100V, SOD123 | DIODES INC., 1N4448W | (OPT) |
| 12 | 0 | D4 | DIODE ZENER, SOD123 | CENTRAL SEMI., CMHZ5236B | (OPT) |
| 13 | 6 | E1-E6 | TP, TURRET, .094" | MILL-MAX, 2501-2 |  |
| 14 | 1 | L1 | IND, SMT, 47uH, L-CDRH104R | SUMIDA, CDRH104R-470NC |  |
| 15 | 0 | Q1 | TRANS, MMBT2222A, SOT23 | DIODES INC., MMBT2222A-7 | (OPT) |
| 16 | 1 | R1 | RES, 3.3k 5\%, 0603 | AAC, CR16-332JM |  |
| 17 | 1 | R2 | RES, 15.4k 1\%, 0603 | AAC, CR16-1542FM |  |
| 18 | 1 | R3 | RES, 4.99k 1\%, 0603 | AAC, CR16-4991FM |  |
| 19 | 0 | R4,R5,R6 | RES, 0603 | OPT |  |
| 20 | 1 | U1 | IC, LT1766EFE, TSSOPFE16 | LINEAR TECH., LT1766EFE |  |
| 21 | 4 | MTG HOLES AT 4 CORNERS | STANDOFF, NYLON HEX \#4-40 x 1/4" | MICRO PLASTICS, \#14HTSP101 |  |
| 22 | 4 |  | SCREW, \#4-40 x 1/4 | ANY |  |
|  |  |  |  |  |  |
|  |  |  |  |  |  |
|  |  | NOTES: UNLESS OTHERWISE SPECIFIED |  |  |  |
|  |  | 1. ALL RESISTORS ARE IN OHMS. |  |  |  |
|  |  | 2. INSTALL 4 STANDOFFS AT 4 CORNERS OF BOARD AND ON THE BOTTOM SIDE. |  |  |  |

