

2-String × 60mA, 2MHz Step-Up LED Driver with ±2% Current Matching

DESCRIPTION

Demonstration circuit 2176A features the LT[®]3909, a 2-string × 60mA, 2MHz step-up LED driver with ±2% current matching.

The LT3909 precisely regulates two LED strings for equal current, supports LED dimming that reduces brightness down to a 1/40,000 ratio, protects against faults and also reports faults. The LT3909 consists of a step-up converter with a fixed 2MHz switching frequency and current sources for the LED strings.

The operating voltage range for DC2176A is from 7V to 36V. The maximum current in each LED string is 40mA and is programmed by 24.9k I_{SET} resistor R1. The I_{SET} resistor can be changed to 20k or 16.7k to program the string current to 50mA or 60mA respectively. 16.7k is a 0.1% resistor—a 1% 16.9k resistor will result in slightly less than 60mA string current. See the data sheet for application examples with 50mA and 60mA string currents.

The LT3909 utilizes voltage feedback during PWM dimming and when there are faults on both LED strings. Voltage feedback resistors R2 and R3 on DC2176A optimize operation for LEDs that are 35V maximum. Efficiency is 86% at 12V input, 35V for the LED string forward voltages and 40mA per string.

LED current can be reduced by an external voltage applied to the CTRL terminal. Resistor R8 pulls up CTRL to the INTV_{CC} voltage of the LT3909 so the LED current is at the 40mA maximum when there is no external CTRL voltage applied. There are placeholders to add voltage dividers either from V_{IN} or INTV_{CC} to the CTRL pin. A voltage divider from V_{IN} to the CTRL pin can be used to reduce LED current at low input voltages. A resistor divider from INTV_{CC} to CTRL allows an NTC to reduce LED current at high temperatures. There are placeholders for an RC filter between the CTRL terminal and the CTRL pin. The RC filter limits the ramp time of the CTRL pin and prevent false faults when CTRL ramps too quickly. The FAULT terminal

is pulled up by 100k R6 to V_{IN}, but there is a placeholder to pull up FAULT to INTV_{CC} instead.

Resistor divider R4 and R5 from the input to the EN/UVLO pin programs the circuit to turn off when V_{IN} decreases below 5V with 0.9V of hysteresis. The circuit can also be turned off by connecting the EN/UVLO terminal to ground.

An external voltage must be applied to the PWM terminal in order for DC2176A to operate. The external voltage can be a DC level or an appropriate PWM dimming signal. A common frequency for PWM dimming is 100Hz. There is a placeholder for a resistor to pull up PWM to INTV_{CC} so an external voltage on PWM for operation is unnecessary.

There is a placeholder that allows an external diode to be placed in parallel with the internal LT3909 rectifier for higher current capability. DC2176A uses an inductor that is optimized for high efficiency, and the input supply for power to the inductor can be separated from the LT3909 V_{IN} pin to allow operation from two input sources. Ceramic capacitors are used at the input, output and for INTV_{CC}. A 50V tantalum polymer capacitor protects the input from ringing that may occur when the input is hot-plugged using long leads. There are placeholders occupied by 0805 size 0Ω jumpers on the back of the PCB for two LED strings and also placeholders for SOD323 diodes to clamp the LED1 and LED2 voltages from ringing below ground.

2-string operation, close current matching, an extremely wide dimming range and fault protection make the LT3909 ideal for functionally advanced back lighting and illumination circuits. DC2176A features the LT3909EDD in a thermally enhanced 12-lead 3mm × 3mm DFN package. The LT3909 data sheet must be read in conjunction with this demo manual to properly use or modify DC2176A.

Design files for this circuit board are available at <http://www.linear.com/demo/DC2176A>

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DEMO MANUAL DC2176A

PERFORMANCE SUMMARY

Specifications are at $T_A = 25^\circ\text{C}$

PARAMETERS	CONDITIONS	MIN	TYP	MAX	UNITS
Minimum Input Voltage, V_{IN}	$V_{LED1} = V_{LED2} = 35\text{V}$, $I_{LED1} = I_{LED2} \leq 40\text{mA}$			7	V
Maximum Input Voltage, V_{IN}	$V_{LED1} = V_{LED2} = 35\text{V}$, $I_{LED1} = I_{LED2} \leq 40\text{mA}$	36			V
Maximum LED Voltage, V_{LED1} , V_{LED2}	$R2 = 976\text{k } 1\%$, $R3 = 31.6\text{k } 1\%$	35			V
LED Current, I_{LED1} , I_{LED2}	$R1 = 24.9\text{k}$, $V_{CTRL} = \text{INTV}_{CC}$		40		mA
Input EN/UVLO Turn-On Voltage, V_{IN}	$R4 = 402\text{k}$, $R5 = 130\text{k}$, V_{IN} Rising		5.9		V
Input EN/UVLO Turn-Off Voltage, V_{IN}	$R4 = 402\text{k}$, $R5 = 130\text{k}$, V_{IN} Falling		5		V
Efficiency	$V_{IN} = 12\text{V}$, $V_{LED1} = V_{LED2} = 35\text{V}$, $I_{LED1} = I_{LED2} = 40\text{mA}$		86		%
Switching Frequency			2		MHz

QUICK START PROCEDURE

To evaluate the performance of the LT3909 using DC2176A, refer to Figure 1 for the proper measurement equipment setup and then follow the procedure below:

NOTE: An external voltage must be applied to the PWM terminal in order for DC2176A to operate. See Step 4 for details.

1. Connect two strings of LEDs with a forward voltage of 35V or less and corresponding meters.
2. Connect the EN/UVLO terminal to GND with a clip-on lead.
3. With the power off, connect the input power supply and corresponding meters.
4. Apply an external voltage to the PWM terminal. The external voltage can be a 2V to 5V DC source or a 100Hz or higher PWM dimming signal. Install resistor R10 to pull up PWM to INTV_{CC} if an external voltage on PWM is not desired.
5. Turn the V_{IN} power supply on and verify that the input voltage is between 7V and 36V. The V_{IN} voltage is allowed to exceed the LED voltage by a few volts, but a V_{IN} that is much higher than the LED voltages may activate overtemperature protection or dissipate too much power in the LT3909.
6. Release the EN/UVLO to GND connection.
7. Once the LED strings are running at the programmed LED current; adjust the input voltage within the operating range and observe the LED current regulation or reduction in brightness with LED dimming.



QUICK START PROCEDURE

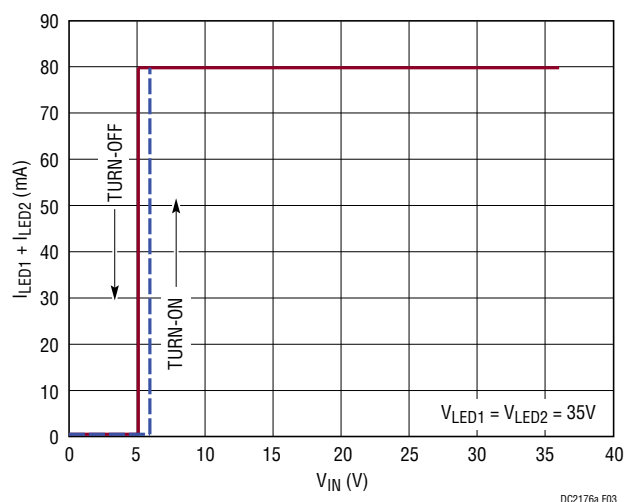


Figure 3. Total Output Current $I_{LED1} + I_{LED2}$ vs Input Voltage V_{IN} with $V_{LED1} = V_{LED2} = 35V$. UVLO Occurs at $V_{IN} = 5V$ Input Falling with 0.9V of Hysteresis

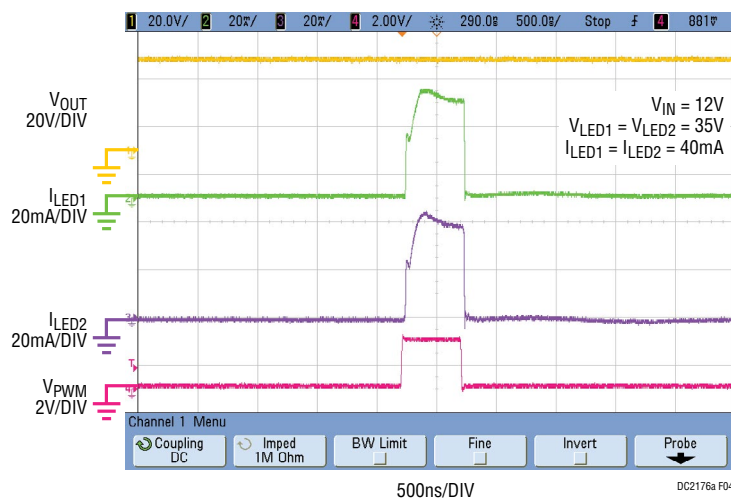
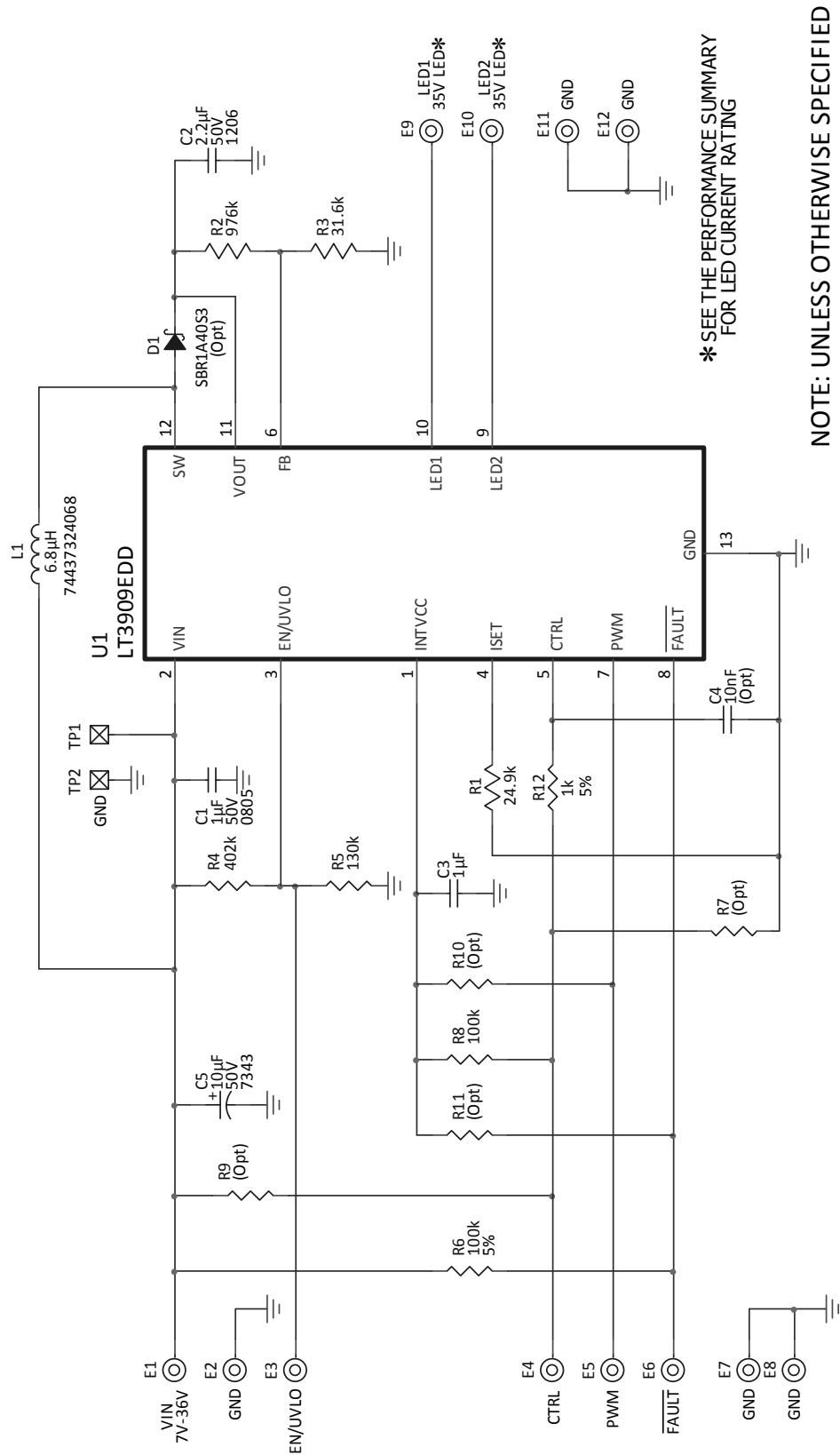


Figure 4. LED Current When Brightness is Reduced to a 1/20,000 Ratio by PWM Dimming at 100Hz

PARTS LIST

ITEM	QTY	REFERENCE	PART DESCRIPTION	MANUFACTURER/PART NUMBER
Required Circuit Components				
1	1	C1	Cap, 1 μ F X7R 50V 10% 0805	Murata GRM21BR71H105KA12L
2	1	C2	Cap, 2.2 μ F X7R 50V 20% 1206	TDK C3216X7R1H225M
3	1	C3	Cap, 1 μ F X5R 6.3V 20% 0603	TDK C1608X5R0J105M
4	1	L1	Inductor, 6.8 μ H 4020	Würth Elektronik 74437324068
5	1	R1	Res, Chip 24.9k 0.06W 1% 0603	Vishay CRCW060324K9FKEA
6	1	R2	Res, Chip 976k 0.01W 1% 0603	Vishay CRCW0603976KFKEA
7	1	R3	Res, Chip 31.6k 0.10W 1% 0603	Vishay CRCW060331K6FKEA
8	1	R4	Res, Chip 402k 0.06W 1% 0603	Vishay CRCW0603402KFKEA
9	1	R5	Res, Chip 130k 0.06W 1% 0603	Vishay CRCW0603130KFKEA
10	1	U1	IC, LED Driver DFN(12) (DD) 3mm \times 3mm	Linear Technology Corporation LT3909EDD#PBF
Additional Demo Board Circuit Components				
1	0	C4 (Opt)	Cap, 10nF X7R 100V 10% 0603	AVX 06031C103KAT2A
2	1	C5	Cap, Tant Polymer 10 μ F 50V 20% 7343	AVX TCJD106M050R0120
3	0	D1 (Opt)	Schottky Diode, 1A SOD-323	Diodes Inc SBR1A40S3
4	1	R6	Res, Chip 100k 0.06W 5% 0603	Vishay CRCW0603100KJEA
5	0	R7, R9, R10, R11 (Opt)	Res, 0603	
6	1	R8	Res, Chip 100k 0.06W 1% 0603	Vishay CRCW0603100KFKEA
7	1	R12	Res, Chip 1k 0.06W 5% 0603	Vishay CRCW06031K00JEA
8	28	R13, R14, R15, R16, R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32, R33, R34, R35, R36, R37, R38, R39, R40	Res/Jumper, Chip 0 0.125W 0805	Vishay CRCW08050000Z0EA
Hardware: For Demo Board Only				
1	12	E1, E2, E3, E4, E5, E6, E7, E8, E9, E10, E11, E12	Turret, Testpoint	Mill Max 2501-2-00-80-00-00-07-0
2	4	MH1 to MH4	Stand-Off, Nylon 1/4" Tall (Snap On)	Keystone, 8831(Snap On)

SCHEMATIC DIAGRAM

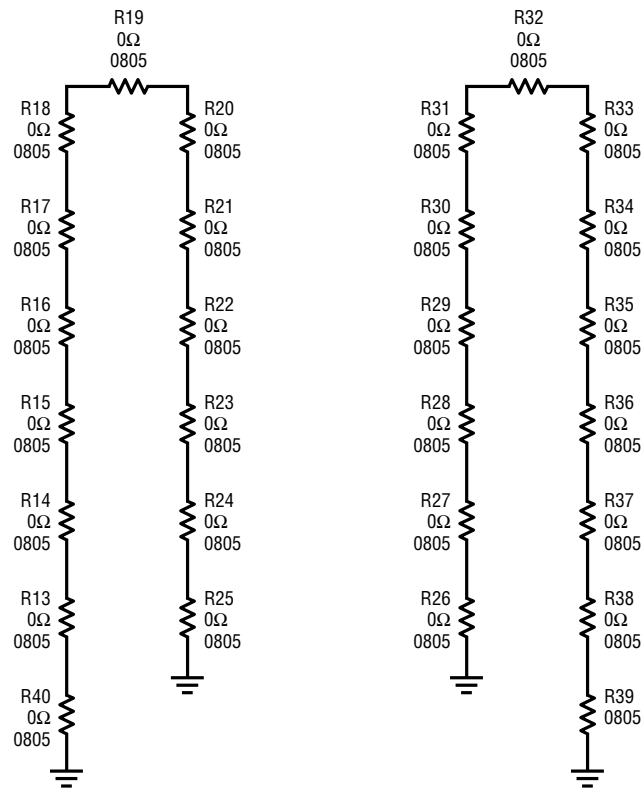


* SEE THE PERFORMANCE SUMMARY
FOR LED CURRENT RATING

NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS AND CAPACITORS 0603.
2. ALL RESISTORS ARE 1%.

SCHEMATIC DIAGRAM



DEMO MANUAL DC2176A

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