# STK681-310



### Thick-Film Hybrid IC

# Forward/Reverse Motor Driver

http://onsemi.com

#### Overview

The STK681-310 is a hybrid IC for use in current control forward/reverse DC motor driver with brush.

### **Applications**

• Office photocopiers, printers, etc.

#### **Features**

- Allows forward, reverse, and brake operations in accordance with the external input signal.
- 4.2A startup output current and 8A peak brake output current.
- Incorporating a current detection resistor (0.10 $\Omega$ ), fixed current control is possible.
- Obviate the need to design for the dead time in order to turn off the upper- and lower drive devices when switching between the forward and reverse operation mode.

### **Specifications**

### **Absolute maximum ratings** at Tc = 25°C

Parameter	Symbol	Conditions	Ratings	unit
Maximum supply voltage 1	V <sub>CC</sub> 1 max	V <sub>CC</sub> 2=0V	52	V
Maximum supply voltage 2	V <sub>CC</sub> 2 max	No signal -0.3 to +7.0		V
Input voltage	V <sub>IN</sub> max	Logic input pins	-0.3 to +7.0	V
Output current	I <sub>O</sub> max	V <sub>CC</sub> 2=5.0V, DC current	4.2	Α
Brake current	I <sub>O</sub> B max	V <sub>CC</sub> 2=5.0V, square wave current, operating time 60ms (single pulse)		А
Allowable power dissipation	PdPK max	No heat sink	3.1	W
Operating substrate temperature	Tc max		105	°C
Junction temperature	Tj max		150	°C
Storage temperature	Tstg		-40 to +125	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### Allowable Operating Ranges at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	unit
Operating supply voltage 1	V <sub>CC</sub> 1	With signals applied	10 to 42	V
Operating supply voltage 2	V <sub>CC</sub> <sup>2</sup>	With signals applied	5±5%	V
Input voltage	V <sub>IN</sub>		0 to V <sub>CC</sub> 2	V
Output current 1	I <sub>O</sub> 1	V <sub>CC</sub> 2=5.0V, DC current, Tc≤70°C	4.2	Α
Output current 2	I <sub>O</sub> 2	V <sub>CC</sub> 2=5.0V, DC current, Tc=90°C	3.2	Α
Output current 3	I <sub>O</sub> 3	V <sub>CC</sub> 2=5.0V, DC current, Tc=105°C	2.5	Α
Brake current	IOB	$V_{CC}$ 2=5.0V, square wave current, operating time 3.6ms, Tc=105°C	8	А

Refer to the graph for each conduction-period tolerance range for the output current and brake current.

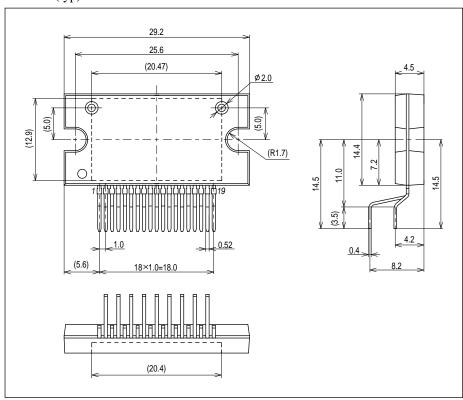
## **Electrical Characteristics** at Tc = 25°C, $V_{CC}1 = 24V$ , $V_{CC}2 = 5.0V$

Parameter	Symbol	Conditions	min	typ	max	unit
V <sub>CC</sub> 2 supply current	lcco	Forward or reverse operation		1.7	4	mA
FET diode forward voltage	Vdf	If=1A (R <sub>L</sub> =23Ω)		1.0 1.6		V
Output saturation voltage 1	Vsat1	R <sub>L</sub> =23Ω, TR1, TR2		0.80 1.1		V
Output saturation voltage 2	Vsat2	R <sub>L</sub> =23Ω, F1, F2+current detection resistance		0.19 0.26		V
Output leak current	lOL	TR1, TR2, F1, and F2 OFF operation			50	μΑ
Input high voltage 1	V <sub>IH</sub> 1	IN1, IN2 pins	4.5			V
Input high voltage 2	V <sub>IH</sub> 2	INH pin	2.5			V
Input low voltage	VIL	IN1, IN2, INH pins			0.6	V
Input current 1	I <sub>IH</sub> 1	IN1, IN2 pins, V <sub>IH</sub> 1=5V	0.10	0.20	0.40	mA
Input current 2	I <sub>IH</sub> 2	INH pin, V <sub>IH</sub> 2=5V	0.30	0.60	1.2	mA
Current set ting voltage	Vref1	Between pins Vref1 and S.P		0.42		V

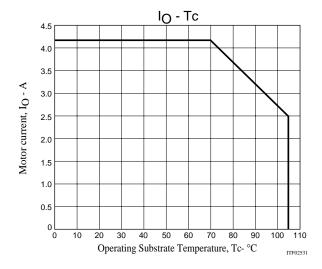
Note: A fixed-voltage power supply must be used.

# **Package Dimensions**

unit:mm (typ)

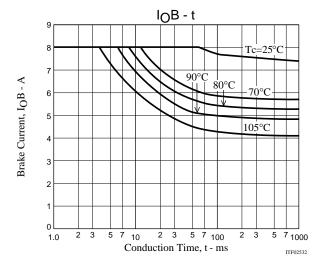


Derating Curve of Motor Current, IO. vs. STK681-310 Operating Board Temperature, Tc

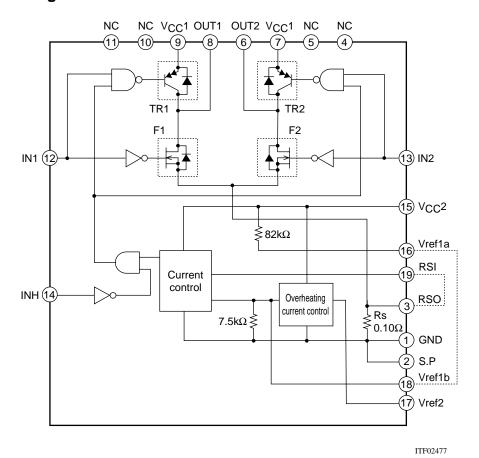


The range of DC operating current lies within the above derating curve in the  $V_{\rm CC}1$  allowable operating range. The above motor current  $I_{\rm O}$  is valid within the chopping operating range of  $V_{\rm CC}1$ =28V or lower. The above operating substrate temperature, Tc, is measured immediately when the motor is started. Since Tc fluctuates due to the ambient temperature, Ta, the motor current value, and continuous or intermittent operations of the motor current, always confirm this values using an actual set.

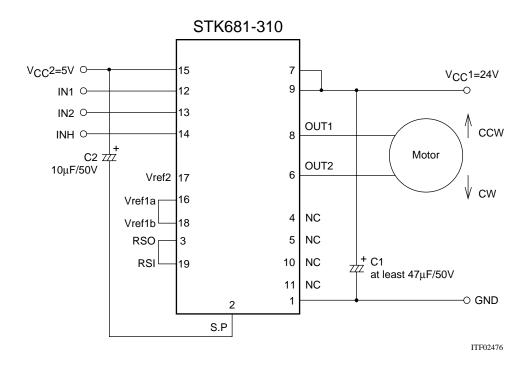
STK681-310 Allowable Brake Current Range



## **Internal Block Diagram**



### **Sample Application Circuit**



#### STK681-310

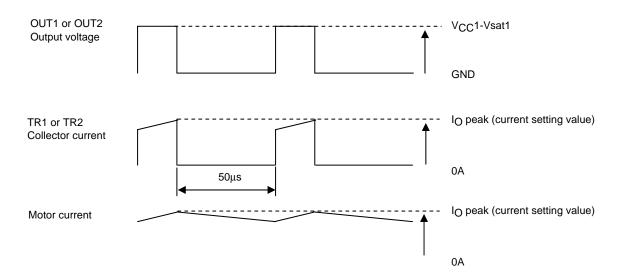
### Motor Drive Conditions (H: High-level input; L: Low-Level Input)

	IN1	IN2	INH	Remarks
Stop 1 (standby)	Н	Н	H or L	When motor is not rotating
Stop 2 (supply power turned off by input during	Н	Н	Н	Stop signal that is applied when the motor is
motor rotation)	Н	L	Н	running and used to turn off power.
	L	Н	Н	
Forward (CW)	Н	L	L	No input signal is needed that turns off the
Reverse (CCW)	L	Н	L	upper- and lower-side drive devices when switching the rotational direction.
Brake	L	L	L or H	GND side MOSFET ON

<sup>\*</sup> IN1=IN2=H and INH=L are prohibited during motor rotation.

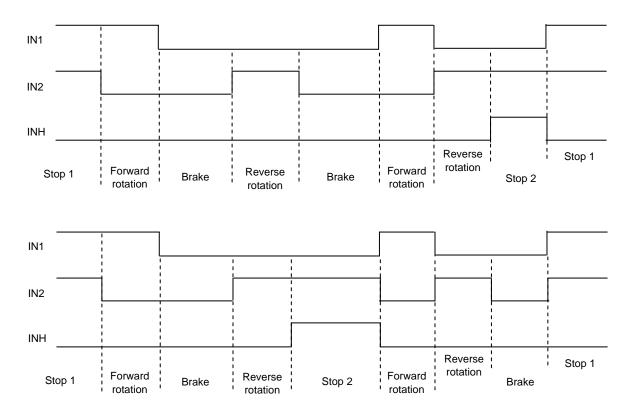
#### Notes

- (1) Be sure to set the capacitance of the power supply bypass capacitor, C1, so that the ripple current of the capacitor, which varies as motor current increases, falls within the allowed range.
- (2) Although the Vref2 pin is kept open, if connected to the GND or S.P pin, the overheating current control circuit ceases to function.
- (3) Fixed current chopping operations based on TR1 and TR2 are used for current control. The timing given below is used for OUT1 or OUT2 voltage output and for TR1 or TR2 collector current.



- (4) Do not connect the N.C pins shown in the internal block diagram or sample application circuit to a circuit pattern on the PCB.
- (5) This IC is not designed for use in an H-bridge driver for power supplies because the I/O response time of its GND side driving device, required when switching the motor rotational direction, is several scores of microseconds. It should be used only for DC motor drivers.

### (6) Sample Timing Diagram



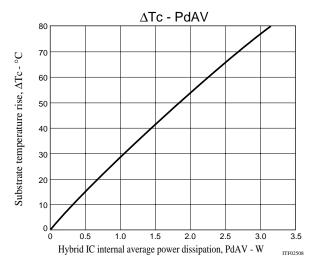
(7) Smoke Emission Precautions: There is a possibility of smoke emission if the hybrid IC is subjected to physical or electrical damage as the result of being used without compliance with the specifications.

### I/O Functions of Each Pin

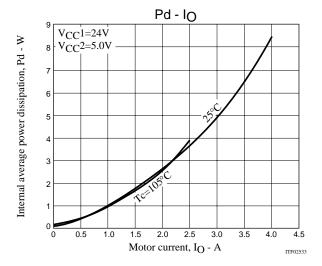
Pin Name	Pin No.	Function
IN1	12	Input pin for turning TR1 and F1 ON and OFF
		At high level, TR1: ON and F1: OFF; at low level TR1: OFF and F1: ON
IN2	13	Input pin for turning TR2 and F2 ON and OFF
		At high level, TR2: ON and F2: OFF; at low level, TR2: OFF and F2: ON
INH	14	Pin for turning TR1 and TR2 OFF; At high level TR1 and TR2: OFF
		This pin is usually low or open.
OUT1	8	This pin connects to the motor and outputs source/sync current depending on conditions at IN1 and IN2.
OUT2	6	This pin connects to the motor and outputs source/sync current depending on conditions at IN1 and IN2.
Vref1a	16	This pin is used for current setting for constant-current operation performed with the Vrefa and Vrefb pins
Vref1b	18	connected
		A voltage of 0.42V at Tc=25°C results for Vref1.
		0.42V is set by connecting $82k\Omega$ and $7.5k\Omega$ in series.
		Current detection resistance is Rs=0.10 $\Omega$ . Set using I <sub>O</sub> peak=Vref1÷Rs.
Vref2	17	Be sure to usually leave this pin open.
		The overheating control circuit can be made to stop operating by connecting this pin to the GND or S.P pin.
S.P	2	Vref1 voltage can be lowered by connecting a resistor between the Vref1 and S.P pins.
RSO	3	This pin is used to monitor the voltage across the current sensing resistor, Rs. Must be connected to the RSI
		pin.
RSI	19	This pin is connected to the RSO pin and serves as an input to the circuit that compares the input with Vref1.

### **Technical Information**

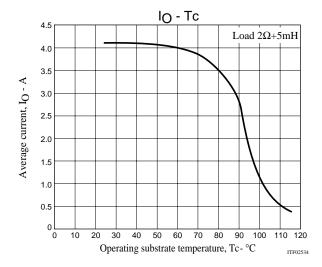
(1) Substrate temperature rise ,  $\Delta Tc$  (no heat sink) - Internal average power dissipation, PdAV



(2) Internal average power dissipation, Pd, in the DC current-motor current, IQ, characteristics (typ values for Pd)

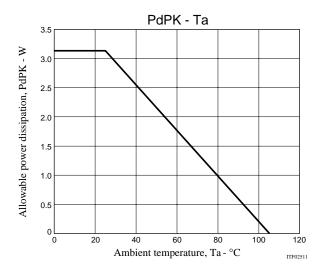


(3) Overheating current control characteristics

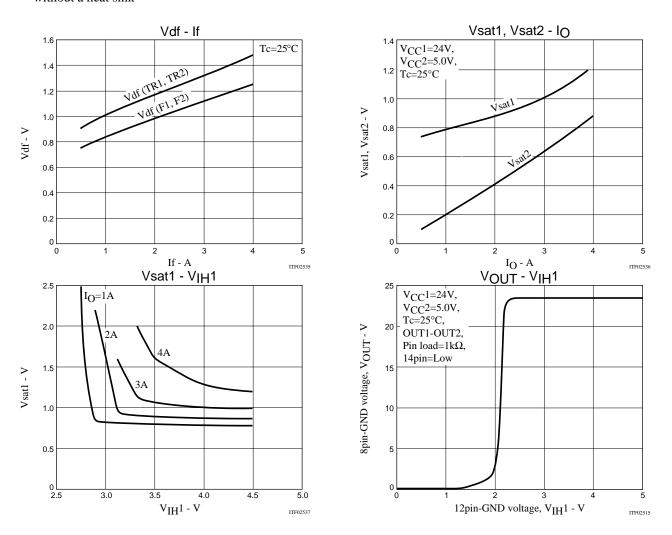


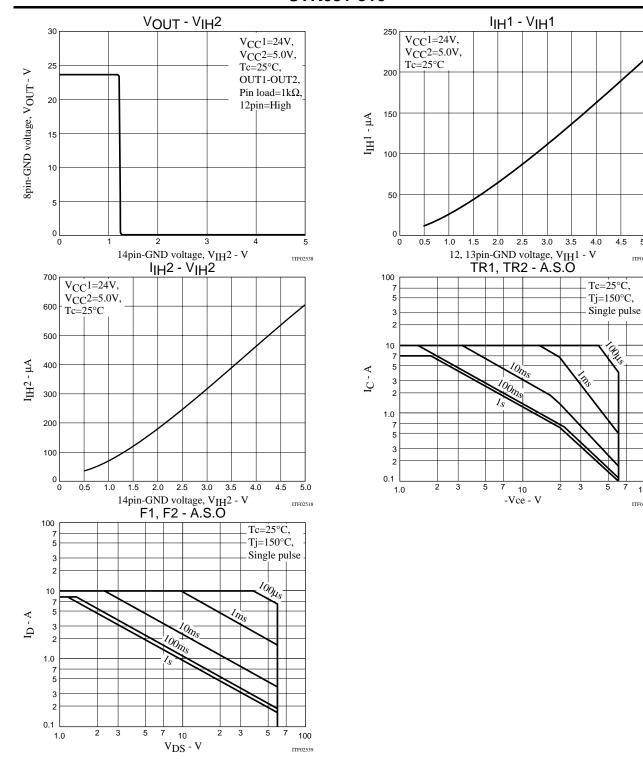
Overheating current control functions to prevent driver failure if a motor lock malfunction occurs.

(4) Derating curve of the package power dissipation, PdPK, against the ambient temperature, Ta



The package power dissipation, PdPK, refers to the internal average power dissipation, Pd, permissible if used without a heat sink





4.5 5.0

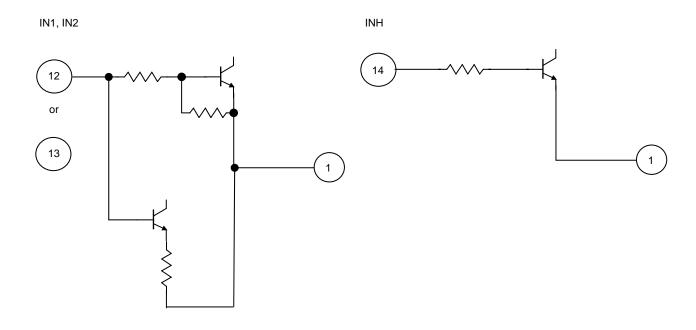
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100

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### **Input Pin Configurations**



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