# **Triple 3-Input AND Gate**

# High–Performance Silicon–Gate CMOS

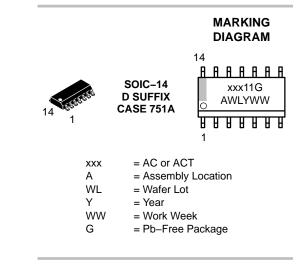
### Features

- Outputs Source/Sink 24 mA
- 'ACT11 Has TTL Compatible Inputs
- These are Pb–Free Devices



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#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

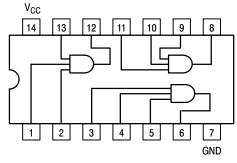


Figure 1. Pinout: 14–Lead Packages Conductors (Top View)

#### MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	DC Supply Voltage	-0.5 to +7.0	V
VI	DC Input Voltage	$-0.5 \leq V_{I} \leq V_{CC} + 0.5$	V
Vo	DC Output Voltage (Note 1)	$-0.5 \le V_O \le V_{CC} + 0.5$	V
I <sub>IK</sub>	DC Input Diode Current	±20	mA
I <sub>OK</sub>	DC Output Diode Current	±50	mA
I <sub>O</sub>	DC Output Sink/Source Current	±50	mA
I <sub>CC</sub>	DC Supply Current per Output Pin	±50	mA
I <sub>GND</sub>	DC Ground Current per Output Pin	±50	mA
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C
TL	Lead temperature, 1 mm from Case for 10 Seconds	260	°C
TJ	Junction temperature under Bias	+ 150	°C
$\theta_{JA}$	Thermal Resistance (Note 2)	125	°C/W
P <sub>D</sub>	Power Dissipation in Still Air at 85°C	125	mW
MSL	Moisture Sensitivity	Level 1	
F <sub>R</sub>	Flammability Rating Oxygen Index: 30% – 35%	UL 94 V-0 @ 0.125 in	
V <sub>ESD</sub>	ESD Withstand Voltage Human Body Model (Note 3) Machine Model (Note 4) Charged Device Model (Note 5)	> 2000 > 200 > 1000	V
I <sub>Latch-Up</sub>	Latch–Up Performance Above V <sub>CC</sub> and Below GND at 85°C (Note 6)	±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. I<sub>O</sub> absolute maximum rating must be observed.

2. The package thermal impedance is calculated in accordance with JESD51-7.

3. Tested to EIA/JESD22-A114-A.

4. Tested to EIA/JESD22-A115-A.

5. Tested to JESD22-C101-A.

6. Tested to EIA/JESD78.

#### **RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter	Parameter			Мах	Unit
	Oursels Malla an	'AC	2.0 5.0		6.0	N
V <sub>CC</sub>	Supply Voltage	′ACT	4.5	5.0	5.5	V
V <sub>in</sub> , V <sub>out</sub>	DC Input Voltage, Output Voltage (Ref. to GND)		0	-	V <sub>CC</sub>	V
			_	150	_	
t <sub>r</sub> , t <sub>f</sub>	Input Rise and Fall Time (Note 1) 'AC Devices except Schmitt Inputs	V <sub>CC</sub> @ 4.5 V	_	40	_	ns/V
		V <sub>CC</sub> @ 5.5 V	_	25	_	
	Input Rise and Fall Time (Note 2)	V <sub>CC</sub> @ 4.5 V	_	10	_	ns/V
t <sub>r</sub> , t <sub>f</sub>	'ACT Devices except Schmitt Inputs	V <sub>CC</sub> @ 5.5 V	_	8.0	_	115/ V
TJ	Junction Temperature (PDIP)		_	-	140	°C
T <sub>A</sub>	Operating Ambient Temperature Range		-40	25	85	°C
I <sub>OH</sub>	Output Current – High		-	-	-24	mA
I <sub>OL</sub>	Output Current – Low		_	-	24	mA

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability. 1.  $V_{in}$  from 30% to 70%  $V_{CC}$ ; see individual Data Sheets for devices that differ from the typical input rise and fall times. 2.  $V_{in}$  from 0.8 V to 2.0 V; see individual Data Sheets for devices that differ from the typical input rise and fall times.

#### **DC CHARACTERISTICS**

			74	AC	74AC		
Symbol	Parameter	V <sub>CC</sub> (V)	T <sub>A</sub> = -	+25°C	T <sub>A</sub> = –40°C to +85°C	Unit	Conditions
			Тур	Guar	anteed Limits		
V <sub>IH</sub>	Minimum High Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	2.1 3.15 3.85	2.1 3.15 3.85	v	$V_{OUT} = 0.1 V$ or $V_{CC} - 0.1 V$
V <sub>IL</sub>	Maximum Low Level Input Voltage	3.0 4.5 5.5	1.5 2.25 2.75	0.9 1.35 1.65	0.9 1.35 1.65	v	V <sub>OUT</sub> = 0.1 V or V <sub>CC</sub> – 0.1 V
V <sub>OH</sub>	Minimum High Level Output Voltage	3.0 4.5 5.5	2.99 4.49 5.49	2.9 4.4 5.4	2.9 4.4 5.4	v	l <sub>OUT</sub> = –50 μA
		3.0 4.5 5.5		2.56 3.86 4.86	2.46 3.76 4.76	V	$V_{IN} = V_{IL} \text{ or } V_{IH}$ -12 mA $I_{OH}$ -24 mA -24 mA
V <sub>OL</sub>	Maximum Low Level Output Voltage	3.0 4.5 5.5	0.002 0.001 0.001	0.1 0.1 0.1	0.1 0.1 0.1	v	l <sub>OUT</sub> = 50 μA
		3.0 4.5 5.5		0.36 0.36 0.36	0.44 0.44 0.44	V	$V_{IN} = V_{IL} \text{ or } V_{IH}$ $12 \text{ mA}$ $I_{OL}$ $24 \text{ mA}$ $24 \text{ mA}$
I <sub>IN</sub>	Maximum Input Leakage Current	5.5	-	±0.1	±1.0	μΑ	$V_{I} = V_{CC}, \text{ GND}$
I <sub>OLD</sub>	†Minimum Dynamic	5.5	-	-	75	mA	V <sub>OLD</sub> = 1.65 V Max
I <sub>OHD</sub>	Output Current	5.5	-	-	-75	mA	V <sub>OHD</sub> = 3.85 V Min
I <sub>CC</sub>	Maximum Quiescent Supply Current	5.5	_	4.0	40	μΑ	$V_{IN} = V_{CC}$ or GND

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. \*All outputs loaded; thresholds on input associated with output under test.

†Maximum test duration 2.0 ms, one output loaded at a time. NOTE:  $I_{IN}$  and  $I_{CC}$  @ 3.0 V are guaranteed to be less than or equal to the respective limit @ 5.5 V V<sub>CC</sub>.

#### **AC CHARACTERISTICS**

				74AC		74	AC		
Symbol	Parameter	V <sub>CC</sub> * (V)	T <sub>A</sub> = +25°C C <sub>L</sub> = 50 pF		T <sub>A</sub> = - to +8 C <sub>L</sub> = 5	35°C	Unit	Fig. No.	
			Min	Тур	Max	Min	Max		
t <sub>PLH</sub>	Propagation Delay	3.3 5.0	1.5 1.5	5.5 4.0	9.5 8.0	1.0 1.0	10.0 8.5	ns	3–5
t <sub>PHL</sub>	Propagation Delay	3.3 5.0	1.5 1.5	5.5 4.0	8.5 7.0	1.0 1.0	9.5 7.5	ns	3–5

\*Voltage Range 3.3 V is 3.3 V  $\pm$ 0.3 V.

Voltage Range 5.0 V is 5.0 V  $\pm 0.5$  V.

#### **DC CHARACTERISTICS**

			74/	CT	74ACT						
Symbol	Parameter	V <sub>CC</sub> (V)	T <sub>A</sub> = +25°C		T <sub>A</sub> = +25°C		T <sub>A</sub> = +25°C		T <sub>A</sub> = –40°C to +85°C	Unit	Conditions
			Тур	Typ Guaranteed Limits							
V <sub>IH</sub>	Minimum High Level Input Voltage	4.5 5.5	1.5 1.5	2.0 2.0	2.0 2.0	V	$V_{OUT} = 0.1 V$ or $V_{CC} - 0.1 V$				
V <sub>IL</sub>	Maximum Low Level Input Voltage	4.5 5.5	1.5 1.5	0.8 0.8	0.8 0.8	V	$V_{OUT} = 0.1 V$ or $V_{CC} - 0.1 V$				
V <sub>OH</sub>	Minimum High Level Output Voltage	4.5 5.5	4.49 5.49	4.4 5.4	4.4 5.4	V	I <sub>OUT</sub> = -50 μA				
		4.5 5.5		3.86 4.86	3.76 4.76	V	$V_{IN} = V_{IL} \text{ or } V_{IH}$ -24 mA $I_{OH}$ -24 mA				
V <sub>OL</sub>	Maximum Low Level Output Voltage	4.5 5.5	0.001 0.001	0.1 0.1	0.1 0.1	V	I <sub>OUT</sub> = 50 μA				
		4.5 5.5		0.36 0.36	0.44 0.44	V	$V_{IN} = V_{IL} \text{ or } V_{IH}$ 24 mA $I_{OL}$ 24 mA				
I <sub>IN</sub>	Maximum Input Leakage Current	5.5	-	±0.1	±1.0	μΑ	$V_{I} = V_{CC}, GND$				
$\Delta I_{CCT}$	Additional Max. I <sub>CC</sub> /Input	5.5	0.6	-	1.5	mA	$V_I = V_{CC} - 2.1 \text{ V}$				
I <sub>OLD</sub>	†Minimum Dynamic	5.5	-	-	75	mA	$V_{OLD}$ = 1.65 V Max				
I <sub>OHD</sub>	Output Current	5.5	-	-	-75	mA	V <sub>OHD</sub> = 3.85 V Min				
I <sub>CC</sub>	Maximum Quiescent Supply Current	5.5	-	4.0	40	μΑ	$V_{IN} = V_{CC}$ or GND				

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†Maximum test duration 2.0 ms, one output loaded at a time.

#### **AC CHARACTERISTICS**

		74ACT				74A	СТ		
Symbol	Parameter	V <sub>CC</sub> * (V)	T <sub>A</sub> = +25°C C <sub>L</sub> = 50 pF		T <sub>A</sub> = - to +8 C <sub>L</sub> = 9		Unit	Fig. No.	
			Min	Тур	Max	Min	Max		
t <sub>PLH</sub>	Propagation Delay	5.0	1.5	-	9.5	1.0	10.5	ns	3–5
t <sub>PHL</sub>	Propagation Delay	5.0	1.5	-	9.5	1.0	10.5	ns	3–5

\*Voltage Range 5.0 V is 5.0 V  $\pm$ 0.5 V.

#### CAPACITANCE

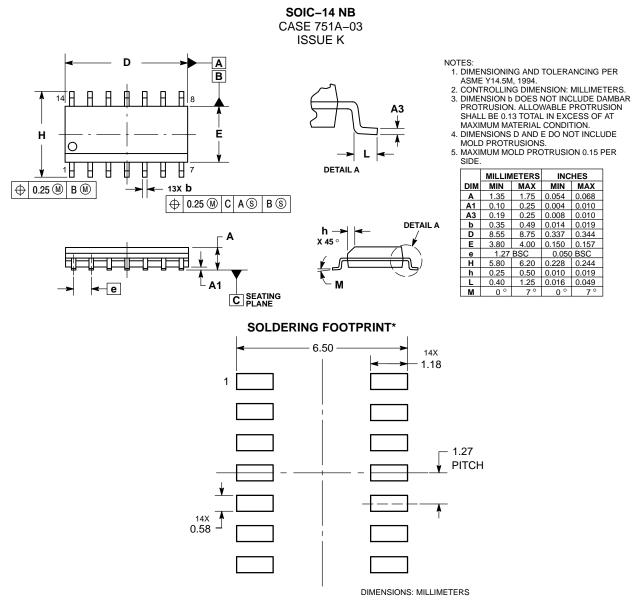
Symbol	Parameter	Value Typ	Unit	Test Conditions
C <sub>IN</sub>	Input Capacitance	4.5	pF	V <sub>CC</sub> = 5.0 V
C <sub>PD</sub>	Power Dissipation Capacitance	20	pF	V <sub>CC</sub> = 5.0 V

**ORDERING INFORMATION** 

Device	Package	Shipping <sup>†</sup>
MC74AC11DG	SOIC-14 (Pb-Free)	55 Units/Rail
MC74AC11DR2G	SOIC-14 (Pb-Free)	2500/Tape & Reel
MC74ACT11DR2G	SOIC-14 (Pb-Free)	2500/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### PACKAGE DIMENSIONS



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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