## MSB710-RT1

Preferred Device

# **PNP General Purpose Amplifier Transistor Surface Mount**

#### **Features**

• Pb-Free Package is Available

#### **MAXIMUM RATINGS** $(T_A = 25^{\circ}C)$

Rating	Symbol	Value	Unit
Collector-Base Voltage	V <sub>(BR)CBO</sub>	-60	Vdc
Collector-Emitter Voltage	V <sub>(BR)CEO</sub>	-50	Vdc
Emitter-Base Voltage	V <sub>(BR)EBO</sub>	-7.0	Vdc
Collector Current – Continuous	Ic	-500	mAdc
Collector Current – Peak	I <sub>C(P)</sub>	-1.0	Adc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Power Dissipation	P <sub>D</sub>	200	mW
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C)

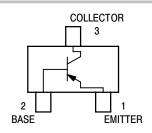
Characteristic	Symbol	Min	Max	Unit
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = -10 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	-50	ı	Vdc
Collector–Base Breakdown Voltage ( $I_C = -10 \mu Adc, I_E = 0$ )	V <sub>(BR)CBO</sub>	-60	ı	Vdc
Emitter–Base Breakdown Voltage ( $I_E = -10 \mu Adc, I_C = 0$ )	V <sub>(BR)EBO</sub>	-7.0	ı	Vdc
Collector–Base Cutoff Current $(V_{CB} = -20 \text{ Vdc}, I_E = 0)$	I <sub>CBO</sub>	1	-0.1	μAdc
DC Current Gain (Note 1) ( $V_{CE} = -10 \text{ Vdc}$ , $I_{C} = -150 \text{ mAdc}$ ) ( $V_{CE} = -10 \text{ Vdc}$ , $I_{C} = 500 \text{ mAdc}$ )	h <sub>FE1</sub> h <sub>FE2</sub>	120 40	240 –	-
Collector–Emitter Saturation Voltage ( $I_C = -300 \text{ mAdc}$ , $I_B = -30 \text{ mAdc}$ )	V <sub>CE(sat)</sub>	ı	-0.6	Vdc
Collector–Base Saturation Voltage (I <sub>C</sub> = -300 mAdc, I <sub>B</sub> = -30 mAdc)	V <sub>BE(sat)</sub>	ı	-1.5	Vdc
Output Capacitance (V <sub>CB</sub> = -10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	_	15	pF

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, D.C.  $\leq$  2%.



## ON Semiconductor®

#### http://onsemi.com





SC-59 **CASE 318D** 

#### **MARKING DIAGRAM**



CR = Device Code = Date Code\* = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation may vary depending upon manufacturing location.

#### ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
MSB710-RT1	SC-59	3000 / Tape & Reel
MSB710-RT1G	SC-59 (Pb-Free)	3000 / Tape & Reel

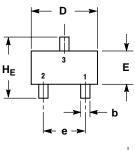
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

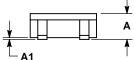
Preferred devices are recommended choices for future use and best overall value

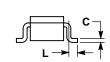
#### MSB710-RT1

#### PACKAGE DIMENSIONS

SC-59 CASE 318D-04 ISSUE G





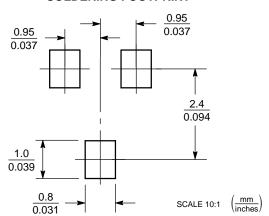


#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982
- 2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.00	1.15	1.30	0.039	0.045	0.051
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.35	0.43	0.50	0.014	0.017	0.020
С	0.09	0.14	0.18	0.003	0.005	0.007
D	2.70	2.90	3.10	0.106	0.114	0.122
E	1.30	1.50	1.70	0.051	0.059	0.067
е	1.70	1.90	2.10	0.067	0.075	0.083
L	0.20	0.40	0.60	0.008	0.016	0.024
HE	2.50	2.80	3.00	0.099	0.110	0.118

#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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