# SBS817



# Applications

• High frequency rectification (switching regulators, converters, choppers)

## Features

- · Low switching noise
- Low forward voltage (IF=2.0A, VF max=0.46V)
- Ultrasmall package permitting applied sets to be small and slim (Mounting height 0.75mm)

# Specifications

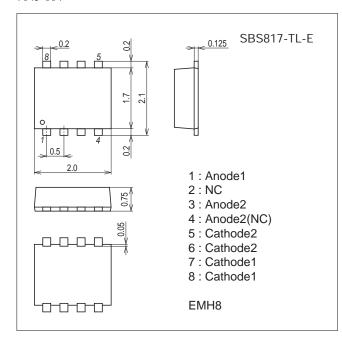
#### Absolute Maximum Ratings at Ta=25°C (Value per element)

Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>		15	V
Nonrepetitive Peak Reverse Surge Voltage	V <sub>RSM</sub>		15	V
Average Output Current	IO		2.0	А
Surge Forward Current	IFSM	50Hz sine wave, 1 cycle	20	А
Junction Temperature	Tj		-55 to +125	°C
Storage Temperature	Tstg		-55 to +125	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

## Package Dimensions

unit : mm (typ) 7045-004



## Product & Package Information

- Package
- JEITA, JEDEC

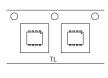
• Minimum Packing Quantity : 3,000 pcs./reel

: EMH8

Marking

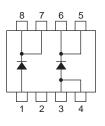
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#### Packing Type : TL





#### **Electrical Connection**



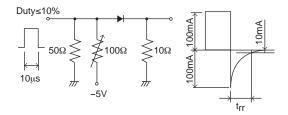
ON Semiconductor® http://onsemi.com

#### Electrical Characteristics at Ta=25°C (Value per element)

Parameter	Symbol	Conditions	Ratings			Unit	
Faianietei	Symbol	Conditions	min	typ	max	Unit	
Reverse Voltage	VR	IR=1mA	15			V	
Forward Voltage	V <sub>F</sub> 1	IF=1.0A		0.33	0.39	V	
	V <sub>F</sub> 2	IF=2.0A		0.39	0.46	V	
Reverse Current	IR	V <sub>R</sub> =7.5V			300	μΑ	
Interterminal Capacitance	С	VR=10V, f-1MHz		35		pF	
Reverse Recovery Time	t <sub>rr</sub>	IF=IR=100mA, See specified Test Circuit.			10	ns	
Thermal Resistance	Rth(j-a)	When mounted on ceramic substrate (900mm <sup>2</sup> ×0.8mm)		65		°C / W	

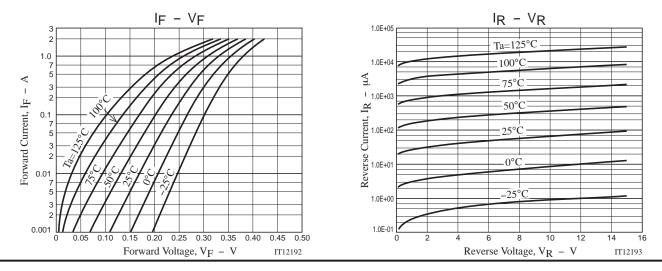
\*: The absolute maximum ratings and electrical characteristics refer to those between Terminal 1 and Terminal 7 (or 8), and between Terminal 3 and Terminal 5 (or 6).

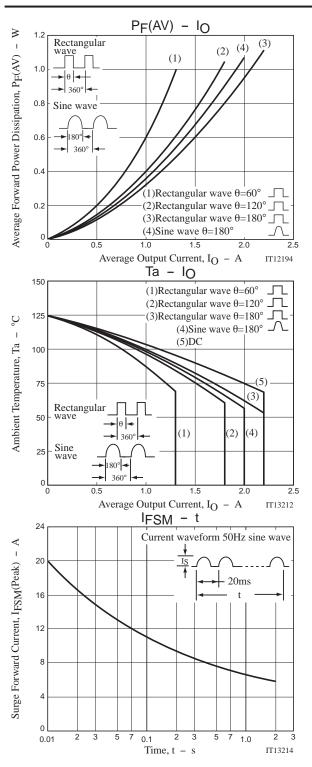
## trr Test Circuit

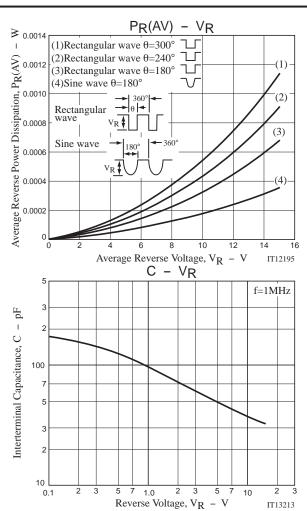


### **Ordering Information**

Device	Package	Shipping	memo		
SBS817-TL-E	EMH8	3,000pcs./reel	Pb Free		







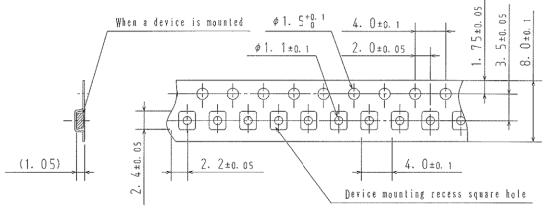
## Embossed Taping Specification SBS817-TL-E

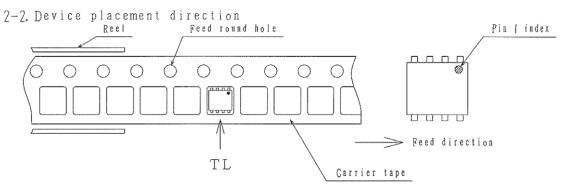
1. Packing Format

Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing format	
	Туре	Reel	Inner box	Quier box	Inner BOX (C-1) Outer BOX (A-7)	
EMH8	MCP4	3,000	15,000	90,000	S reels contained 6 inner boxes contained	
					Dinensions:mm (external) Dimensions:mm (external)	
					183×72×185 440×195×210	
Reel label, [nner box label Outer box label						
Packing method (unit:mm) It is a label at the time of factory shipm The form of a label may change in physical distribution process.						
0			¥		59 108	
	Type LOT Quan Orig	No. tity		TO LOT OO II 11 III III III III IIIIII GTY O, OO II 11 IIII IIIIII SPECTAL # 207221	MILLING INF INFINITION Image: Constraint of the image: Constraintof the image: Constraint of the image: Constraintof the image: Con	
Reel label				e LEAD FI	L ASSEMELY:****(DIFFUSION:****) REE & description shows that the surface of the terminal is lead free.	
				Label LEAD FRE LEAD FRE	E 3 JEITA Phase 3A	

# 2. Taping configuration

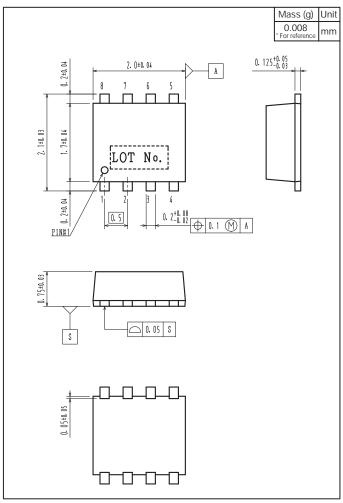
2-1. Carrier tape size (unit:mm)



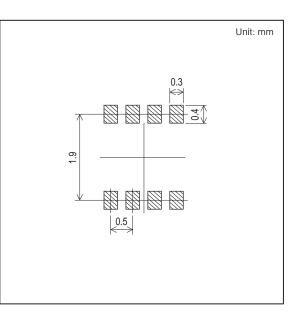


Those with pin 1 index on the feed hole side ..... TL

# Outline Drawing SBS817-TL-E



# Land Pattern Example



**SBS817** 

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