# SCH1330

# Power MOSFET -20V, 241mΩ, -1.5A, Single P-Channel

This low-profile high-power MOSFET is produced using ON Semiconductor's trench technology, which is specifically designed to minimize gate charge and ultra low on resistance. This device is suitable for applications with low gate charge driving or ultra low on resistance requirements.

## Features

- Low On-Resistance
- High Speed Switching
- 1.8V drive
- ESD Diode-Protected Gate
- Pb-Free, Halogen Free and RoHS compliance
- Ultra small package SCH6 (1.6mm×1.6mm×0.56mmt)

## **Typical Applications**

- Battery Switch
- Load Switch

## SPECIFICATIONS

**ABSOLUTE MAXIMUM RATING** at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	VDSS	-20	V
Gate to Source Voltage	VGSS	±10	V
Drain Current (DC)	ID	-1.5	Α
Drain Current (Pulse) PW $\leq 10\mu$ s, duty cycle $\leq 1\%$	IDP	-6	А
Power Dissipation When mounted on ceramic substrate $(900mm^2 \times 0.8mm)$	PD	1	w
Junction Temperature	Tj	150	°C
Storage Temperature	Tstg	–55 to +150	°C

Note 1 : Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate (900mm <sup>2</sup> $\times$ 0.8mm)	R <sub>θJA</sub>	125	°C/W

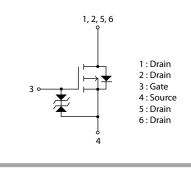


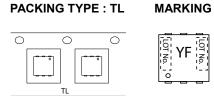
# **ON Semiconductor®**

www.onsemi.com

-		
VDSS	R <sub>DS</sub> (on) Max	ID Max
	241mΩ@ –4.5V	
-20V	385mΩ@ –2.5V	-1.5A
	615mΩ@ –1.8V	

#### ELECTRICAL CONNECTION P-Channel





ORDERING INFORMATION

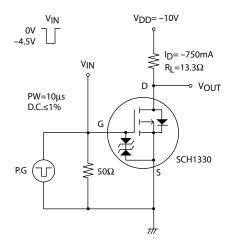
See detailed ordering and shipping information on page 5 of this data sheet.

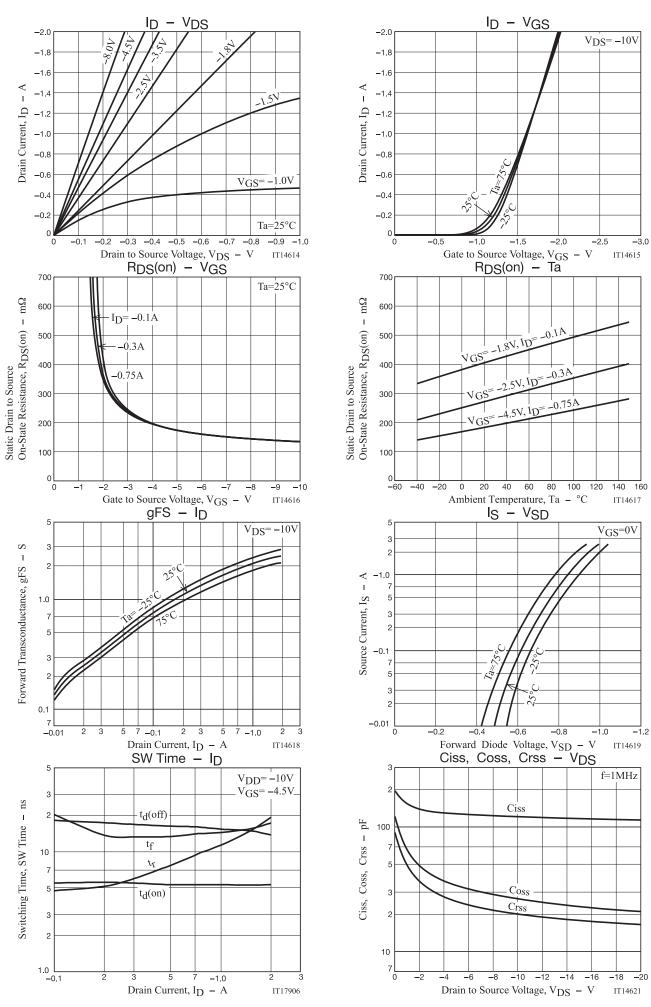
Decemeter	Cumphiel		Value			1.1
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0V	-20			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V			-1	μA
Gate to Source Leakage Current	IGSS	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V			±10	μA
Gate Threshold Voltage	VGS(th)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-0.4		-1.4	V
Forward Transconductance	9FS	V <sub>DS</sub> =-10V, I <sub>D</sub> =-750mA	1.14	1.9		S
	R <sub>DS</sub> (on)1	ID=-750mA, VGS=-4.5V		185	241	mΩ
Static Drain to Source On-State	R <sub>DS</sub> (on)2	ID=-300mA, VGS=-2.5V		275	385	mΩ
Resistance	R <sub>DS</sub> (on)3	ID=-100mA, VGS=-1.8V		410	615	mΩ
Input Capacitance	Ciss			120		pF
Output Capacitance	Coss	V <sub>DS</sub> =–10V, f=1MHz		26		pF
Reverse Transfer Capacitance	Crss			20		pF
Turn-ON Delay Time	t <sub>d</sub> (on)			5.3		ns
Rise Time	tr			9.7		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		16		ns
Fall Time	tf			14		ns
Total Gate Charge	Qg			1.7		nC
Gate to Source Charge	Qgs	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1.5A		0.28		nC
Gate to Drain "Miller" Charge	Qgd			0.47		nC
Forward Diode Voltage	VSD	IS=-1.5A, VGS=0V		-0.89	-1.2	V

## **ELECTRICAL CHARACTERISTICS** at Ta = 25°C (Note 2)

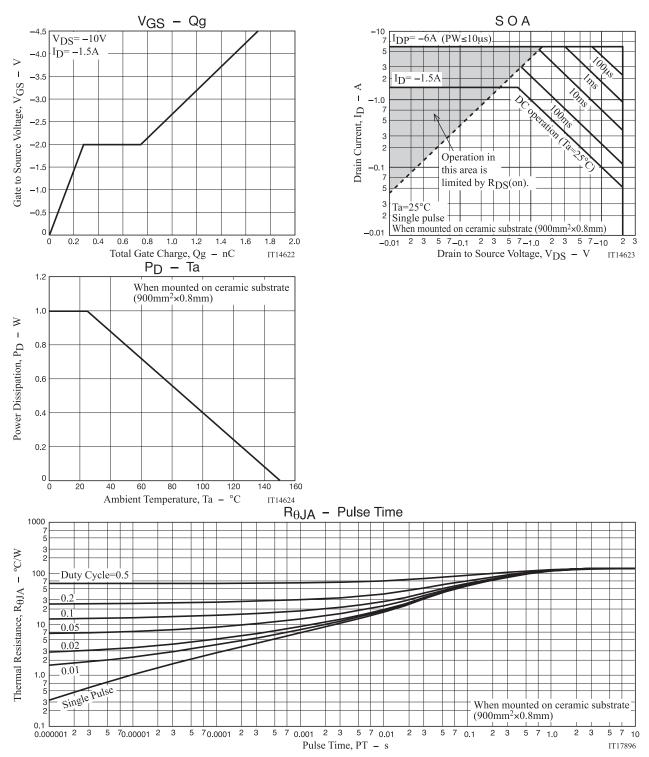
Note 2 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## Switching Time Test Circuit





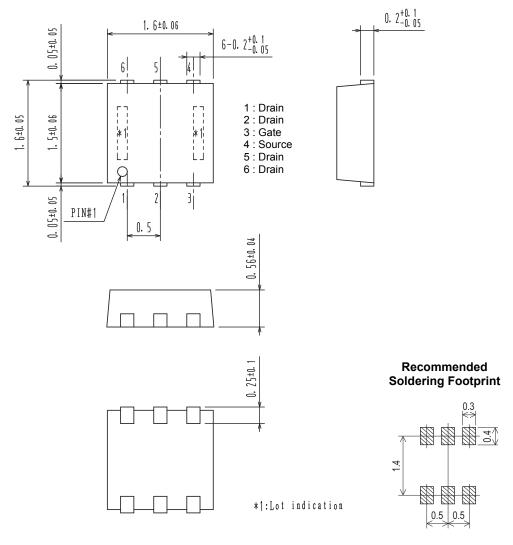
www.onsemi.com 3



## PACKAGE DIMENSIONS

unit : mm

SOT-563 / SCH6 CASE 463AB ISSUE O



#### **ORDERING INFORMATION**

Device	Marking	Package	Shipping (Qty / Packing)				
SCH1330-TL-H	YF	SOT-563 / SCH6	5,000 / Tape & Reel				
SCH1330-TL-W	ĨF	(Pb-Free / Halogen Free)					

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub\_link/Collateral/BRD8011-D.PDF

Note on usage : Since the SCH1330 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly, or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC and its officers, negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable co