

KSD362

B/W TV Horizontal Deflection Output

- Collector-Base Voltage : V_{CBO}=150V
- Collector Current : I_C=5A
 Collector Dissipation : P_C=40W(T_C=25°C)



1.Base 2.Collector 3.Emitter

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	150	V
V _{CEO}	Collector-Emitter Voltage	70	V
V _{EBO}	Emitter-Base Voltage	8	V
I _C	Collector Current	5	Α
P _C	Collector Dissipation (T _C =25°C)	40	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{C} = 1 \text{mA}, I_{E} = 0$	150			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 2mA, R_{BE} = \infty$	70			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 1 \text{mA}, I_C = 0$	8			V
I _{CBO}	Collector Cut-off Current	$V_{CB} = 100 V, I_{E} = 0$			20	μΑ
h _{FE}	DC Current Gain	$V_{CE} = 5V, I_{C} = 5A$	20		140	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 5A, I_B = 0.5A$			1	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_C = 5A, I_B = 0.5A$			1.5	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 5V, I_{C} = 0.5A$		10		MHz

\mathbf{h}_{FE} Classification

Classification	N	R	0
h _{FE}	20 ~ 50	40 ~ 80	70 ~ 140

Typical Characteristics

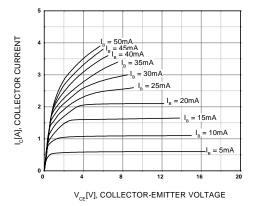


Figure 1. Static Characteristic

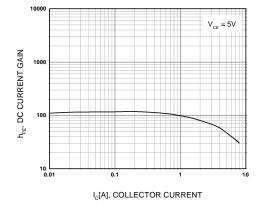


Figure 2. DC current Gain

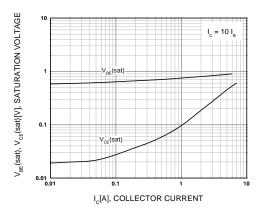


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

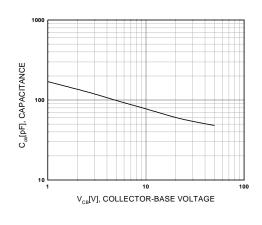


Figure 4. Collector Output Capacitance

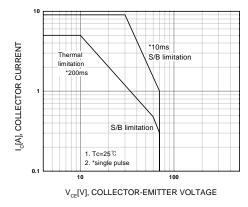


Figure 5. Safe Operating Area

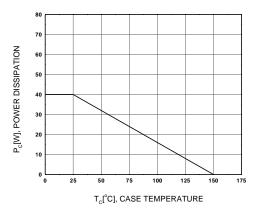
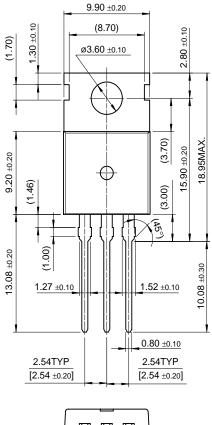


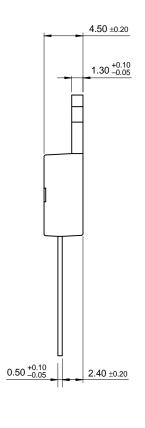
Figure 6. Power Derating

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Package Demensions

TO-220





10.00 ±0.20

Dimensions in Millimeters

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