

L9639

FOLDER ACTUATOR

- BIDIRECTIONAL SWITCH
- MOTOR STOP CONTROLLED BY MOTOR CURRENT
- START UP AND END CURRENT DETEC-TION THRESHOLDS PROGRAMMABLE WITH EXTERNAL RSHUNT
- STOP DELAY TIME FOR START UP AND END PHASE PROGRAMMABLE WITH EX-TERNAL RC
- OUTPUT SHORT CIRCUIT PROTECTION
- OUTPUT CURRENT LIMITING > 8A
- THERMAL PROTECTION
- ACTIVE DIODE BRIDGE INTERNALLY DIF-FUSED
- MAXIMUM VOLTAGE SUPPLY 50V

BLOCK DIAGRAM



■ TECHI CLOGY MULTIPOWER BCD60II



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L9639

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{bat}	Supply Voltage	50	V
l _{out}	Output current DC at Is	5 6	A A
T _{op}	Operating Temperature	-40 to 125	°C

PIN CONNECTION



THERMAL DATA

Symbol	Parameter	Value	Unit
R _{th j-case}	Thermal Resistance Junction to case Max.	4	°C/W

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Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Unit	
V _{bat}	Power supply		8		17	V	
I _q	Quiescent current	Stanby		100	200	μA	
I _{ref}	Current ref. threshold for start and end current (note 1)	V _{bat} = 8 to 17V	70	100	135	μA	
I _{refcc}	Current ref. threshold for short circuit current det (note 2)	Vbat = 8V V _{bat} = 12V V _{bat} = 17V	240 300 400	300 440 550	400 560 690	μΑ	
I _{sc1}	Current short circuit limit		8			А	
T _{dsc}	Short circuit time delay	I _{load} > I _{thcc}			100	μs	
R _{on}	Ron output power DMOS	$I_{load} = 1.2A$		0.6	1	Oı`m	
R _{ona}	Ron active bridge DMOS	$V_{bat} = 8V, I_{gnd} = 1A$			0.7	Ohm	
R _{load}	Open load detector max. load resistance				200	Ohm	
K _{td}	Delay constant (note 3)			60		1	
Deb	Immunity debouncer switch	V _{bat} = pulse				5	
Note 1: $I_{th} = \frac{R_{ref}}{R_{shunt}} I_{ref}$ Note 2: $I_{thcc} = \frac{R_{ref}}{R_{shunt}} \cdot I_{refoc}$							

ELECTRICAL CHARACTERISTICS (Refer to the test circuit, unless otherwise specified.)

Note 1: I_{th} =
$$\frac{R_ref}{R_{shunt}}$$
 I_{ref}
Note 2: I_{thcc} = $\frac{R_ref}{R_{shunt}} \cdot$ I_{refcc}
Note 3: T_d = In $\left(\frac{V_{th_high}}{V_{th_low}}\right)$ · Rex · Cex = Rex · Cex · Ktd

DESCRIPTION

This device typically drives a direct current motor servomechanism providing two extreme and positions and replaces end position switches or sensors.

For more details see the Ting diagram (Fig. 1) and the Application diagram (Fig. 2)

When the power supply is applied, or its polarity is inverted the motor is powered up (start point). The current of the motor reaches the start up value near to the stall current, always higher than the threshold value of the device (ITH) . A delay on the detection (T_D) permits the motor start up and the consequent decrease of the current.

During the free running phase, the current in the motor must always be lower the threshold ITH. When the motor reaches the end of the run limit, the current increases reaching a value that, depending on the application, can be the stall value or can depend on some torque limiting friction (end point). Provided that this value is higher the programmed threshold ITH, the motor is stopped after a time delay T_D , and the device goes into a low consumption standby status, ready to restart the motor for a new cycle if the polarity of the power supply is inverted (or power is switched off and on).

In any case, if the current exceeds the higher threshold I_{THCC}, the motor is immediately stopped because a short circuit is detected. The delay TD also permits the motor to overcome some small obstacle during the free run.

The threshold current for the running phase ITH is obtained by comparing the voltage on an external sensing resistor (R_{SHUNT}) to a threshold voltage Vтн.

1) ITH = VTH / RSHUNT

V_{TH} is constant in respect to the power supply voltage because in most applications, the end of run current is depending only on motor and the mechanic torque limiting device (friction current).

The threshold current for the short circuit detection is:

and depends intentionally on the supply voltage because of the same dependence of the stall current. The time T_D depends on two external components, capacitor C_{EX} and resistor R_{EX}. TD is obtained by the following expression :

 $T_D = R_{EX} \cdot R_{EX} \cdot K_{TD}$

K_{TD} is a constant typically of unit value.

The block diagram is shown on the first page. The change of the polarity between pins COM-MON and LIVE, needs the active bridge to supply the internal circuit. The internal supply voltage is available between pins V+ and GND and a storage and filter capacitor (100nF) must be connected between these pins. The output stage

consists of two DMOS transistors connected in series with common drain to act as a switch with the voltage applied in both direction. A charge pump takes the gates of the DMOS above the supply voltage. The motor is controlled by the control 'logic low voltage' block that receives the motor status for the comparator. The 'charge C_{EX} ' block controls the T_D delay.

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Figure 1. Functional timing diagram.







All the necessary external components are shown in the application diagram. It is important to shunt the motor with a device that limits the maximum over voltage to 40V. This is necessary when the power supply circuit is opened on motor power up. In this case the back E.M.F. must be clamped because there is no other free wheel current path.

DIM	mm			inch			
DINI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
А			3.6			0.142	
a1	0.1		0.3	0.004		0.012	
a2			3.3			0.130	
a3	0		0.1	0.000		0.004	
b	0.4		0.53	0.016		0.021	
С	0.23		0.32	0.009		0.013	
D (1)	15.8		16	0.622		0.630	
D1	9.4		9.8	0.370		0.386	
E	13.9		14.5	0.547		0.570	
е		1.27			0.050		
e3		11.43			0.450		
E1 (1)	10.9		11.1	0.429		0.437	
E2			2.9			0.114	
E3	5.8		6.2	0.228		0.244	
G	0		0.1	0.000		0.004	
Н	15.5		15.9	0.610		0.626	
h			1.1			0.043	
L	0.8		1.1	0.031		0.043	
N	10° (max.)						
S	8° (max.)						
Т		10			0.394		

OUTLINE AND MECHANICAL DATA



(1) "D and F" do not include mold flash or protrusions.
Mold flash or protrusions shall not exceed 0.15 mm (0.006").
Critical dimensions: "E", "G" and "a3"



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