

## High voltage fast-switching NPN power transistor

### **Features**

- High voltage capability
- Low spread of dynamic parameters
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed
- Integrated antiparallel collector-emitter diode

### **Applications**

■ Electronic ballast for fluorescent lighting

### **Description**

The device is manufactured using high voltage multi-epitaxial planar technology for high switching speeds and medium voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

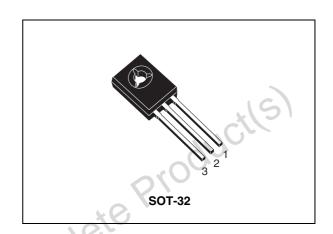


Figure 1. Internal schematic diagram

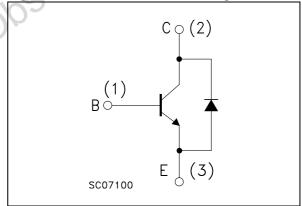


Table 1. Device summary

Order code	Marking	Package	Packaging
ST13003D-K	13003D	SOT-32	Bag

Electrical ratings ST13003D-K

# 1 Electrical ratings

Table 2. Absolute maximum ratings

\/	Parameter	Value	Unit
$V_{CES}$	Collector-emitter voltage (V <sub>BE</sub> = 0)	700	V
V <sub>CEO</sub>	Collector-emitter voltage (I <sub>B</sub> = 0)	400	V
V <sub>EBO</sub>	Emitter-base voltage ( $I_C = 0$ , $I_B = 0.75$ A, $t_P < 10 \mu s$ )	V <sub>(BR)EBO</sub>	V
I <sub>C</sub> Collector current		1.5	Α
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5 ms)	3	Α
Ι <sub>Β</sub>	Base current	0.75	Α
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 5 ms)	1.5	Α
P <sub>TOT</sub>	Total dissipation at T <sub>c</sub> = 25 °C	40	W
T <sub>STG</sub>	Storage temperature	-55 to 150	°C
TJ	Max. operating junction temperature	150	°C
	Max. operating junction temperature		

#### **Electrical characteristics** 2

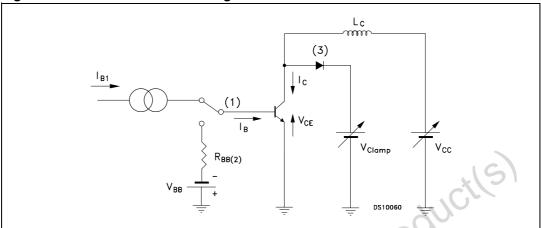
T<sub>case</sub> = 25 °C unless otherwise specified

**Electrical characteristics** Table 3.

1	Parameter	Test condition	ns	Min.	Тур.	Max.	Unit
ICES	Collector cut-off current (V <sub>BE</sub> = 0)	V <sub>CE</sub> = 700 V V <sub>CE</sub> = 700 V T <sub>c</sub> = 7	125 °C			1 5	mA mA
V <sub>(BR)EBO</sub>	Emitter-Base breakdown voltage (I <sub>C</sub> = 0)	I <sub>E</sub> = 10 mA		9		18	٧
V <sub>CEO(sus)</sub> (1)	Collector-emitter sustaining voltage (I <sub>B</sub> = 0)	I <sub>C</sub> = 10 mA		400		11/9	V
V <sub>CE(sat)</sub> (1)	Collector-emitter saturation voltage	I <sub>C</sub> = 1 A I <sub>B</sub> =	0.1 A 0.25 A 0.5 A	Q)	90,	0.5 1 3	V V V
V <sub>BE(sat)</sub> (1)	Base-emitter saturation voltage	- 0	0.1 A 0.25 A			1 1.2	V V
h <sub>FE</sub>	DC current gain	0	= 2 V = = 2 V	8 5		20 25	
t <sub>r</sub> t <sub>s</sub>	Resistive load Rise time Storage time Fall time	$V_{CC} = 125 \text{ V}$ $I_{C} = I_{B1} = 0.2 \text{ A}$ $I_{B2} = I_{p} = 25  \mu\text{s}$	1 A = -0.2 A			1 4 0.7	μs μs μs
t <sub>s</sub>	Inductive load Storage time		= 0.2 A 50 mH		0.8		μs
$V_{F}$	Diode forward voltage	$I_F = 0.5 A$				1.5	V

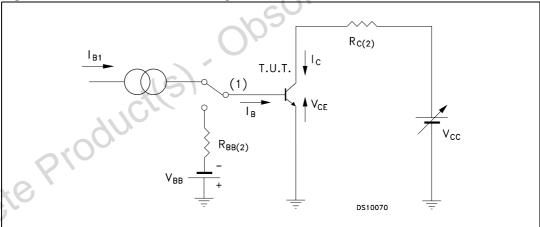
### 2.1 Test circuits

Figure 2. Inductive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor
- 3. Fast recovery rectifier

Figure 3. Resistive load switching test circuit



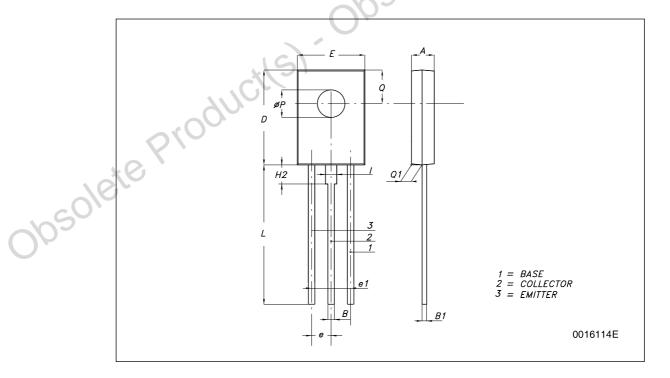
- 1. Fast electronic switch
- 2. Non-inductive resistor

## 3 Package mechanical data

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Obsolete Product(s). Obsolete Product(s)

DIM		mm.				
DIM.	MIN.	TYP	MAX.			
А	2.4		2.9			
В	0.64		0.88			
B1	0.39		0.63			
D	10.5		11.05			
E	7.4		7.8			
е	2.04	2.29	2.54			
e1	4.07	4.58	5.08			
L	15.3		16			
Р	2.9		3.2			
Q		3.8	400			
Q1	1		1.52			
H2		2.15				
I		1.27				



ST13003D-K Revision history

## 4 Revision history

Table 4. Document revision history

Date	Revision	Changes	
15-Nov-2007	1	Initial release.	
08-Sep-2009	2	Updated packaging information <i>Table 1 on page 1</i> .	



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