

NPN power Darlington transistor

Features

- Good h_{FE} linearity
- High f_T frequency
- Monolithic Darlington configuration with integrated antiparallel collector-emitter diode

Application

■ Linear and switching industrial equipment

Description

The device is manufactured in planar technology with "base island" layout and monolithic Darlington configuration.

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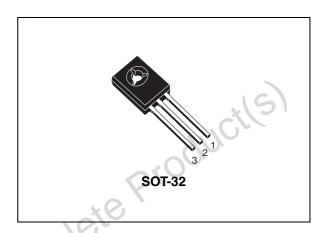


Figure 1. Internal schematic diagram

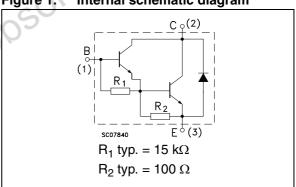


Table 1. Device summary

Order code	Marking	Package	Packaging
MJE802	MJE802	SOT-32	Tube

1 Absolute maximum ratings

Table 2. Absolute maximum ratings

V _{CBO} V _{CEO} V _{EBO} I _C	Collector-base voltage ($I_E = 0$) Collector-emitter voltage ($I_B = 0$) Emitter-base voltage ($I_C = 0$)	80 5	V
V _{EBO}	Emitter-base voltage (I _C = 0)		
	-	5	V
I _C			•
	Collector current	4	Α
I _{CM}	Collector peak current	8	Α
I _B	Base current	0.1	A
P _{TOT}	Total dissipation at T _{case} = 25 °C	40	W
T _{STG}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C
e Pr	oduci(s)		
	I _B	I _B Base current P _{TOT} Total dissipation at T _{case} = 25 °C T _{case} Storage temperature	I_{B} Base current 0.1 P_{TOT} Total dissipation at $T_{case} = 25 ^{\circ}\text{C}$ 40 T_{STG} Storage temperature -65 to 150

2 Electrical characteristics

 T_{case} = 25 °C; unless otherwise specified.

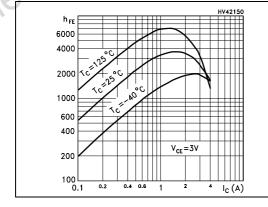
Table 3. Electrical characteristics

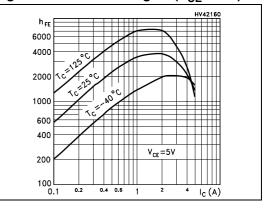
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E = 0)	V _{CB} = 80 V V _{CB} = 80 V, T _C = 125 °C		1	0.1 0.5	mA
I _{CEO}	Collector cut-off current (I _B = 0)	V _{CE} = 80 V		ı	0.1	mA
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = 5 V			2	mA
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage (I _B = 0)	I _C = 50 mA	80	33		٧
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	$I_C = 1.5 \text{ A}$ $I_B = 30 \text{ mA}$ $I_C = 4 \text{ A}$ $I_B = 40 \text{ mA}$		-	2.5	V
V _{BE(on)}	Base-emitter on voltage	$I_C = 1.5 \text{ A}$ $V_{CE} = 3 \text{ V}$ $I_C = 4 \text{ A}$ $V_{CE} = 3 \text{ V}$		-	2.5	٧
h _{FE} ⁽¹⁾	DC current gain	$I_C = 1.5 \text{ A}$ $V_{CE} = 3 \text{ V}$ $I_C = 4 \text{ A}$ $V_{CE} = 3 \text{ V}$	750 100	-		
h _{fe}	Small signal current gain	I _C = 1.5 A V _{CE} = 3 V f = 1 MHz	1	-		

^{1.} Pulse test: pulse duration $300 \le \mu s$, duty cycle ≤ 2 %.

2.1 Typical characteristic (curves)

Figure 2. DC current gain ($V_{CE} = 3 \text{ V}$) Figure 3. DC current gain ($V_{CE} = 5 \text{ V}$)

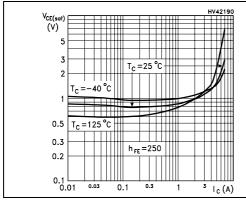




Electrical characteristics MJE802

Figure 4. Collector-emitter saturation voltage

Figure 5. Base-emitter saturation voltage



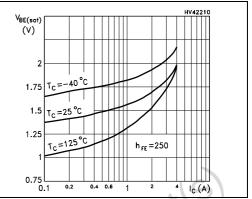
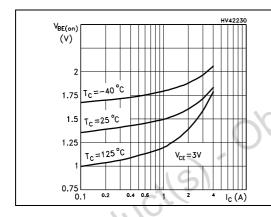


Figure 6. Base-emitter on voltage

Figure 7. Resistive load switching time (on)



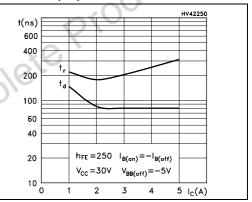
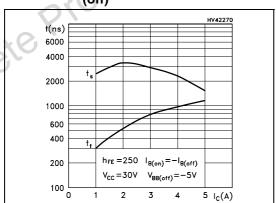


Figure 8. Resistive load switching time (off)



3 Package mechanical data

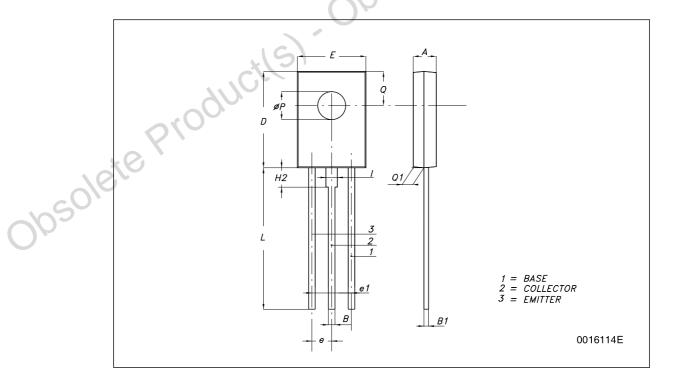
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SOT-32 (TO-126) MECHANICAL DATA

DIM.	mm.			
DIWI.	MIN.	TYP	MAX.	
Α	2.4		2.9	
В	0.64		0.88	
B1	0.39		0.63	
D	10.5		11.05	
E	7.4		7.8	
е	2.04	2.29	2.54	
e1	4.07	4.58	5.08	
L	15.3		16	
Р	2.9		3.2	
Q		3.8	(0-	
Q1	1		1.52	
H2		2.15		
1		1.27		



MJE802 Revision history

4 Revision history

Table 4. Document revision history

Date	Revision	Changes
21-Jun-2004	3	Document migration, no content change.
28-Aug-2009	4	Modified SOT-32 mechanical data.

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