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LOC	DIST
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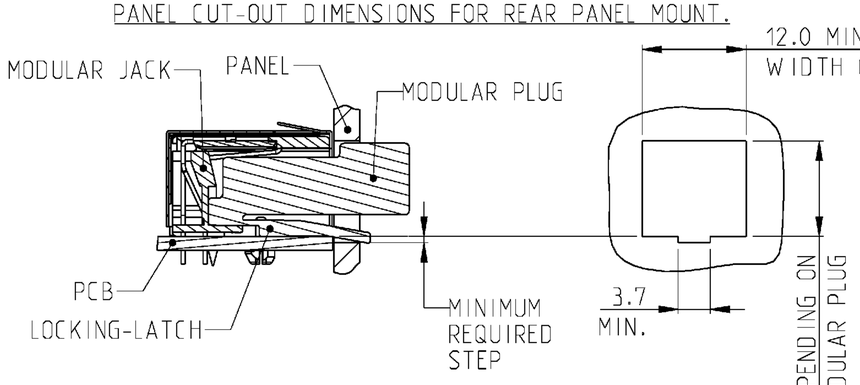
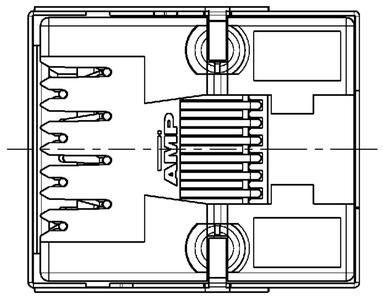
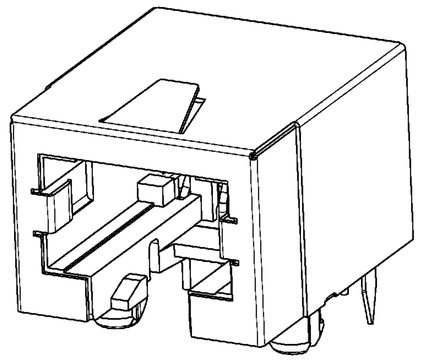
REVISIONS

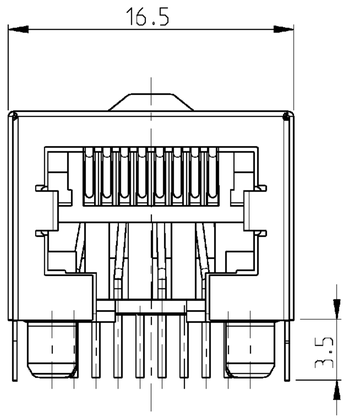
P	LTR	DESCRIPTION	DATE	DWN	APVD
	M	ECR-10-020218	21JUN11	PS	RRP
	N	ECR-11-016284	08MAR12	PS	RRP

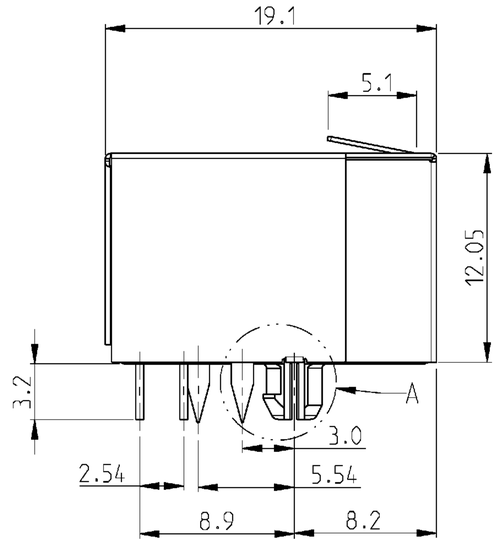
PANEL CUT-OUT DIMENSIONS FOR REAR PANEL MOUNT.

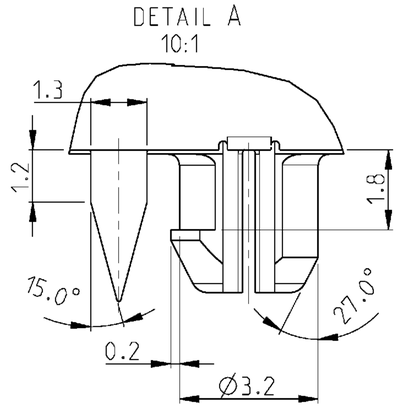


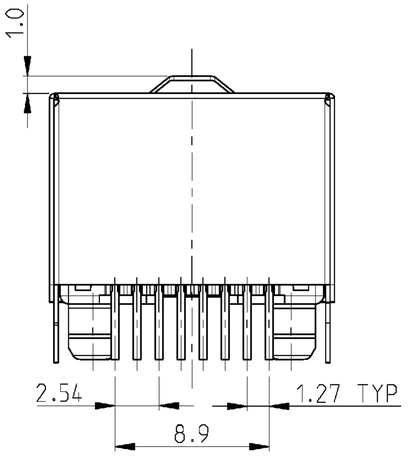


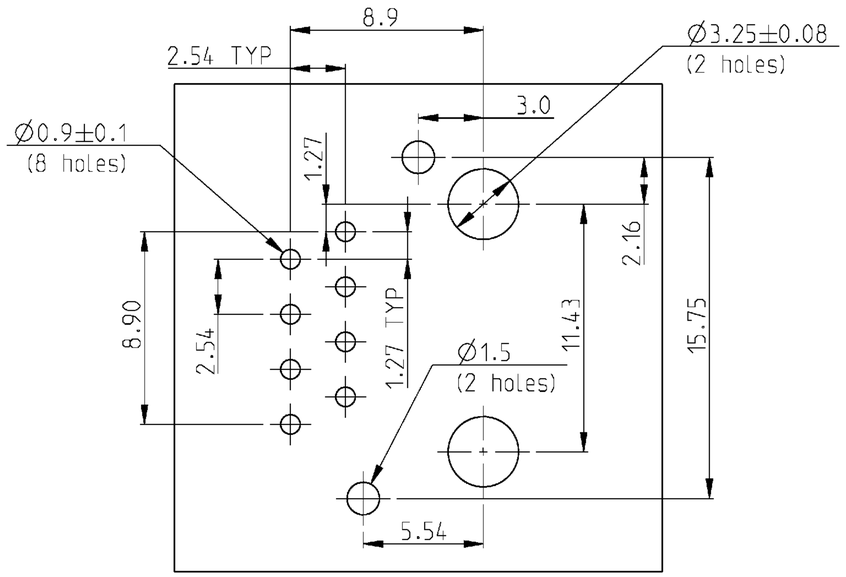






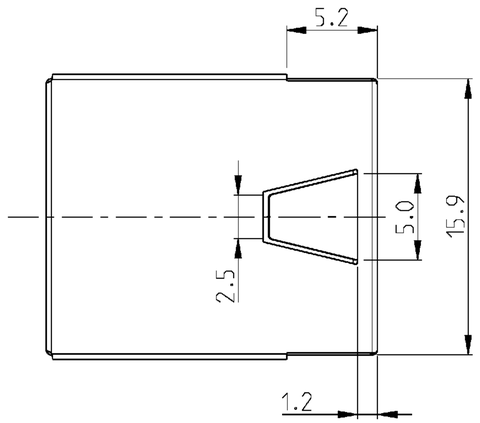






PRINTED CIRCUIT BOARD LAYOUT.

(VIEW SHOWN IS CIRCUIT (NONCOMPONENT) SIDE OF BOARD)
PRINTED CIRCUIT BOARD THICKNESS: 1,6 mm NOMINAL.
TOLERANCES UNLESS OTHERWISE SPECIFIED: ±0,05



1 FOR APPLICATION INFORMATION SEE SPEC. 114-19019

2 FOR PERFORMANCE REQUIREMENTS SEE SPEC. 108-19064
EXCEPT SHIELDING REQUIREMENTS.

PACKAGING	FINISH	MATERIAL	P/N
190 PCS IN A TRAY	OVERALL:Ni 1.27µm MIN. ALL OVER THE CONTACTS SOLDER ZONE: Sn 2.5µm MIN. ON THE SOLDER ZONE OF THE CONTACT MATING ZONE: PLATING CONFIRMS TO PRODUCT REQUIREMENTS AS PER SPECIFICATION 108-19064	HOUSING: PBT UL 94V-0 BLACK CONTACTS: PHBRONZE SHIELD: BRASS PRE-PLATED (BRIGHT TIN)	106066-2

THIS DRAWING IS A CONTROLLED DOCUMENT FOR TYCO ELECTRONICS CORPORATION IT IS SUBJECT TO CHANGE AND THE CONTROLLING ENGINEERING ORGANIZATION SHOULD BE CONTACTED FOR THE LATEST REVISION.

DWN 29 JAN 02 Verougstraete
CHK –
APVD –
PRODUCT SPEC –
APPLICATION SPEC –
WEIGHT –

DIMENSIONS: mm
TOLERANCES UNLESS OTHERWISE SPECIFIED:
0 PLC ±0.15
1 PLC ±0.15
2 PLC ±0.15
3 PLC ±0.15
4 PLC ±0.15
ANGLES ±0°30'
MATERIAL –
FINISH –

TE Connectivity

NAME MODULAR JACK 8 POS., LOW PROFILE FULLY SHIELDED, SIDE ENTRY, WITH INSULATING TAPE

SIZE	CAGE CODE	DRAWING NO	RESTRICTED TO
A2	00779	C-106066	–

CUSTOMER DRAWING | SCALE 4:1 | SHEET 1 OF 1 | REV N