

LM567/LM567C Tone Decoder

Check for Samples: LM567, LM567C

FEATURES

- 20 to 1 Frequency Range with an External Resistor
- Logic Compatible Output with 100 mA Current Sinking Capability
- Bandwidth Adjustable from 0 to 14%
- High Rejection of Out of Band Signals and Noise
- **Immunity to False Signals**
- **Highly Stable Center Frequency**
- Center Frequency Adjustable from 0.01 Hz to 500 kHz

APPLICATIONS

- **Touch Tone Decoding**
- **Precision Oscillator**
- **Frequency Monitoring and Control**
- **Wide Band FSK Demodulation**
- **Ultrasonic Controls**
- **Carrier Current Remote Controls**
- **Communications Paging Decoders**

DESCRIPTION

The LM567 and LM567C are general purpose tone decoders designed to provide a saturated transistor switch to ground when an input signal is present within the passband. The circuit consists of an I and Q detector driven by a voltage controlled oscillator which determines the center frequency of the External components are used to independently set center frequency, bandwidth and output delay.

CONNECTION DIAGRAM

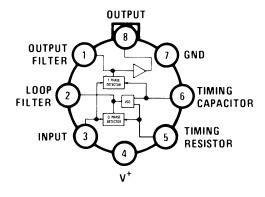


Figure 1. Metal Can Package Top View See Package Number LMC0008C **OBSOLETE**

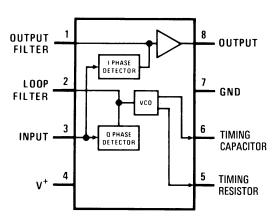


Figure 2. PDIP and SOIC Packages Top View See Package Number D0008A See Package Number P0008E

Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet. All trademarks are the property of their respective owners.





These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

ABSOLUTE MAXIMUM RATINGS(1)(2)(3)

Supply Voltage Pin	9V
Power Dissipation (4)	1100 mW
V ₈	15V
V_3	-10V
V_3	V ₄ + 0.5V
Storage Temperature Range	−65°C to +150°C
Operating Temperature Range	
LM567H	−55°C to +125°C
LM567CH, LM567CM, LM567CN	0°C to +70°C
Soldering Information	
PDIP Package	
Soldering (10 sec.)	260°C
SOIC Package	
Vapor Phase (60 sec.)	215°C
Infrared (15 sec.)	220°C
See http://www.ti.com for other methods of soldering surface mount devices.	

- (1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which ensure specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not ensured for parameters where no limit is given, however, the typical value is a good indication of device performance.
- (2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.
- (3) Refer to RETS567X drawing for specifications of military LM567H version.
- (4) The maximum junction temperature of the LM567 and LM567C is 150°C. For operating at elevated temperatures, devices in the TO-5 package must be derated based on a thermal resistance of 150°C/W, junction to ambient or 45°C/W, junction to case. For the DIP the device must be derated based on a thermal resistance of 110°C/W, junction to ambient. For the SOIC package, the device must be derated based on a thermal resistance of 160°C/W, junction to ambient.

ELECTRICAL CHARACTERISTICS

AC Test Circuit, T_A = 25°C, V⁺ = 5V

Bonometone	Conditions		LM567		LM	567C/LM56	7CM	Units
Parameters	Conditions	Min	Min Typ Max		Min	Тур	Typ Max	
Power Supply Voltage Range		4.75	5.0	9.0	4.75	5.0	9.0	V
Power Supply Current Quiescent	R _L = 20k		6	8		7	10	mA
Power Supply Current Activated	R _L = 20k		11	13		12	15	mA
Input Resistance		18	20		15	20		kΩ
Smallest Detectable Input Voltage	$I_L = 100 \text{ mA}, f_i = f_0$		20	25		20	25	mVrms
Largest No Output Input Voltage	$I_C = 100 \text{ mA}, f_i = f_o$	10	15		10	15		mVrms
Largest Simultaneous Outband Signal to Inband Signal Ratio			6			6		dB
Minimum Input Signal to Wideband Noise Ratio	B _n = 140 kHz		-6			-6		dB
Largest Detection Bandwidth		12	14	16	10	14	18	% of f _o
Largest Detection Bandwidth Skew			1	2		2	3	% of f _o
Largest Detection Bandwidth Variation with Temperature			±0.1			±0.1		%/°C
Largest Detection Bandwidth Variation with Supply Voltage	4.75–6.75V		±1	±2		±1	±5	%V
Highest Center Frequency		100	500		100	500		kHz

Submit Documentation Feedback

Copyright © 1999–2013, Texas Instruments Incorporated

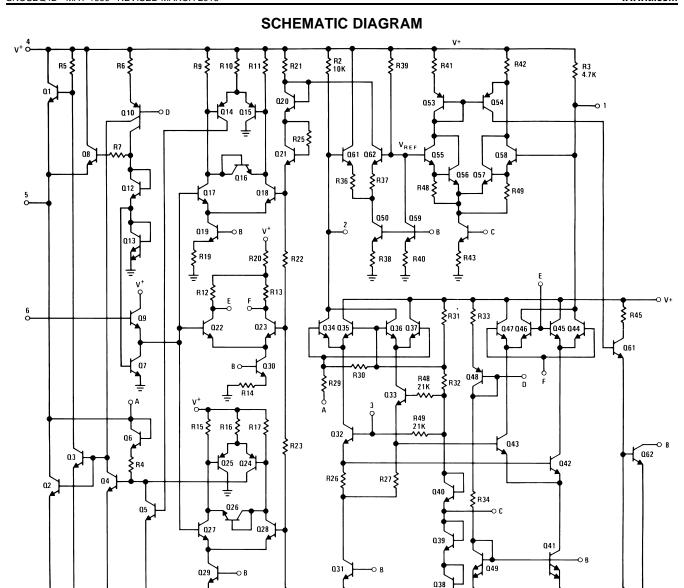


ELECTRICAL CHARACTERISTICS (continued)

AC Test Circuit, $T_A = 25$ °C, $V^+ = 5V$

Donomotoro	Conditions	LM567			LM	Unita		
Parameters	Conditions		Тур	Max	Min	Тур	Max	Units
Center Frequency Stability (4.75–5.75V)	0 < T _A < 70 -55 < T _A < +125		35 ± 60 35 ± 140			35 ± 60 35 ± 140		ppm/°C ppm/°C
Center Frequency Shift with Supply Voltage	4.75V-6.75V 4.75V-9V		0.5	1.0 2.0		0.4	2.0 2.0	%/V %/V
Fastest ON-OFF Cycling Rate			f _o /20			f _o /20		
Output Leakage Current	V ₈ = 15V		0.01	25		0.01	25	μΑ
Output Saturation Voltage	e _i = 25 mV, I ₈ = 30 mA e _i = 25 mV, I ₈ = 100 mA		0.2 0.6	0.4 1.0		0.2 0.6	0.4 1.0	V
Output Fall Time			30			30		ns
Output Rise Time			150			150		ns





R28

R24 \$

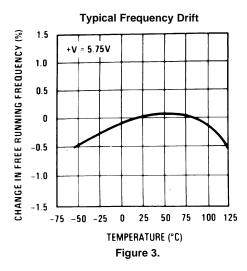
R18

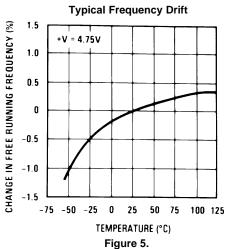
R44

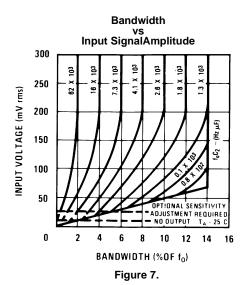
€ R35

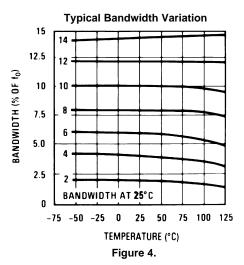


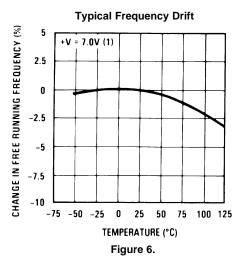
TYPICAL PERFORMANCE CHARACTERISTICS

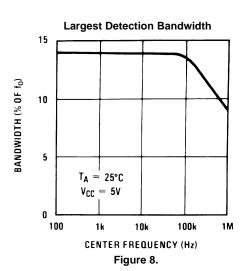






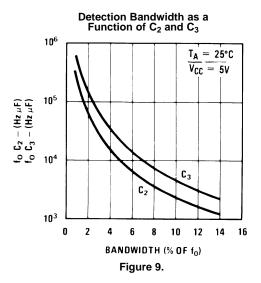


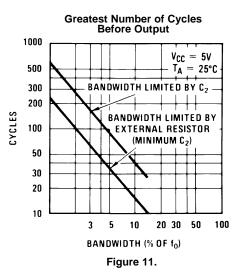






TYPICAL PERFORMANCE CHARACTERISTICS (continued)





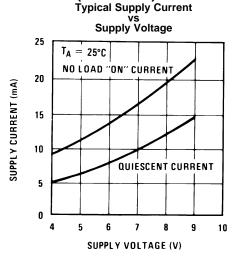
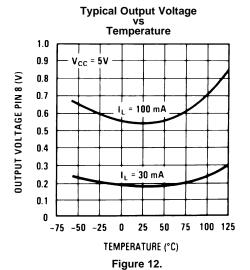
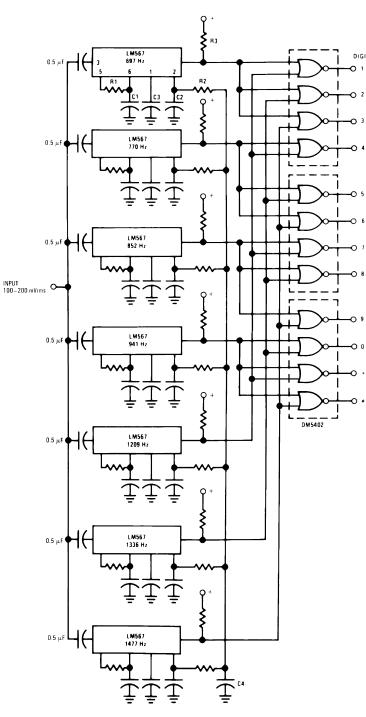


Figure 10.





TYPICAL APPLICATIONS



Component values (typ)

R1 6.8 to 15k

R2 4.7k

R3 20k

C1 0.10 mfd

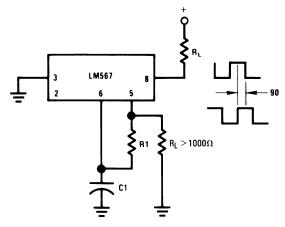
C2 1.0 mfd 6V

C3 2.2 mfd 6V

C4 250 mfd 6V

Figure 13. Touch-Tone Decoder





Connect Pin 3 to 2.8V to Invert Output

Figure 14. Oscillator with Quadrature Output

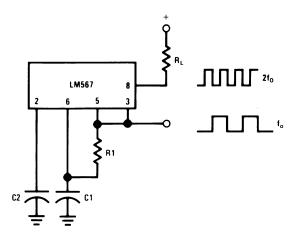


Figure 15. Oscillator with Double Frequency Output

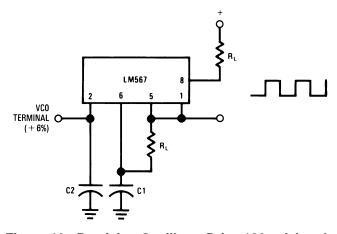
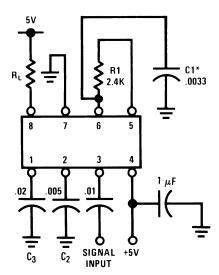


Figure 16. Precision Oscillator Drive 100 mA Loads

Submit Documentation Feedback



AC TEST CIRCUIT



 $f_i = 100 \text{ kHz} + 5V$ *Note: Adjust for $f_0 = 100 \text{ kHz}$.

APPLICATIONS INFORMATION

The center frequency of the tone decoder is equal to the free running frequency of the VCO. This is given by

$$f_0 \cong \frac{1}{1.1 R_1 C_1}$$

The bandwidth of the filter may be found from the approximation

BW = 1070
$$\sqrt{\frac{V_i}{f_0 C_2}}$$
 in % of f_0

where

- V_i = Input voltage (volts rms), $V_i \le 200 \text{mV}$
- C₂ = Capacitance at Pin 2(μF)

LM567C MDC MWC ToNE DECODER

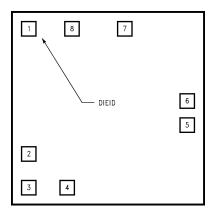


Figure 17. Die Layout (C - Step)

Table 1. Die/Wafer Characteristics

Fabrication Attribut	tes	General Die Information				
Physical Die Identification	LM567C	Bond Pad Opening Size (min)	91µm x 91µm			
Die Step	С	Bond Pad Metalization	0.5% COPPER_BAL. ALUMINUM			
Physical Attribute	S	Passivation	VOM NITRIDE			
Wafer Diameter	150mm	Back Side Metal	BARE BACK			
Dise Size (Drawn)	1600µm x 1626µm 63.0mils x 64.0mils	Back Side Connection	Floating			
Thickness	406µm Nominal		•			
Min Pitch	198µm Nominal					
Special Assembly Requirements:						
Note: Actual die size is rounded to the nea	rest micron.					

Die Bond Pad Coordinate Locations (C - Step) (Referenced to die center, coordinates in μm) NC = No Connection, N.U. = Not Used												
SIGNAL NAME	PAD# NUMBER	Х	Y	Х		Y						
OUTPUT FILTER	1	-673	686	91	х	91						
LOOP FILTER	2	-673	-419	91	х	91						
INPUT	3	-673	-686	91	х	91						
V+	4	-356	-686	91	х	91						
TIMING RES	5	673	-122	91	х	91						
TIMING CAP	6	673	76	91	х	91						
GND	7	178	686	117	х	91						
OUTPUT	8	-318	679	117	х	104						

Submit Documentation Feedback





REVISION HISTORY

CI	hanges from Revision C (March 2013) to Revision D	Pa	ıge
•	Changed layout of National Data Sheet to TI format		10

Product Folder Links: LM567 LM567C





27-Mar-2014

PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
LM567CM	ACTIVE	SOIC	D	8	95	TBD	Call TI	Call TI	0 to 70	LM 567CM	Samples
LM567CM/NOPB	ACTIVE	SOIC	D	8	95	Green (RoHS & no Sb/Br)	SN CU SN	Level-1-260C-UNLIM	0 to 70	LM 567CM	Samples
LM567CMX/NOPB	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	SN CU SN	Level-1-260C-UNLIM	0 to 70	LM 567CM	Samples
LM567CN	LIFEBUY	PDIP	Р	8	40	TBD	Call TI	Call TI	0 to 70	LM 567CN	
LM567CN/NOPB	ACTIVE	PDIP	Р	8	40	Green (RoHS & no Sb/Br)	CU SN	Level-1-NA-UNLIM	0 to 70	LM 567CN	Samples
NE567V	LIFEBUY	PDIP	Р	8	40	TBD	Call TI	Call TI	0 to 70	LM 567CN	

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.



PACKAGE OPTION ADDENDUM

27-Mar-2014

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

PACKAGE MATERIALS INFORMATION

www.ti.com 26-Mar-2013

TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing			Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM567CMX/NOPB	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1

www.ti.com 26-Mar-2013



*All dimensions are nominal

Device	Package Type Package Drawing		Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LM567CMX/NOPB	SOIC	D	8	2500	367.0	367.0	35.0

P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE PACKAGE



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001 variation BA.



D (R-PDSO-G8)

PLASTIC SMALL OUTLINE



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AA.



IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, enhancements, improvements and other changes to its semiconductor products and services per JESD46, latest issue, and to discontinue any product or service per JESD48, latest issue. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products (also referred to herein as "components") are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its components to the specifications applicable at the time of sale, in accordance with the warranty in TI's terms and conditions of sale of semiconductor products. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by applicable law, testing of all parameters of each component is not necessarily performed.

TI assumes no liability for applications assistance or the design of Buyers' products. Buyers are responsible for their products and applications using TI components. To minimize the risks associated with Buyers' products and applications, Buyers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI components or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of significant portions of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI components or services with statements different from or beyond the parameters stated by TI for that component or service voids all express and any implied warranties for the associated TI component or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Buyer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of TI components in its applications, notwithstanding any applications-related information or support that may be provided by TI. Buyer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Buyer will fully indemnify TI and its representatives against any damages arising out of the use of any TI components in safety-critical applications.

In some cases, TI components may be promoted specifically to facilitate safety-related applications. With such components, TI's goal is to help enable customers to design and create their own end-product solutions that meet applicable functional safety standards and requirements. Nonetheless, such components are subject to these terms.

No TI components are authorized for use in FDA Class III (or similar life-critical medical equipment) unless authorized officers of the parties have executed a special agreement specifically governing such use.

Only those TI components which TI has specifically designated as military grade or "enhanced plastic" are designed and intended for use in military/aerospace applications or environments. Buyer acknowledges and agrees that any military or aerospace use of TI components which have *not* been so designated is solely at the Buyer's risk, and that Buyer is solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI has specifically designated certain components as meeting ISO/TS16949 requirements, mainly for automotive use. In any case of use of non-designated products, TI will not be responsible for any failure to meet ISO/TS16949.

Products Applications

Audio www.ti.com/audio Automotive and Transportation www.ti.com/automotive Communications and Telecom Amplifiers amplifier.ti.com www.ti.com/communications **Data Converters** dataconverter.ti.com Computers and Peripherals www.ti.com/computers **DLP® Products** www.dlp.com Consumer Electronics www.ti.com/consumer-apps

DSP **Energy and Lighting** dsp.ti.com www.ti.com/energy Clocks and Timers www.ti.com/clocks Industrial www.ti.com/industrial Interface interface.ti.com Medical www.ti.com/medical logic.ti.com Logic Security www.ti.com/security

Power Mgmt power.ti.com Space, Avionics and Defense www.ti.com/space-avionics-defense

Microcontrollers microcontroller.ti.com Video and Imaging www.ti.com/video

RFID www.ti-rfid.com

OMAP Applications Processors www.ti.com/omap TI E2E Community e2e.ti.com/omap

Wireless Connectivity <u>www.ti.com/wirelessconnectivity</u>