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# **Pulse Proof Thick Film Chip Resistors**



- High pulse performance, up to 10 kW
- Stability △R/R ≤ 1 % for 1000 h at 70 °C
- AEC-Q200 qualified



COMPLIANT

HALOGEN FREE • Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

STANDARD ELECTRICAL SPECIFICATIONS									
ТҮРЕ	CASE SIZE IMPERIAL	CASE SIZE METRIC	POWER RATING P <sub>70</sub> W	LIMITING ELEMENT VOLTAGE U <sub>max.</sub> AC <sub>RMS</sub> /DC V	TEMPERATURE COEFFICIENT ppm/K	TOLERANCE %	RESISTANCE RANGE Ω	SERIES	
D10/CRCW0402-IF	0402	RR1005M	0.063	50	± 200	± 5 ± 10	1.0 to 100K	E24	
D11/CRCW0603-IF	0603	RR1608M	0.10	75	± 200	± 5 ± 10	1.0 to 100K	E24	
D12/CRCW0805-IF	0805	RR2012M	0.125	150	± 200	± 5 ± 10	1.0 to 100K	E24	
D25/CRCW1206-IF	1206	RR3216M	0.25	200	± 200	± 5 ± 10	1.0 to 100K	E24	
CRCW1210-IF	1210	RR3225M	0.50	200	± 200	± 5 ± 10	1.0 to 100K	E24	
CRCW2010-IF	2010	RR5025M	0.75	400	± 200	± 5 ± 10	1.0 to 100K	E24	
CRCW2512-IF	2512	RR6332M	1.0	500	± 200	± 5 ± 10	1.0 to 100K	E24	

### Notes

These resistors do not feature a limited lifetime when operated within the limits of rated dissipation, permissible operating voltage, and permissible film temperature. However, the resistance typically increase due to the resistor's film temperature over operating time, generally known as drift. The drift may exceed the stability requirements of an individual application circuit and thereby limits the functional time.

Marking: See data sheet "Surface Mount Resistor Marking" (document number 20020).

Power rating depends on the max. temperature at the solder point, the component placement density and the substrate material.

TECHNICAL SPECIFICATIONS								
PARAMETER	UNIT	D10/ CRCW0402-IF	D11/ CRCW0603-IF	D12/ CRCW0805-IF	D25/ CRCW1206-IF	CRCW1210-IF	CRCW2010-IF	CRCW2512-IF
Rated dissipation P <sub>70</sub> <sup>(1)</sup>	W	0.063	0.1	0.125	0.25	0.5	0.75	1.0
Operating voltage U <sub>max.</sub> AC <sub>RMS</sub> /DC	v	50	75	150	200	200	400	500
Insulation voltage U <sub>ins</sub> (1 min)	V	75	100	200	300	300	300	300
Insulation resistance	Ω		> 109					
Operating temperature range	°C	-55 to +155						
Failure rate	h⁻¹		< 0.1 x 10 <sup>-9</sup>					
Mass	mg	0.65 2 5.5 10 16 25.5 40.5						

#### Note

(1) The power dissipation on the resistor generates a temperature rise against the local ambient, depending on the heat flow support of the printe-circuit board (thermal resistance). The rated dissipation applies only if the permitted film temperature of 155 °C is not exceeded.

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PART NUMBER AND PRODUCT DESCRIPTION								
Part Number: CRCW08051R00JNEAIF								
C R C W 0 8 0 5 1 R 0 0 J N E A I F								
TYPE	VALUE	TOLERANCE	TCR	PACKAGING	SPECIAL			
CRCW0402 CRCW0603 CRCW0805	<b>R</b> = Decimal <b>K</b> = Thousand	<b>J</b> = ± 5 % <b>K</b> = ± 10 %	<b>N</b> = ± 200 ppm/K	EA, EB, EC, ED, EE, EF,	Up to 2 digits <b>IF</b> = Pulse proof			
CRCW1206				EG, EH,				
CRCW1210 CRCW2010				EI, EL				
CRCW2512								
Product Description:	D12/CRCW0805-IF 2	00 1R0 5 % ET1 e3						
D12/CRCW0805-IF	200	1R0	5 %	ET1	e3			
TYPE	TCR	RESISTANCE VALUE	TOLERANCE	PACKAGING	LEAD (Pb)-FREE			
D10/CRCW0402-IF	± <b>200</b> ppm/K	$\mathbf{1R0} = 1 \Omega$	± 5 %	ET1, ET2,	<b>e3</b> = Pure tin			
D11/CRCW0603-IF D12/CRCW0805-IF		<b>10K</b> = 10 kΩ	± 10 %	ET3, ET4, ET5, ET6,	termination finish			
D25/CRCW1206-IF				ET7, ET8,				
CRCW1210-IF CRCW2010-IF				ET9, EF4, E02, E67,				
CRCW2512-IF				E82				

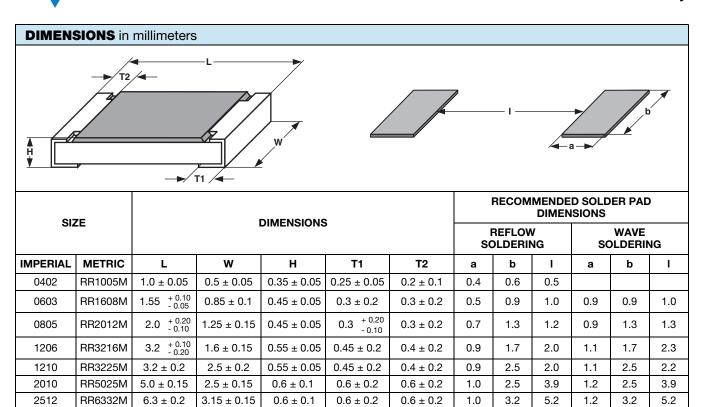
PACKAGING							
ТҮРЕ	CODE	QUANTITY	CARRIER TAPE	WIDTH	PITCH	REEL DIAMETER	
D10/CRCW0402-IF	ED = ET7	10 000		8 mm	2 mm	180 mm/7"	
	EE = EF4	50 000				330 mm/13"	
	EI = ET2	5000		8 mm		180 mm/7"	
	ED = ET3	10 000			2 mm	180 mm/7"	
	EL = ET4	20 000			2 mm	285 mm/11.25"	
D11/CRCW0603-IF	EE = ET8	50 000				330 mm/13"	
	EA = ET1	5000		8 mm	4 mm	180 mm/7"	
	EB = ET5	10 000	Paper tape acc. to IEC 60286-3			285 mm/11.25"	
	EC = ET6	20 000				330 mm/13"	
	EA = ET1	5000	Type 1a	8 mm	4 mm	180 mm/7"	
D12/CRCW0805-IF	EB = ET5	10 000				285 mm/11.25"	
	EC = ET6	20 000				330 mm/13"	
	EA = ET1	5000		8 mm	4 mm	180 mm/7"	
D25/CRCW1206-IF	EB = ET5	10 000				285 mm/11.25"	
	EC = ET6	20 000				330 mm/13"	
	EA = ET1	5000		8 mm	4 mm	180 mm/7"	
CRCW1210-IF	EB = ET5	10 000				285 mm/11.25"	
	EC = ET6	20 000				330 mm/13"	
CRCW2010-IF	EF = E02	4000	Pressed tape	12 mm	4 mm	180 mm/7"	
	EG = E67	2000	acc. to IEC 60286-3	10	8 mm	100 mm/7"	
CRCW2512-IF	EH = E82	4000	Type 1b	12 mm	4 mm	- 180 mm/7"	

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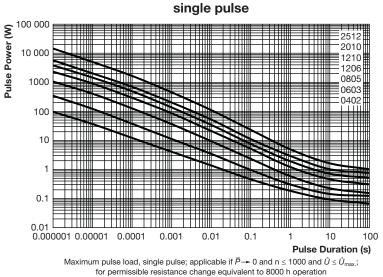
# D/CRCW-IF e3

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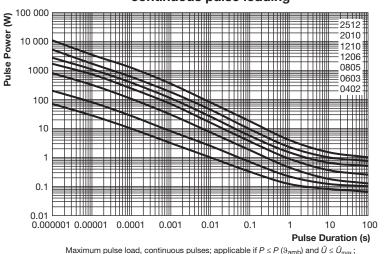
### FUNCTIONAL PERFORMANCE



Maximum pulse dissipation as a function of the pulse duration,

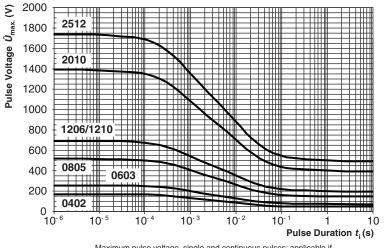
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# Maximum pulse dissipation as a function of the pulse duration, continuous pulse loading

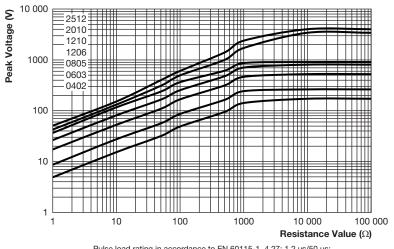
Maximum pulse load, continuous pulses; applicable if  $P \le P(9_{amb})$  and  $\hat{U} \le \hat{U}_{max}$ ; for permissible resistance change equivalent to 8000 h operation



Maximum pulse voltage, single and continuous pulses; applicable if  $\hat{P}\leq\hat{P}_{max};$  for permissible resistance change equivalent to 8000 h operation

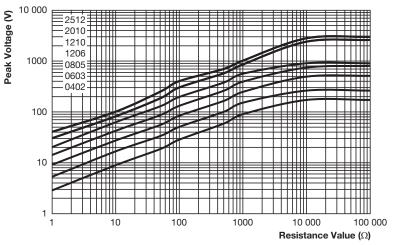
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### Single-pulse high voltage overload test 1.2 $\mu$ s/50 $\mu$ s EN 140000 4.27

Pulse load rating in accordance to EN 60115-1, 4.27; 1.2  $\mu$ s/50  $\mu$ s; 5 pulses at 12 s intervals; for permissible resistance change 1 %



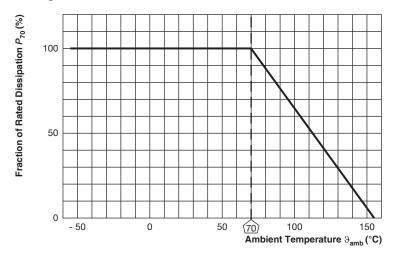
Single-pulse high voltage overload test 10 µs/700 µs EN 140000 4.27

Pulse load rating in accordance to EN 60115-1, 4.27; 10  $\mu s/700~\mu s;$  10 pulses at 1 min intervals; for permissible resistance change 1 %

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### Derating



TEST PROCEDURES AND REQUIREMENTS							
IEC	IEC		PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE (ΔR)			
EN 60115-1	60082-2	TEST		STABILITY CLASS 1 OR BETTER			
	TEST METHOD		Stability for product type:	1.0.1- 100.1-0			
			D/CRCW-IF e3	1 Ω to 100 kΩ			
4.5	-	Resistance	-	± 5 %; ± 10 %			
4.7	-	Voltage proof	<i>U</i> = 1.4 x <i>U</i> <sub>ins</sub> ; 60 s	No flashover or breakdown			
4.13	-	Short time overload	$U = 2.5 \times \sqrt{P_{70} \times R} \le 2 \times U_{\text{max.};}$ duration acc. to style	± (0.25 % <i>R</i> + 0.05 Ω)			
4.17.2 58 (Td)	<sup>-</sup> d) Solderability	Solder bath method; Sn60Pb40; non-activated flux; (235 ± 5) °C, (2 ± 0.2) s	Good tinning (≥ 95 % covered); no visible damage				
		Solder bath method; Sn96.5Ag3Cu0.5; non-activated flux; (245 ± 5) °C, (3 ± 0.3) s	Good tinning (≥ 95 % covered); no visible damage				
4.8.4.2	-	Temperature coefficient	(20/- 55/20) °C and (20/125/20) °C	± 200 ppm/K			
4.19 14 (N	14 (Na)	Rapid change of temperature	30 min. at - 55 °C; 30 min. at 125°C				
	. 1		5 cycles 1000 cycles	$\pm$ (0.25 % R + 0.05 Ω) $\pm$ (1 % R + 0.05 Ω)			



TEST PROCEDURES AND REQUIREMENTS								
EN IEC			PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE (∆R)				
60115-1	60082-2 TEST	TEST		STABILITY CLASS 1 OR BETTER				
CLAUSE	METHOD		Stability for product type:	1 Ω to 100 kΩ				
			D/CRCW-IF e3	1 32 10 100 K32				
4.23	-	Climatic sequence:	-					
4.23.2	2 (Ba)	Dry heat	125 °C; 16 h					
4.23.3	30 (Db)	Damp heat, cyclic	55 °C; ≥ 90 % RH; 24 h; 1 cycle					
4.23.4	1 (Aa)	Cold	- 55 °C; 2 h	± (1 % <i>R</i> + 0.05 Ω)				
4.23.5	13 (M)	Low air pressure	1 kPa; (25 ± 10) °C; 1 h					
4.23.6	30 (Db)	Damp heat, cyclic	55 °C; ≥ 90 % RH; 24 h; 5 cycles					
4.23.7	-	DC load	$U = \sqrt{P_{70} \times R}$					
4.25.1	-	Endurance at 70 °C	U = √P <sub>70</sub> x R ≤ U <sub>max.</sub> 1.5 h on; 0.5 h off; 70 °C; 1000 h 70 °C; 8000 h	± (1 % <i>R</i> + 0.05 Ω) ± (2 % <i>R</i> + 0.1 Ω)				
4.18.2	58 (Td)	Resistance to soldering heat	Solder bath method (260 $\pm$ 5) °C; (10 $\pm$ 1) s	± (0.25 % <i>R</i> + 0.05 Ω)				
4.24	78 (Cab)	Damp heat, steady state	(40 ± 2) °C; (93 ± 3) % RH; 56 days	± (1 % <i>R</i> + 0.05 Ω)				
4.25.3	-	Endurance at upper category temperature	155 °C; 1000 h	± (1 % <i>R</i> + 0.05 Ω)				
4.27	-	Single pulse high voltage overload, 10 μs/700 μs	$\hat{U} = 10 \text{ x } \sqrt{P_{70} \text{ x } R} \le 2 \text{ x } U_{\text{max.}};$ 10 pulses	± (1 % <i>R</i> + 0.05 Ω)				

All tests are carried out in accordance with the following specifications:

- EN 60115-1, generic specification
- EN 140400, sectional specification
- EN 140401-802, detail specification
- IEC 60068-2-x, environmental test procedures

Packaging of components is done in paper or blister tapes according to IEC 60286-3.



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