Quad 2-input NOR gate Rev. 5 — 26 November 2015

Product data sheet

1. General description

The 74HC02; 74HCT02 is a quad 2-input NOR gate. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC} .

2. Features and benefits

- Input levels:
 - For 74HC02: CMOS level
 - ◆ For 74HCT02: TTL level
- Complies with JEDEC standard no. 7A
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
- Multiple package options
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

3. Ordering information

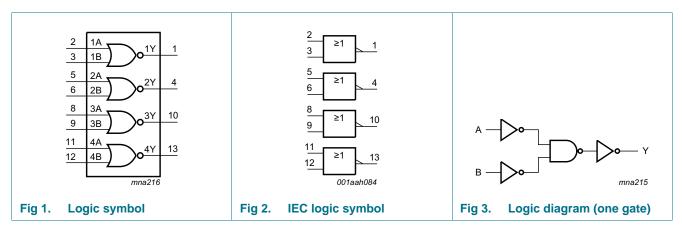
Table 1. Ordering information

Type number	Package					
	Temperature range	Name	Description	Version		
74HC02D	–40 °C to +125 °C	SO14	plastic small outline package; 14 leads; body width	SOT108-1		
74HCT02D			3.9 mm			
74HC02DB	–40 °C to +125 °C	· · · · · · · · · · · · · · · · · · ·	plastic shrink small outline package; 14 leads; body	SOT337-1		
74HCT02DB			width 5.3 mm			
74HC02PW	–40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads;	SOT402-1		
74HCT02PW			body width 4.4 mm			
74HC02BQ	–40 °C to +125 °C	DHVQFN14	plastic dual in-line compatible thermal enhanced very	SOT762-1		
74HCT02BQ			thin quad flat package; no leads; 14 terminals; body 2.5 \times 3 \times 0.85 mm			

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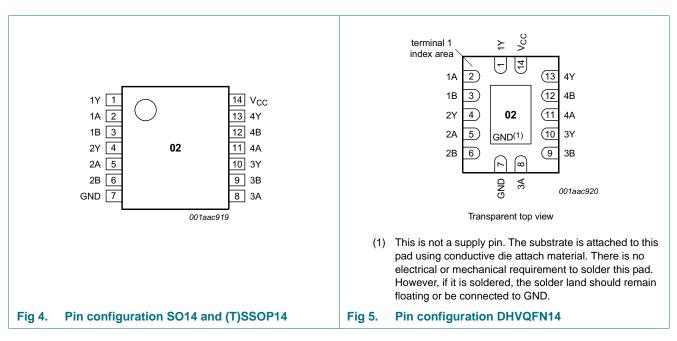
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4. Functional diagram



5. Pinning information

5.1 Pinning



5.2 Pin description

Table 2. **Pin description** Symbol Pin Description 1Y to 4Y 1, 4, 10, 13 data output 1A to 4A 2, 5, 8, 11 data input 1B to 4B 3, 6, 9,12 data input GND 7 ground (0 V) 14 supply voltage V_{CC}

74HC_HCT02

6. Functional description

Table 3.Function table^[1]

Input	Output	
nA	nB	nY
L	L	Н
Х	Н	L
Н	X	L

[1] H = HIGH voltage level;

L = LOW voltage level;

X = don't care.

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+7	V
I _{IK}	input clamping current	V_{I} < -0.5 V or V_{I} > V_{CC} + 0.5 V	<u>[1]</u>	-	±20	mA
I _{OK}	output clamping current	V_{O} < -0.5 V or V_{O} > V_{CC} + 0.5 V	<u>[1]</u>	-	±20	mA
I _O	output current	$-0.5 \text{ V} < \text{V}_{\text{O}} < \text{V}_{\text{CC}} + 0.5 \text{ V}$		-	±25	mA
I _{CC}	supply current			-	50	mA
I _{GND}	ground current			-50	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	SO14, (T)SSOP14 and DHVQFN14 packages	[2]	-	500	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

For SO14 package: P_{tot} derates linearly with 8 mW/K above 70 °C.
 For (T)SSOP14 packages: P_{tot} derates linearly with 5.5 mW/K above 60 °C.
 For DHVQFN14 packages: P_{tot} derates linearly with 4.5 mW/K above 60 °C.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V)

Symbol	Parameter	Conditions		74HC02			74HCT02		
			Min	Тур	Max	Min	Тур	Max	
V _{CC}	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
VI	input voltage		0	-	V _{CC}	0	-	V _{CC}	V
Vo	output voltage		0	-	V _{CC}	0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	-	+125	-40	-	+125	°C
$\Delta t / \Delta V$	input transition rise and fall rate	$V_{CC} = 2.0 V$	-	-	625	-	-	-	ns/V
		$V_{CC} = 4.5 V$	-	1.67	139	-	1.67	139	ns/V
		$V_{CC} = 6.0 V$	-	-	83	-	-	-	ns/V

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9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	–40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	Ī
74HC02	1	1								
VIH	HIGH-level	V _{CC} = 2.0 V	1.5	1.2	-	1.5	-	1.5	-	V
	input voltage	V _{CC} = 4.5 V	3.15	2.4	-	3.15	-	3.15	-	V
		V _{CC} = 6.0 V	4.2	3.2	-	4.2	-	4.2	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 2.0 V	-	0.8	0.5	-	0.5	-	0.5	V
		V _{CC} = 4.5 V	-	2.1	1.35	-	1.35	-	1.35	V
		V _{CC} = 6.0 V	-	2.8	1.8	-	1.8	-	1.8	V
V _{OH} HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$									
	$I_{O} = -20 \ \mu A; \ V_{CC} = 2.0 \ V$	1.9	2.0	-	1.9	-	1.9	-	V	
		$I_{O} = -20 \ \mu A; \ V_{CC} = 4.5 \ V$	4.4	4.5	-	4.4	-	4.4	-	V
		$I_{O} = -20 \ \mu A; \ V_{CC} = 6.0 \ V$	5.9	6.0	-	5.9	-	5.9	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.98	4.32	-	3.84	-	3.7	-	V
		$I_{O} = -5.2 \text{ mA}; V_{CC} = 6.0 \text{ V}$	5.48	5.81	-	5.34	-	5.2	-	V
V _{OL}	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}$								
	output voltage	$I_0 = 20 \ \mu A; \ V_{CC} = 2.0 \ V$	-	0	0.1	-	0.1	-	0.1	V
		$I_0 = 20 \ \mu A; \ V_{CC} = 4.5 \ V$	-	0	0.1	-	0.1	-	0.1	V
		$I_0 = 20 \ \mu\text{A}; \ V_{CC} = 6.0 \ \text{V}$	-	0	0.1	-	0.1	-	0.1	V
		I_{O} = 4.0 mA; V_{CC} = 4.5 V	-	0.15	0.26	-	0.33	-	0.4	V
		$I_{O} = 5.2 \text{ mA}; V_{CC} = 6.0 \text{ V}$	-	0.16	0.26	-	0.33	-	0.4	V
I	input leakage current	$V_1 = V_{CC}$ or GND; $V_{CC} = 6.0 V$	-	-	±0.1	-	±1	-	±1	μΑ
lcc	supply current		-	-	2.0	-	20	-	40	μΑ
CI	input capacitance		-	3.5	-	-	-	-	-	pF

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Symbol	Parameter	Conditions		25 °C		–40 °C t	o +85 °C	–40 °C to	o +125 ℃	Unit
			Min	Тур	Max	Min	Max	Min	Max	
74HCT0	2									
V _{IH}	HIGH-level input voltage	$V_{CC} = 4.5 V \text{ to } 5.5 V$	2.0	1.6	-	2.0	-	2.0	-	V
V _{IL}	LOW-level input voltage	V_{CC} = 4.5 V to 5.5 V	-	1.2	0.8	-	0.8	-	0.8	V
V _{OH}	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = -20 μA	4.4	4.5	-	4.4	-	4.4	-	V
	I _O = -4.0 mA	3.98	4.32	-	3.84	-	3.7	-	V	
V _{OL}	CoL LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
		I _O = 20 μA	-	0	0.1	-	0.1	-	0.1	V
		I _O = 5.2 mA	-	0.15	0.26	-	0.33	-	0.4	V
I	input leakage current	$V_1 = V_{CC} \text{ or GND};$ $V_{CC} = 5.5 \text{ V}$	-	-	±0.1	-	±1	-	±1	μΑ
I _{CC}	supply current	$\label{eq:VI} \begin{array}{l} V_{I} = V_{CC} \text{ or } GND; \ I_{O} = 0 \ A; \\ V_{CC} = 5.5 \ V \end{array}$	-	-	2.0	-	20	-	40	μA
∆l _{CC}	additional supply current	per input pin; $V_I = V_{CC} - 2.1 \text{ V}; I_O = 0 \text{ A};$ other inputs at V_{CC} or GND; $V_{CC} = 4.5 \text{ V}$ to 5.5 V	-	150	540	-	675	-	735	μA
CI	input capacitance		-	3.5	-	-	-	-	-	pF

Table 6. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

10. Dynamic characteristics

Table 7. Dynamic characteristics

 $GND = 0 V; C_L = 50 pF;$ for test circuit see <u>Figure 7</u>.

Symbol	Parameter	Conditions		25 °C			–40 °C to	Unit	
			-	Min	Тур	Мах	Max (85 °C)	Max (125 °C)	
74HC02									
t _{pd}	propagation delay	nA, nB to nY; see <u>Figure 6</u>	<u>[1]</u>						
		$V_{\rm CC} = 2.0 \ V$		-	25	90	115	135	ns
	$V_{CC} = 4.5 V$		-	9	18	23	27	ns	
		$V_{CC} = 5.0 \text{ V}; \text{ C}_{L} = 15 \text{ pF}$		-	7	-	-	-	ns
		$V_{\rm CC} = 6.0 \ V$		-	7	15	20	23	ns
t _t	transition time	see <u>Figure 6</u>	[2]						
		$V_{\rm CC} = 2.0 \ V$		-	19	75	95	110	ns
		$V_{CC} = 4.5 V$		-	7	15	19	22	ns
		V _{CC} = 6.0 V		-	6	13	16	19	ns
C _{PD}	power dissipation capacitance	per package; $V_I = GND$ to V_{CC}	<u>[3]</u>	-	22	-	-	-	pF

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Table 7.	Dynamic characteristics
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GND = 0 V; $C_L = 50$ pF; for test circuit see <u>Figure 7</u>.

Symbol	Parameter	eter Conditions		25 °C			–40 °C to	Unit	
				Min	Тур	Max	Max (85 °C)	Max (125 °C)	
74HCT02	2								
t _{pd} propagation delay	nA, nB to nY; see Figure 6	<u>[1]</u>							
		V _{CC} = 4.5 V		-	11	19	24	29	ns
		V _{CC} = 5.0 V; C _L = 15 pF		-	9	-	-	-	ns
t _t	transition time	V_{CC} = 4.5 V; see Figure 6	[2]	-	7	15	19	22	ns
C _{PD}	power dissipation capacitance	per package; V _I = GND to V _{CC} – 1.5 V	<u>[3]</u>	-	24	-	-	-	pF

 $\label{eq:tpd} [1] \quad t_{pd} \text{ is the same as } t_{PHL} \text{ and } t_{PLH}.$

- [2] t_t is the same as t_{THL} and t_{TLH} .
- [3] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W):

 P_{D} = $C_{PD} \times V_{CC}{}^{2} \times f_{i} \times N$ + \sum ($C_{L} \times V_{CC}{}^{2} \times f_{o}$) where:

 f_i = input frequency in MHz;

 f_o = output frequency in MHz;

 C_L = output load capacitance in pF;

 V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma (C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

11. Waveforms

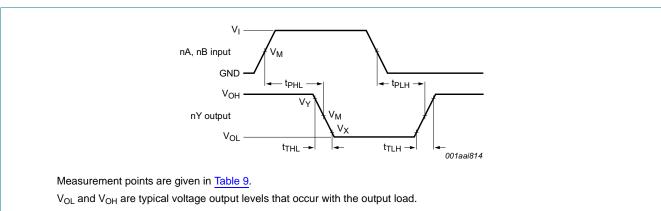


Fig 6. Input to output propagation delays

Table 8. Measurement points

Туре	Input	Output				
	V _M	V _M	V _X	V _Y		
74HC02	0.5V _{CC}	0.5V _{CC}	0.1V _{CC}	0.9V _{CC}		
74HCT02	1.3 V	1.3 V	0.1V _{CC}	0.9V _{CC}		

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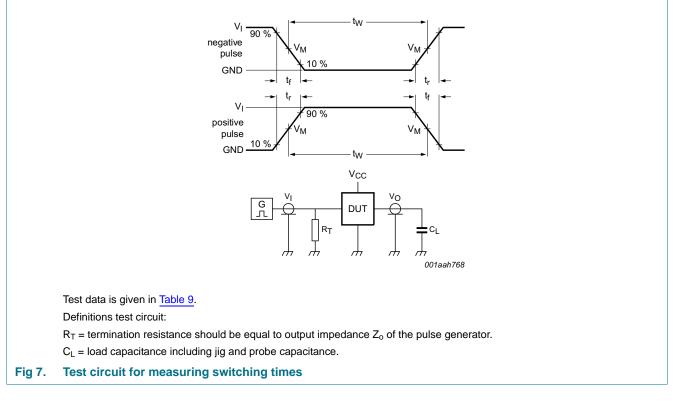


Table 9. Test data

Туре	Input Lo		Load	Test
	VI	t _r , t _f	CL	
74HC02	V _{CC}	6.0 ns	15 pF, 50 pF	t _{PLH} , t _{PHL}
74HCT02	3.0 V	6.0 ns	15 pF, 50 pF	t _{PLH} , t _{PHL}

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12. Package outline

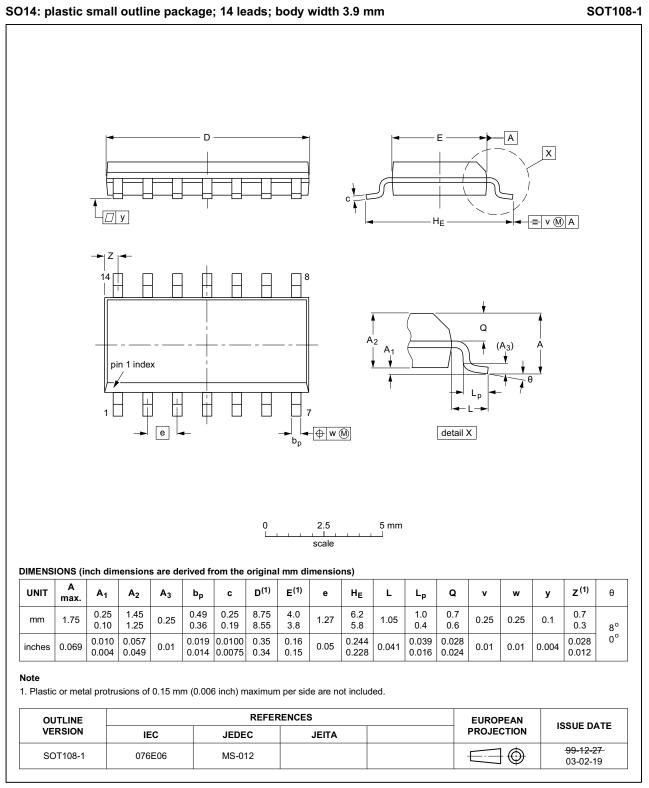


Fig 8. Package outline SOT108-1 (SO14)

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74HC_HCT02

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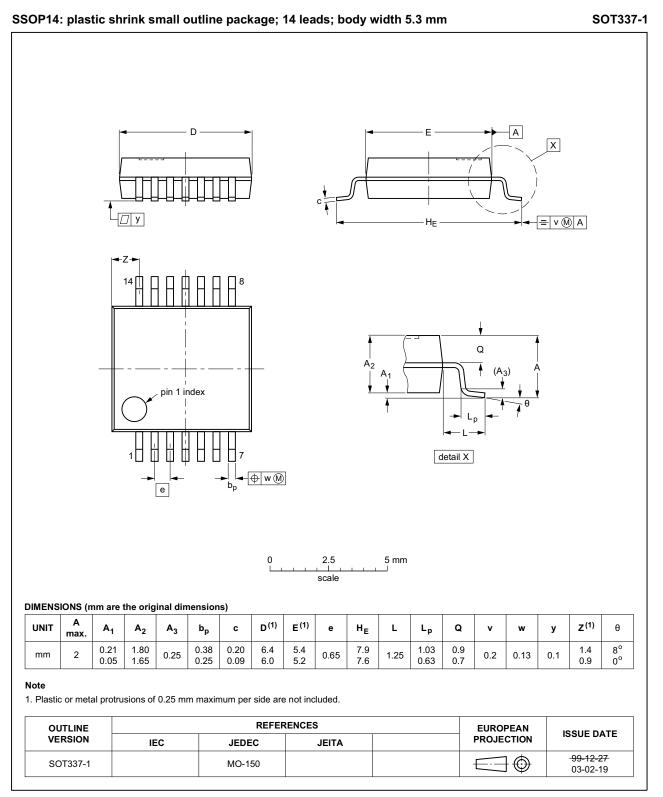


Fig 9. Package outline SOT337-1 (SSOP14)

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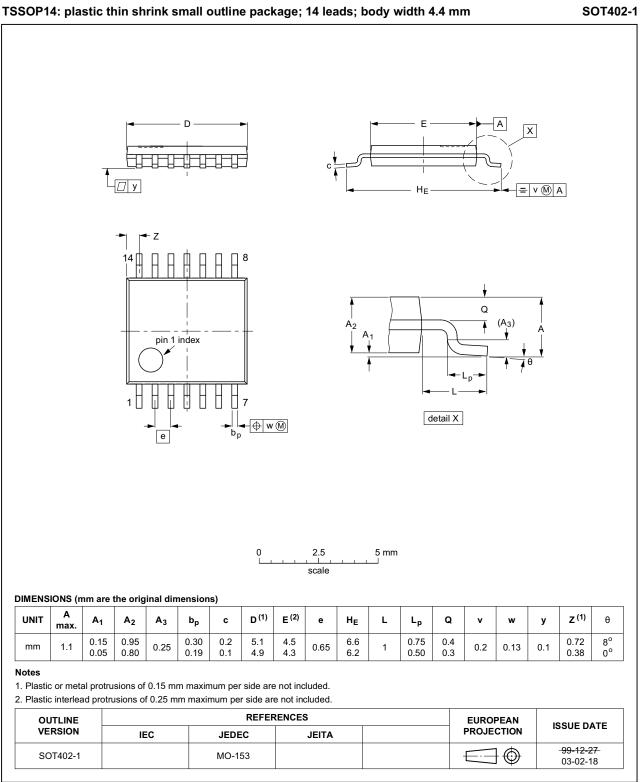
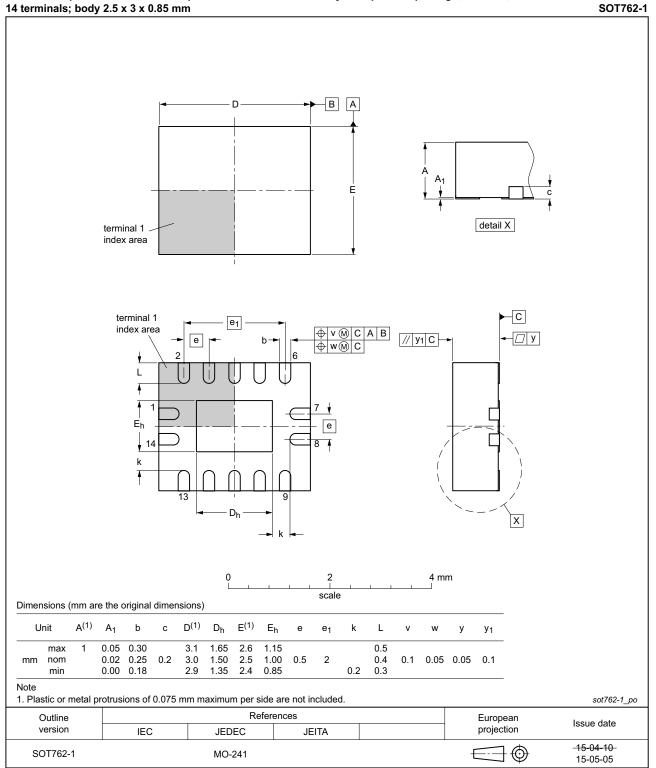


Fig 10. Package outline SOT402-1 (TSSOP14)

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DHVQFN14: plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads;

Fig 11. Package outline SOT762-1 (DHVQFN14)

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13. Abbreviations

Table 10. Abbreviations					
Acronym	Description				
CMOS	Complementary Metal-Oxide Semiconductor				
DUT	Device Under Test				
ESD	ElectroStatic Discharge				
HBM	Human Body Model				
MM	Machine Model				
TTL	Transistor-Transistor Logic				

14. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74HC_HCT02 v.5	20151126	Product data sheet	-	74HC_HCT02 v.4	
Modifications:	 Type numbers 74HC02N and 74HCT02N (SOT27-1) removed. 				
74HC_HCT02 v.4	20120904	Product data sheet	-	74HC_HCT02 v.3	
Modifications:	 Conditions for V_{OH}, I_I and I_{CC} updated to the family specification (errata). 				
74HC_HCT02 v.3	20080918	Product data sheet	-	74HC_HCT02_CNV v.2	
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. 				
	 Legal texts have been adapted to the new company name where appropriate. 				
	 Added type numbers 74HC02BQ and 74HCT02BQ (DHVQFN14 package) 			kage)	
74HC_HCT02_CNV v.2	19970827	Product specification	-	-	

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Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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74HC HCT02

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