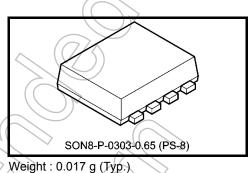
TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA48LS00F

300 mA Output Current, Variable Output Voltage and Low Dropout Voltage Regulator with ON/OFF Control Switch

The TA48LS00F consists of small-surface mount type low-dropout regulators with an output current of 300 mA (maximum) and an ON/OFF control switch. Control by an EN (ON/OFF) terminal enables the regulator to be operated only when required (output ON). The output voltage can be arbitrarily set by external resistance. Therefore, the TA48LS00F can be used for a wide range of applications. TA48LS00F is suitable for use in the power supply circuits of AV, OA and other digital devices equipped with a stand-by function, and of battery operated portable data devices of various types, where they will contribute to energy saving.





- Built-in ON/OFF control function (active high)
- Maximum output current 300 mA
- Output voltage
- Reference voltage accuracy $: V_{REF} \pm 2.3 \% (@T_j = 25^{\circ}C)$
- Low quiescent current $: 1 \text{ mA (Typ.)} @ I_{OUT} = 0 \text{ A})$
- Low standby current (output OFF mode): 0.2 µA(Typ.)
 - Low-dropout voltage : 0.5 V (Max) (@VOUT = 3.3 V, IOUT = 150 m A)

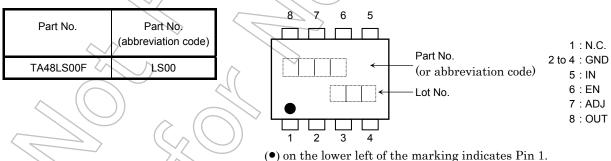
: VOUT = 1.5 to 5.0 N

Protection function
 Overcurrent / Thermal shutdown

: PS-8

• Package type

Marking / Pin Assignment



* Lot No.: The lot no. consists of three digits. The first digit represents the last digit of the year of manufacture, and the following two digits indicates the week of manufacture between 01 and either 52 or 53.

Week of manufacture (01 for the first week of the year, continuing up to 52 or 53) Year of manufacture (Lowest-order digit of the calendar year)

Start of commercial production 2006-12

The product(s) in this document ("Product") contain functions intended to protect the Product from temporary small overloads such as minor short-term overcurrent or overheating. The protective functions do not necessarily protect Product under all circumstances. When incorporating Product into your system, please design the system (1) to avoid such overloads upon the Product, and (2) to shut down or otherwise relieve the Product of such overload conditions immediately upon occurrence. For details, please refer to the notes appearing below in this document and other documents referenced in this document.

Pin Description

	•	
Pin No.	Symbol	Description
1	N.C.	Non-connection
2 to 4	GND	Ground terminal
5	IN	Input terminal. Connected by capacitor (CIN) to GND.
6	EN	Output ON/OFF control terminal. Output is ON when this pin is set to "High", OFF when this pin is open or set to "Low".
7	ADJ	Output voltage feedback to regulator. It is connected to an error amplifier with VREF=1.238 V (Typ.).
8	OUT	Output terminal. Connected by capacitor (C _{OUT}) to GND.

How to Order

Part No.	Package Type and Capaci	ity
TA48LS00F(TE85L, F)	Tape (3000 pcs/reel)	
Block Diagram		
		r current otection
EN fur fur temp prot	Dver erature ection	Reference 1.238 V (Typ.)
\sim	GND	

Absolute Maximum Rating (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Input voltage	V _{IN}	14	V	
EN Input voltage		V _{EN}	14	V
Output current		IOUT	300	mA
Operating junction temperature		Tjopr	-40 to 150	°C
Junction temperature		Tj	150	°C
Storage temperature		T _{stg}	–55 to 150	°C
Power dissipation(Note 3) Ta = 25°C		PD	1.2	W

Note 1: Do not apply current and voltage (including reverse polarity) to any pin that is not specified.

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Unit	
Thermal resistance, junction to ambient	R _{th (j-a)}	°C/W	\langle

 $\begin{array}{c} \mbox{Material: FR-4} \\ 25.4 \times 25.4 \times 1.6 \\ \mbox{Unit: (mm)} \\ \mbox{Cu base thickness: 35 } \mbox{\mu} \mbox{m} \end{array}$

Note 3: Glass epoxy board

Operating Input Voltage Range

Characteristic	Symbol	Min	У) Тур.	Max	Unit
Input voltage	V _{IN}	2.5(Note 4)	_	14.0	V

Note 4: This is the voltage at which the IC begins operating. V_D must be considered when determining the best input voltage for the application.

Output Voltage Range

Characteristic	Min	Тур.	Max	Unit
Output voltage VOUT	1.5		5.0	V

Protection Function (Reference)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Thermal shutdown	T _{SD}	V _{IN} = 4.3 V	150	170	_	°C
Thermal shutdown hysteresis width	T _{SD(hys)}	VIN - 4.5 V		15		°C
Peak circuit current	IPEAK	$V_{IN}=5.3~V,~T_j=25^\circ C$	300	500		mA
r eak circuit current		$V_{IN} = 8.3 \text{ V}, \text{ T}_{j} = 25^{\circ}\text{C}$	300	500	_	IIIA
Short circuit current		$V_{IN}=5.3~V,~T_j=25^\circ C$	_	300		mA
	ISC	$V_{IN} = 16 \text{ V}$, $T_j = 25^{\circ}\text{C}$		300		ШA

Note 5: Protection features do not guarantee that the device will be kept below the absolute maximum rated conditions. Ensure that the devices operate within the limits of the maximum rating when in actual use.

TA48LS00F Electrical Characteristics

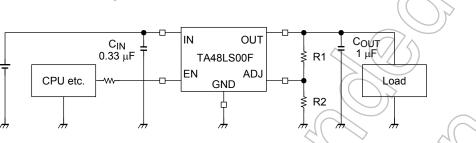
(Unless otherwise specified, V $_{EN}$ = V $_{IN}$, V $_{OUT}$ = 3.3 V, C $_{IN}$ = 0.33 μF , C $_{OUT}$ = 1 μF , T $_{j}$ = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Reference voltage	V _{REF}	V _{IN} = 5.3 V, I _{OUT} = 150 mA	1.209	1.238	1.267	V	
Line regulation	Reg∙line	$\begin{array}{l} 4.3 \ V \leq V_{IN} \leq 8.3 \ V, \\ I_{OUT} = 150 \ mA \end{array}$	Ē	2	20	mV	
Load regulation	Reg·load	$V_{IN} = 5.3 \text{ V}, 5 \text{ mA} \le I_{OUT} \le 300 \text{ mA}$		3	20	mV	
	IB	$\begin{array}{c} 4.3 \ V \leq V_{IN} \leq 8.3 \ V, \\ I_{OUT} = 0 \ A \end{array}$	\mathcal{O}	1.0	1.7	m۸	
Quiescent current	чВ	$\begin{array}{c} \text{4.3 V} \leq \text{V}_{\text{IN}} \leq \text{8.3 V}, \\ \text{I}_{\text{OUT}} = 300 \text{ mA} \end{array}$	>	5	10	mA	
Quiescent current (OFF mode)	IB(OFF)	$\begin{array}{c} 4.3 \ V \leq V_{IN} \leq 8.3 \ V, \\ V_{EN} = 0.4 \ V \end{array}$	_	0,2	5.0	μA	
Starting quiescent current	I _{Bstart}	V _{IN} = 2.1 V, I _{OUT} = 0 A	- (1.4 4.0		mA	
		V _{IN} = 2.8 V, I _{OUT} = 300 mA	((8.3	18.0	ma	
Output noise voltage	V _{NO}	$V_{IN} = 5.3 \text{ V}, I_{OUT} = 50 \text{ mA},$ 10 Hz $\leq f \leq 100 \text{ kHz}$	À	70	/_	μV_{rms}	
Ripple rejection	R.R.	V _{IN} = 5.3 V, I _{OUT} = 50 mA, f = 120 Hz	\mathcal{A}	60		dB	
Dreneutueltere		IOUT = 150 mA	$\langle \underline{\circ} \rangle$	0.2	0.5	V	
Dropout voltage	VD	10UT = 300 mA) —	0.3	0.6	v	
Output control voltage (ON)	V _{EN(ON)}		2	_	_	V	
Output control voltage (OFF)	VEN(OFF)		_	_	0.8	V	
Output control current (ON)	I _{EN(ON})	V _{IN} = V _{EN} = 5.3 V		53	75	μA	
Average temperature coefficient of output voltage	Tcvo	$\label{eq:VIN} \begin{array}{l} V_{IN} = 5.3 \text{ V}, \ I_{OUT} = 5 \text{ mA}, \\ 0^{\circ}C \leq T_{j} \leq 125^{\circ}C \end{array}$		0.3		mV/°C	

Electrical Characteristics Common to All Products

• $T_j = 25^{\circ}C$ in the measurement conditions of each item is the standard condition when a pulse test is carried out, and any drift in the electrical characteristic due to a rise in the junction temperature of the chip may be disregarded.

Application Circuit Example



• Be sure to connect a capacitor near the input terminal and output terminal between both terminals and GND. The use of a monolithic ceramic capacitor (B Characteristic or X7R) of low ESR (equivalent series resistance) is recommended. The IC may oscillate due to external conditions (output current, temperature, or the type of the capacitor used). The type of capacitor required must be determined by the actual application circuit in which the IC is used.

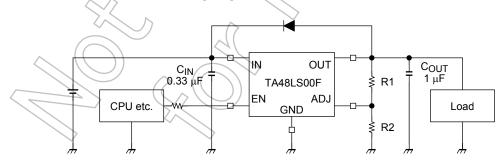
Setting Output Voltage

• The output voltage is determined by the equation shown below. When you control the output voltage with R1, a recommended value to use for R2 is 5 k Ω . R1 and R2 must be placed as close as possible to each other, and the board trace to the ADJ terminal must be kept as short as possible.

$$V_{OUT} = V_{REF} \times (1 + \frac{R1}{R2})$$

Usage Precautions

• The IC might be destroyed if a voltage greater than the input terminal voltage is applied to the output terminal, or if the input terminal is connected to GND during operation. To prevent such an occurrence, connect a diode as in the following diagram.



- There is a possibility that internal parasitic devices may be generated when momentary transients cause a terminal's potential to fall below that of the GND terminal. In such case, that the device could be destroyed. The voltage of each terminal and any state must therefore never fall below the GND potential.
- Depending on the load conditions, a steep increase in the input voltage applied (V_{IN}) may cause a momentary rise in output voltage (V_{OUT}) even if the EN (enable) pin is Low. Treat with care.

• Low voltage

TOSHIBA

Do not apply voltage to the Product that is lower than the minimum operating voltage, or the Product's protective functions will not operate properly and the Product may be permanently damaged.

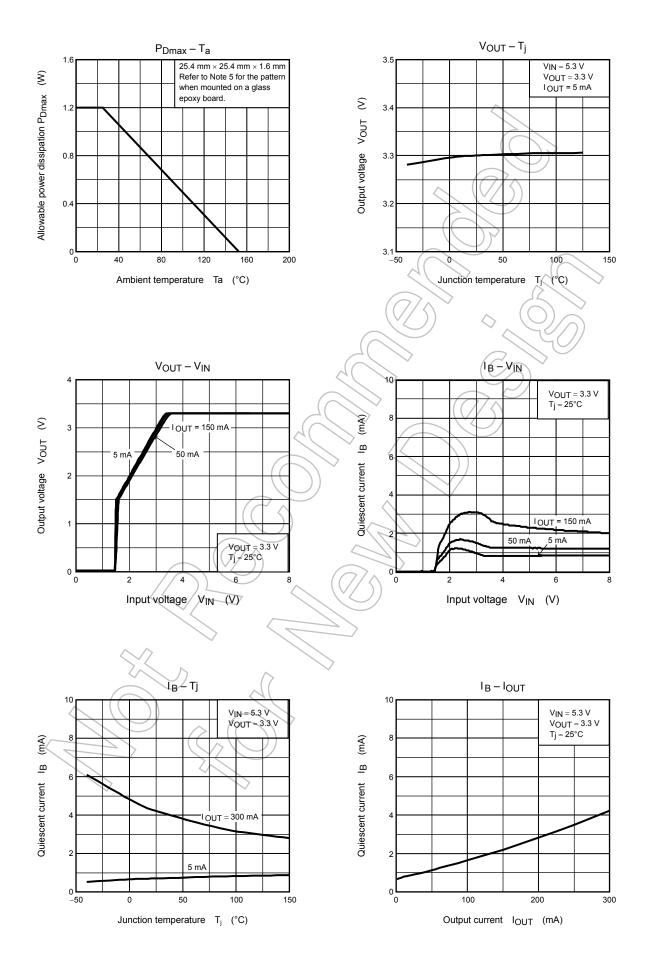
• Overcurrent Protection

The overcurrent protection circuits in the Product are designed to temporarily protect Product from minor overcurrent of brief duration. When the overcurrent protective function in the Product activates, immediately cease application of overcurrent to Product. Improper usage of Product, such as application of current to Product exceeding the absolute maximum ratings, could cause the overcurrent protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

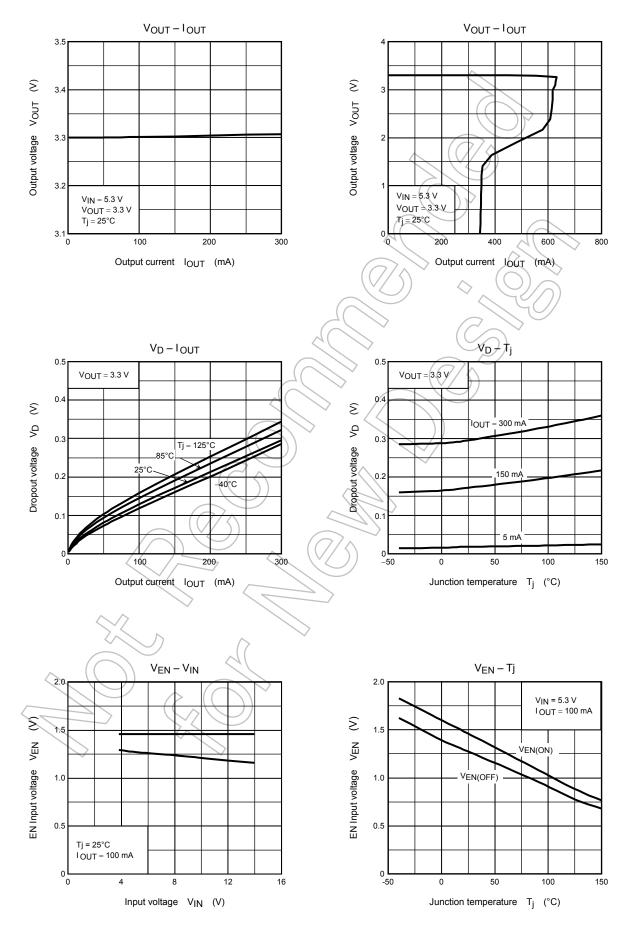
• Thermal shutdown Protection

The thermal shutdown circuits in the Product are designed to temporarily protect Product from minor overheating of brief duration. When the thermal shutdown protective function in the Product activates, immediately correct the overheating situation. Improper usage of Product, such as the application of heat to Product exceeding the absolute maximum ratings, could cause the thermal shutdown protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

TOSHIBA



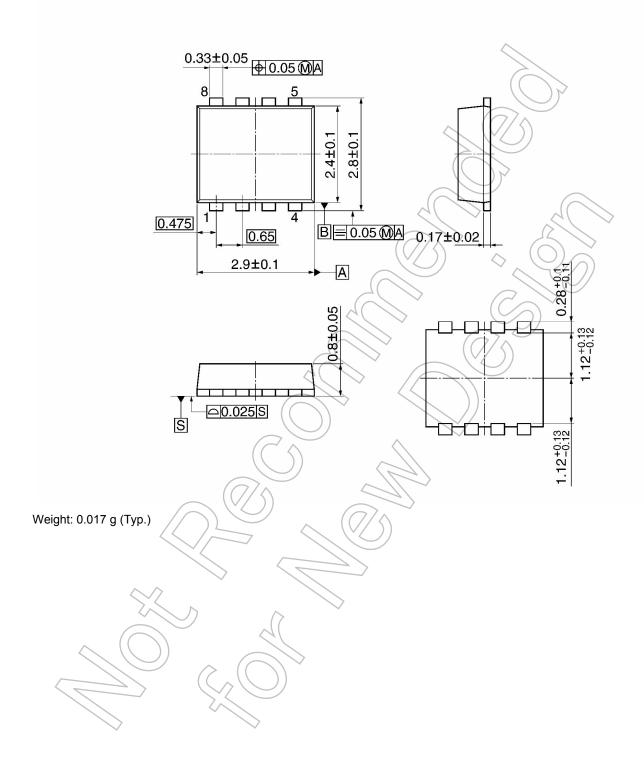
TOSHIBA



Package Dimensions

SON8-P-0303-0.65 (PS-8)

Unit: mm



RESTRICTIONS ON PRODUCT USE

- Toshiba Corporation, and its subsidiaries and affiliates (collectively "TOSHIBA"), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively "Product") without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.
- PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT ("UNINTENDED USE"). Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. IF YOU USE PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT. For details, please contact your TOSHIBA sales representative.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without
 limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile
 technology products (mass destruction weapons). Product and related software and technology may be controlled under the
 applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the
 U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited
 except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.
 Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances,
 including without limitation, the EU RoHS Directive. TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES
 OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.