Unit: mm

TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type (U-MOS V)

## **TPCC8103**

# Notebook PC Applications Portable Equipment Applications

- Small footprint due to a small and thin package
- Low drain-source ON-resistance:

$$R_{DS (ON)} = 9.4 \text{ m}\Omega \text{ (typ.) } (V_{GS} = -10 \text{ V})$$

- Low leakage current:  $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$
- Enhancement mode:  $V_{th}$  = -0.8 to -2.0 V ( $V_{DS}$  = -10 V,  $I_D$  = -1.0 mA)

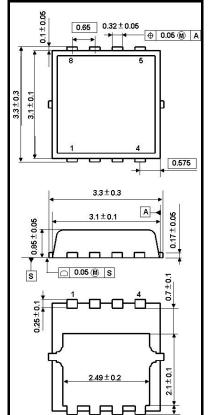
#### Absolute Maximum Ratings (Ta = 25°C)

Characte	eristic	Symbol	Rating	Unit
Drain-source voltage	Orain-source voltage		-30	V
Drain-gate voltage (R	GS = 20 kΩ)	$V_{DGR}$	-30	V
Gate-source voltage		$V_{GSS}$	±20	V
Drain current	DC (Note 1)	I <sub>D</sub>	-18	А
Drain current	Pulsed (Note 1)	$I_{DP}$	<b>−54</b>	A
Drain power dissipati	on (Tc = 25°C)	$P_{D}$	27	W
Drain power dissipati	on (t = 10 s) (Note 2a)	$P_{D}$	1.9	W
Drain power dissipati	on (t = 10 s) (Note 2b)	P <sub>D</sub>	0.7	W
Single-pulse avalance	ne energy (Note 3)	E <sub>AS</sub>	84	mJ
Avalanche current		I <sub>AR</sub>	-18	Α
Repetitive avalanche (To	energy = 25°C) (Note 4)	E <sub>AR</sub>	1.59	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.02 g (typ.)

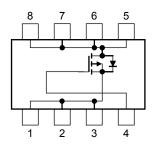
1,2,3:SOURCE

5,6,7,8:DRAIN

JEDEC JEITA TOSHIBA 4:GATE

2-3X1A

#### **Circuit Configuration**

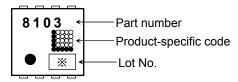


Start of commercial production 2009-06

#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc = 25°C)	R <sub>th (ch-c)</sub>	4.7	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R <sub>th (ch-a)</sub>	66	°C/W
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2b)	R <sub>th (ch-a)</sub>	180	°C/W

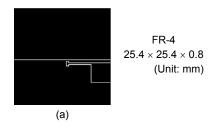
#### Marking (Note 5)

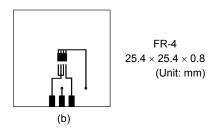


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)





Note 3:  $V_{DD} = -24~V,~T_{ch} = 25^{\circ}C$  (initial), L = 200  $\mu H,~R_G = 25~\Omega,~I_{AR} = -18~A$ 

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: \* Weekly code: (Three digits)



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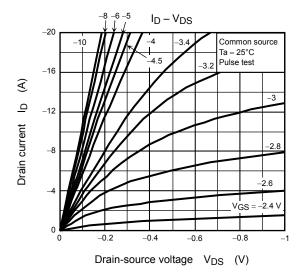
## **Electrical Characteristics (Ta = 25°C)**

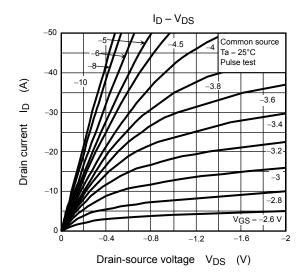
Ch	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cui	rent	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cutoff curre	ent	I <sub>DSS</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА
Drain-source bre	akdown voltago	V <sub>(BR)</sub> DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-30	_	_	V
Diain-source bre	akdown voltage	V <sub>(BR) DSX</sub>	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	-13	±100 	v	
Gate threshold ve	oltage	V <sub>th</sub>	$V_{DS} = -10 \text{ V}, I_D = -1.0 \text{ mA}$	—     17     25       —     9.4     12       15     30     —		٧	
Drain-source ON	rosistanco	Pro (OVI)	$V_{GS} = -4 \text{ V}, I_D = -9 \text{ A}$	<u> </u>		25	mΩ
Diain-source Oiv	-iesistance	R <sub>DS</sub> (ON)	$V_{GS} = -10 \text{ V}, I_D = -9 \text{ A}$	-30     —       -13     —       -0.8     —       -2.0       —     17       25       —     9.4       12       15     30       —     1600       —     340       —     490       —     9.3       —     16       —     16	1115.2		
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = -10 \text{ V}, I_D = -9 \text{ A}$	15	30	_	S
Input capacitance	е	C <sub>iss</sub>		_	1600	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	340	_	pF
Output capacitance		C <sub>oss</sub>		_	490	_	
	Rise time	t <sub>r</sub>	$V_{GS} = -9 \text{ A}$	_	9.3	_	- ns
Conitabilia a tima a	Turn-on time	t <sub>on</sub>	R <sub>L</sub> = 1.67Ω	_	16	±100 -10   -2.0 25	
Switching time Fall tim	Fall time	t <sub>f</sub>	V <sub>DD</sub> ≈ −15 V	_	68	_	
	Turn-off time	t <sub>off</sub>	Duty ≤ 1%, t <sub>W</sub> = 10 μs	_	175	_	
Total gate charge (gate-source plus		Qg	V <sub>DD</sub> ≈ -24 V, V <sub>GS</sub> = -10 V,		38	38 —	
Gate-source charge 1		Q <sub>gs1</sub>	I <sub>D</sub> = -18 A		4.5		nC
Gate-drain ("Mille	er") charge	Q <sub>gd</sub>			11		

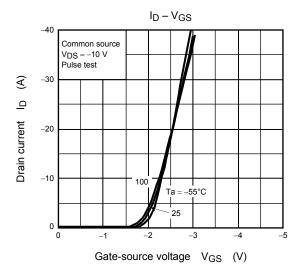
### **Source-Drain Ratings and Characteristics (Ta = 25°C)**

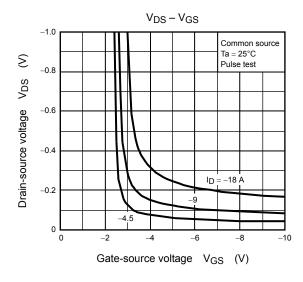
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I <sub>DRP</sub>	_	_	_	-54	Α
Forward voltage (diode)			$V_{DSF}$	$I_{DR} = -18 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.2	V

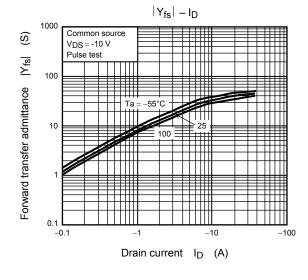
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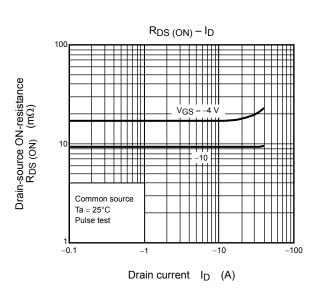




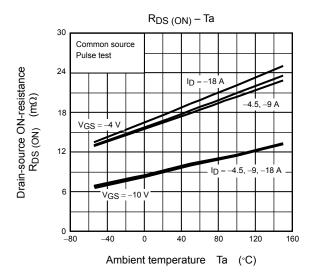


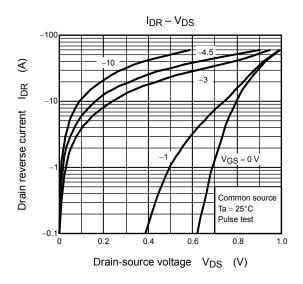


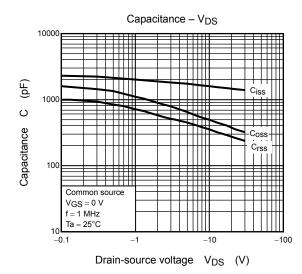


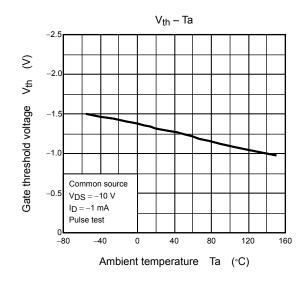


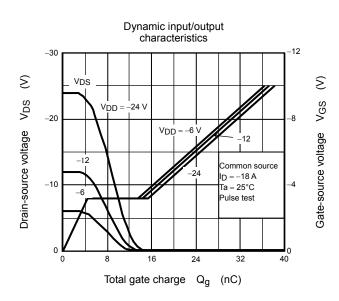
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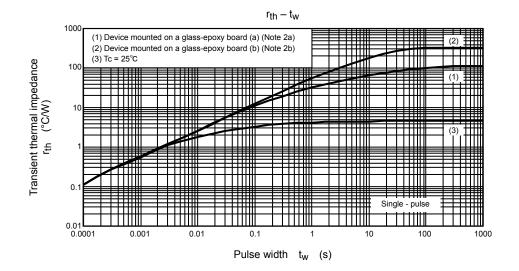


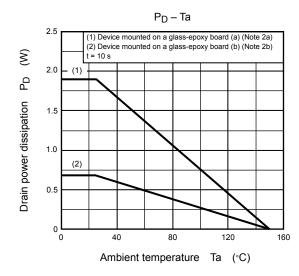


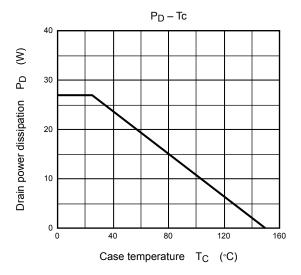


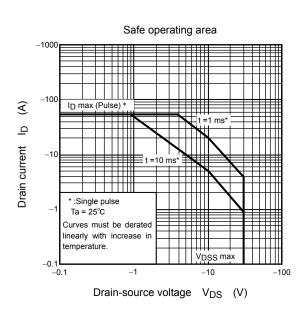


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