TOSHIBA Field-Effect Transistor Silicon N-Channel MOS Type

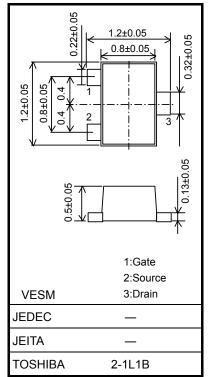
SSM3K36MFV

○ High-Speed Switching Applications

- 1.5-V drive
- Low ON-resistance: R_{on} = 1.52 Ω (max) (@V_{GS} = 1.5 V)
 - : $R_{on} = 1.14 \Omega (max) (@V_{GS} = 1.8 V)$
 - : R_{on} = 0.85 Ω (max) (@V_{GS} = 2.5 V)
 - : R_{on} = 0.66 Ω (max) (@V_{GS} = 4.5 V)
 - : R_{on} = 0.63 Ω (max) (@V_{GS} = 5.0 V)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	20	V	
Gate-source voltage		V _{GSS}	± 10	V	
Drain current	DC	۱ _D	500	mA	
	Pulse	I _{DP}	1000		
Drain power dissipation		P _D (Note 1)	150	mW	
Channel temperature		T _{ch}	150	°C	
Storage temperature		T _{stg}	–55 to 150	°C	



Weight: 1.5 mg (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

temperature, etc.) may cause this product to decrease in the

reliability significantly even if the operating conditions (i.e. operating

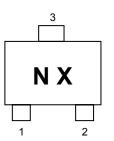
temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

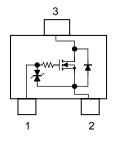
Note1:Mounted on an FR4 board

(25.4 mm \times 25.4 mm \times 1.6 mm, Cu Pad: 0.585 mm²)

Marking



Equivalent Circuit (top view)



Start of commercial production 2008-02

Unit: mm

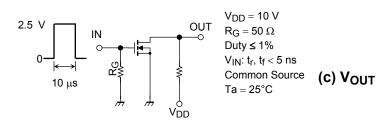
Electrical Characteristics (Ta = 25°C)

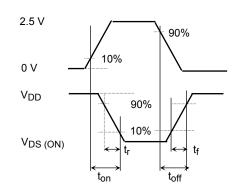
Char	acteristics	Symbol	Test Conditions	Min	Тур.	Max	Unit
Drain-source breakdown voltage	V (BR) DSS	$I_D = 1 \text{ mA}, V_{GS} = 0$	20	_	_	V	
	V (BR) DSX	$I_D = 1 \text{ mA}, V_{GS} = -10 \text{ V}$	12	—	_	v	
Drain cutoff currer	nt	I _{DSS}	$V_{DS} = 20 V, V_{GS} = 0$	_	_	1	μA
Gate leakage curr	ent	I _{GSS}	$V_{GS}=\pm 10~V,~V_{DS}=0$	_	—	±1	μA
Gate threshold vo	Itage	V _{th}	$V_{DS} = 3 V, I_{D} = 1 mA$	0.35	_	1.0	V
Forward transfer a	admittance	Y _{fs}	$V_{DS} = 3 V, I_D = 200 mA$ (Note2)	420	840	_	mS
Drain-source ON-resistance		$I_D = 200 \text{ mA}, V_{GS} = 5.0 \text{ V}$ (Note2)	_	0.46	0.63	Ω	
		$I_D = 200 \text{ mA}, V_{GS} = 4.5 \text{ V}$ (Note2)	_	0.51	0.66		
	R _{DS (ON)}	$I_D = 200 \text{ mA}, V_{GS} = 2.5 \text{ V}$ (Note2)	_	0.66	0.85		
		$I_D = 100 \text{ mA}, V_{GS} = 1.8 \text{ V}$ (Note2)		0.81	1.14		
		$I_D = 50 \text{ mA}, V_{GS} = 1.5 \text{ V}$ (Note2)	_	0.95	1.52		
Input capacitance Output capacitance		C _{iss}		_	46		pF
		C _{OSS}	V _{DS} = 10 V, V _{GS} = 0, f = 1 MHz	_	10.8		
Reverse transfer capacitance		C _{rss}		_	7.3		
Total Gate Charge		Qg		_	1.23		nC
Gate-Source Charge		Q _{gs}	V_{DS} = 10V, I _D = 0.5 A, V _{GS} = 4.0 V	_	0.60		
Gate-Drain Charge		Q _{gd}		—	0.63	—	
Switching time	Turn-on time	t _{on}	V _{DD} = 10 V, I _D = 200 mA	_	30		- ns
	Turn-off time	t _{off}	V_{GS} = 0 to 2.5 V, R_{G} = 50 Ω	_	75	_	
Drain-source forward voltage		V _{DSF}	$I_D = -0.5 \text{ A}, V_{GS} = 0 \text{ V}$ (Note2)	_	-0.88	-1.2	V

Note2: Pulse test

Switching Time Test Circuit

(a) Test Circuit





Usage Considerations

Let V_{th} be the voltage applied between gate and source that causes the drain current (I_D) to below (1 mA for the SSM3K36MFV). Then, for normal switching operation, V_{GS(on)} must be higher than V_{th}, and V_{GS(off)} must be lower than V_{th}. This relationship can be expressed as: V_{GS(off)} < V_{th} < V_{GS(on)}.

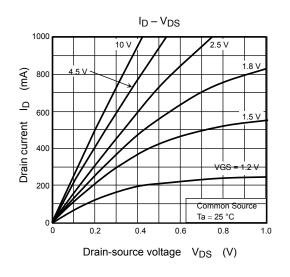
(b) V_{IN}

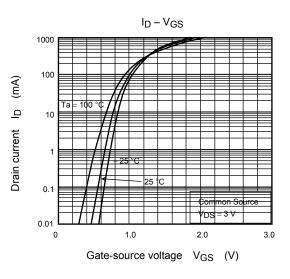
Take this into consideration when using the device.

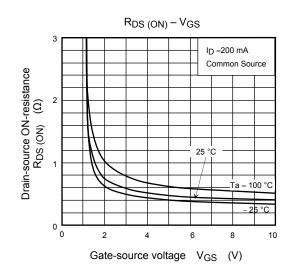
Handling Precaution

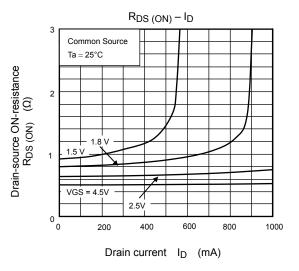
When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

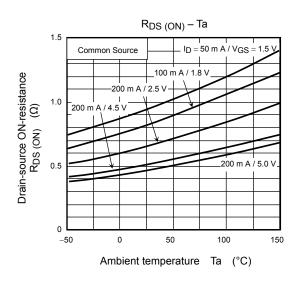
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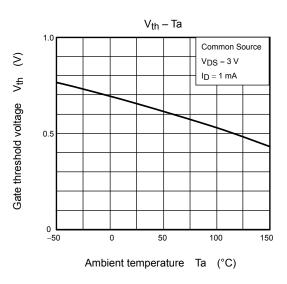




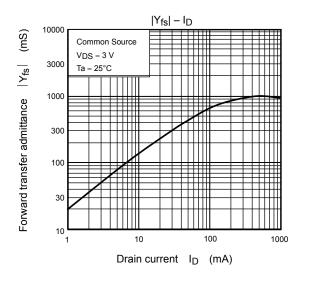


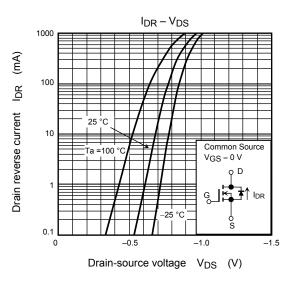


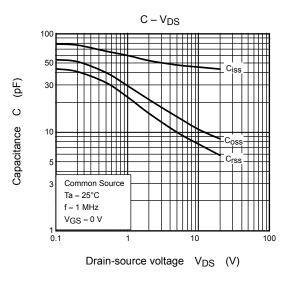


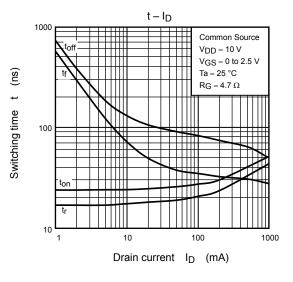


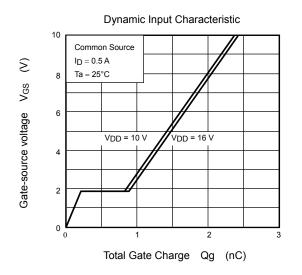
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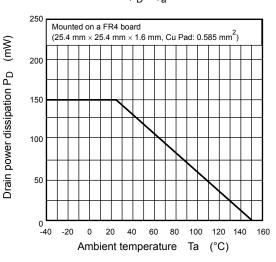








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