

CMOS Digital Integrated Circuits Silicon Monolithic

# 74LCX32FT

#### 1. Functional Description

- Low-Voltage Quad 2-Input OR Gate with 5-V Tolerant Inputs and Outputs

#### 2. General

The 74LCX32FT is a high-performance CMOS 2-input OR gate. Designed for use in 3.3 V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage  $(3.3\ V)\ V_{CC}$  applications, but it could be used to interface to 5 V supply environment for inputs.

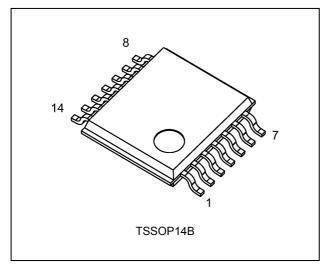
All inputs are equipped with protection circuits against static discharge.

#### 3. Features

- (1) AEC-Q100 (Rev. H) (Note 1)
- (2) Wide operating temperature range:  $T_{opr} = -40$  to 125 °C
- (3) Low-voltage operation:  $V_{CC} = 1.65$  to 3.6 V
- (4) High-speed operation:  $t_{pd} = 6.5 \text{ ns (max)} (V_{CC} = 3.3 \pm 0.3 \text{ V})$
- (5) Output current:  $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- (6) Power-down protection provided on all inputs and outputs
- (7) Pin and function compatible with the 74 series (74LVC/ALVC etc.) 32 type

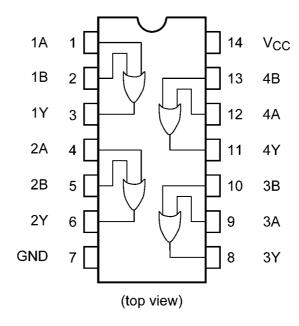
Note 1: This device is compliant with the reliability requirements of AEC-Q100. For details, contact your Toshiba sales representative.

#### 4. Packaging

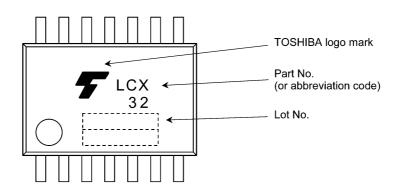




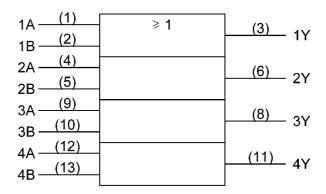
# 5. Pin Assignment



# 6. Marking



# 7. IEC Logic Symbol



Rev.3.0



#### 8. Truth Table

Inputs A	Inputs B	Outputs Y
Ĺ	L	Ĺ
L	Н	Н
Н	L	Н
Н	Н	Н

#### 9. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		-0.5 to 6.5	V
Input voltage	V <sub>IN</sub>		-0.5 to 6.5	V
Output voltage	V <sub>OUT</sub>	(Note 1)	-0.5 to 6.5	V
		(Note 2)	-0.5 to V <sub>CC</sub> + 0.5	
Input diode current	I <sub>IK</sub>		-50	mA
Output diode current	I <sub>OK</sub>	(Note 3)	±50	mA
Output current	I <sub>OUT</sub>		±50	mA
Power dissipation	P <sub>D</sub>	(Note 4)	180	mW
V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>		±100	mA
Storage temperature	T <sub>stg</sub>		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0 V$ 

Note 2: High or low state. I<sub>OUT</sub> absolute maximum rating must be observed.

Note 3:  $V_{OUT} < GND$ ,  $V_{OUT} > V_{CC}$ 

Note 4: 180 mW in the range of  $T_a$  = -40 to 85 °C. From  $T_a$  = 85 to 125 °C a derating factor of -3.25 mW/°C shall be applied until 50 mW.



# 10. Operating Ranges (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		1.65 to 3.6	V
		(Note 1)	1.5 to 3.6	
Input voltage	$V_{IN}$		0 to 5.5	V
Output voltage	V <sub>OUT</sub>	(Note 2)	0 to 5.5	V
		(Note 3)	0 to V <sub>CC</sub>	
Output current	I <sub>OH</sub> ,I <sub>OL</sub>	(Note 4)	±24	mA
		(Note 5)	±12	
Operating temperature	T <sub>opr</sub>		-40 to 125	°C
Input rise and fall times	dt/dv	(Note 6)	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either  $V_{CC}$  or GND.

Note 1: Data retention only

Note 2:  $V_{CC} = 0 V$ 

Note 3: High or low state Note 4:  $V_{CC}$  = 3.0 to 3.6 V

Note 5:  $V_{CC}$  = 2.7 to 3.0 V

Note 6:  $V_{IN}$  = 0.8 to 2.0 V,  $V_{CC}$  = 3.0 V

#### 11. Electrical Characteristics

# 11.1. DC Characteristics (Unless otherwise specified, T<sub>a</sub> = -40 to 85 °C)

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
High-level input voltage	V <sub>IH</sub>	_		1.65 to 2.3	$V_{CC} \times 0.9$	_	V
				2.3 to 2.7	1.7	_	
				2.7 to 3.6	2.0	_	
Low-level input voltage	V <sub>IL</sub>	_		1.65 to 2.3	_	V <sub>CC</sub> × 0.1	V
				2.3 to 2.7	_	0.7	
				2.7 to 3.6	_	0.8	
High-level output voltage	V <sub>OH</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OH}$ = -100 $\mu$ A	1.65 to 3.6	V <sub>CC</sub> - 0.2		\ \
			$I_{OH}$ = -4 mA	1.65	1.05		
			$I_{OH}$ = -8 mA	2.3	1.7	_	
			I <sub>OH</sub> = -12 mA	2.7	2.2	_	
			I <sub>OH</sub> = -18 mA	3.0	2.4	_	
			I <sub>OH</sub> = -24 mA	3.0	2.2	_	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OL</sub> = 100 μA	1.65 to 3.6	_	0.2	V
			I <sub>OL</sub> = 4 mA	1.65	_	0.45	
			I <sub>OL</sub> = 8 mA	2.3	_	0.7	
			I <sub>OL</sub> = 12 mA	2.7	_	0.4	
			I <sub>OL</sub> = 16 mA	3.0	_	0.4	
			I <sub>OL</sub> = 24 mA	3.0	_	0.55	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V		1.65 to 3.6	_	±5.0	μА
Power-OFF leakage current	I <sub>OFF</sub>	V <sub>IN</sub> /V <sub>OUT</sub> = 5.5 V		0	_	10.0	μА
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		1.65 to 3.6		10.0	μА
		V <sub>IN</sub> = 3.6 to 5.5 V		1.65 to 3.6	_	±10.0	
Quiescent supply current	Δl <sub>CC</sub>	V <sub>IH</sub> = V <sub>CC</sub> - 0.6 V (per 1 input)		2.7 to 3.6	_	500	μА



# 11.2. DC Characteristics (Unless otherwise specified, T<sub>a</sub> = -40 to 125 °C)

Characteristics	Symbol	Test Condition	ı	V <sub>CC</sub> (V)	Min	Max	Unit
High-level input voltage	V <sub>IH</sub>	_		1.65 to 2.3	$V_{CC} \times 0.9$	_	V
				2.3 to 2.7	1.7	_	
				2.7 to 3.6	2.0	_	
Low-level input voltage	V <sub>IL</sub>	_		1.65 to 2.3	_	V <sub>CC</sub> × 0.1	٧
				2.3 to 2.7	_	0.7	
				2.7 to 3.6	_	0.8	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -100 μA	1.65 to 3.6	V <sub>CC</sub> - 0.2	_	٧
			$I_{OH}$ = -4 mA	1.65	0.9	_	
			$I_{OH}$ = -8 mA	2.3	1.55	_	
			$I_{OH}$ = -12 mA	2.7	2.0	_	
			$I_{OH}$ = -18 mA	3.0	2.2	_	
			I <sub>OH</sub> = -24 mA	3.0	2.0	_	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IL}$	$I_{OL}$ = 100 $\mu$ A	1.65 to 3.6	_	0.2	٧
			$I_{OL}$ = 4 mA	1.65	_	0.65	
			$I_{OL}$ = 8 mA	2.3	_	0.9	
			$I_{OL}$ = 12 mA	2.7	_	0.6	
			$I_{OL}$ = 16 mA	3.0	_	0.6	
			I <sub>OL</sub> = 24 mA	3.0	_	0.75	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V		1.65 to 3.6	_	±20.0	μΑ
Power-OFF leakage current	I <sub>OFF</sub>	$V_{IN}/V_{OUT} = 5.5 V$		0	_	40.0	μА
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		1.65 to 3.6	_	40.0	μΑ
	Icc	V <sub>IN</sub> = 3.6 to 5.5 V		1.65 to 3.6	_	±40.0	
Quiescent supply current	Δl <sub>CC</sub>	V <sub>IH</sub> = V <sub>CC</sub> - 0.6 V (per 1 input)		2.7 to 3.6	_	5.0	mA

# 11.3. AC Characteristics (Unless otherwise specified, T<sub>a</sub> = -40 to 85 °C)

Characteristics	Symbol	Note	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		See 11.7 AC Test Circuit,	$1.8 \pm 0.15$	_	20.0	ns
		Fig. 11.8.1,	Fig. 11.8.1, Table 11.8.1	$2.5 \pm 0.2$	_	7.2	
				2.7	_	6.2	
				$3.3 \pm 0.3$	1.5	5.5	
Output skew	t <sub>osLH</sub> ,t <sub>osHL</sub>	(Note 1)		2.7	_	_	ns
				$3.3\pm0.3$	_	1.0	

Note 1: Parameter guaranteed by design. ( $t_{osLH} = |t_{PLH}m - t_{PLH}n|$ ,  $t_{osHL} = |t_{PHL}m - t_{PHL}n|$ )

# 11.4. AC Characteristics (Unless otherwise specified, $T_a$ = -40 to 125 °C)

Characteristics	Symbol	Note	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time	$t_{PLH}, t_{PHL}$		See 11.7. AC Test Circuit,	$1.8\pm0.15$	_	22.0	ns
			Table 11.8.1, Fig. 11.8.1	$2.5 \pm 0.2$		8.0	
				2.7		7.0	
				$3.3 \pm 0.3$	1.5	6.5	
Output skew	$t_{osLH}, t_{osHL}$	(Note 1)	_	2.7			ns
				$3.3 \pm 0.3$	_	1.0	

Note 1: Parameter guaranteed by design. ( $t_{osLH} = |t_{PLH}m - t_{PLH}n|$ ,  $t_{osHL} = |t_{PHL}m - t_{PHL}n|$ )



# 11.5. Dynamic Switching Characteristics (Unless otherwise specified, $T_a$ = 25 °C, Input: $t_r$ = $t_f$ = 2.5 ns, $C_L$ = 50 pF, $R_L$ = 500 $\Omega$ )

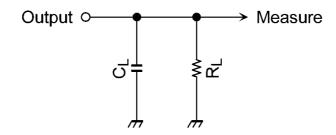
Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Quiet output maximum dynamic V <sub>OL</sub>	V <sub>OLP</sub>	V <sub>IH</sub> = 3.3 V,V <sub>IL</sub> = 0 V	3.3	0.8	V
Quiet output minimum dynamic V <sub>OL</sub>	V <sub>OLV</sub>	V <sub>IH</sub> = 3.3 V,V <sub>IL</sub> = 0 V	3.3	8.0	V

# 11.6. Capacitive Characteristics (Unless otherwise specified, T<sub>a</sub> = 25 °C)

Characteristics	Symbol	Note	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>			3.3	7	pF
Output capacitance	C <sub>OUT</sub>			0	8	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 1)	f <sub>IN</sub> =10 MHz	3.3	25	pF

Note 1:  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.  $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{|N} + I_{CC}/4 \text{ (per 1 gate)}$ 

#### 11.7. AC Test Circuit



#### 11.8. AC Waveform

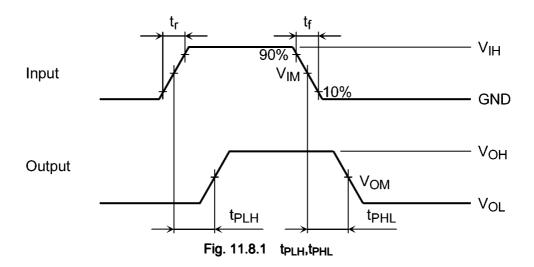


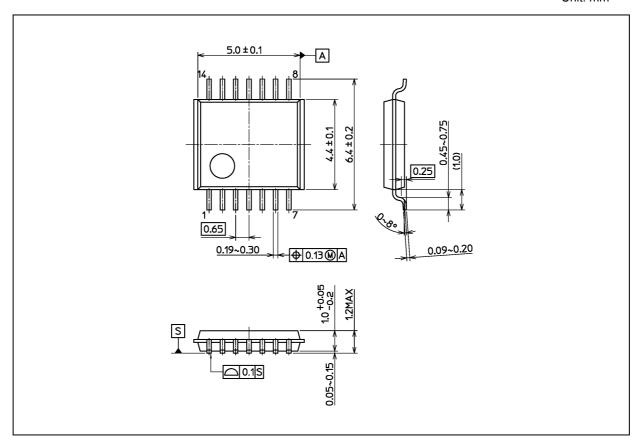
Table 11.6.1 AC Waveform Symbols

	Symbol	$V_{CC} = 3.3 \pm 0.3 \text{ V}$ $V_{CC} = 2.7 \text{ V}$	$V_{CC}$ = 2.5 ± 0.2 V	$V_{CC}$ = 1.8 $\pm$ 0.15 V
Input	V <sub>IH</sub>	2.7 V	V <sub>CC</sub>	V <sub>CC</sub>
	V <sub>IM</sub>	1.5 V	V <sub>CC</sub> /2	V <sub>CC</sub> /2
	t <sub>r</sub> , t <sub>f</sub>	2.5 ns	2.0 ns	2.0 ns
Output	V <sub>OM</sub>	1.5 V	V <sub>OH</sub> /2	V <sub>OH</sub> /2
Load	C <sub>L</sub>	50 pF	30 pF	30 pF
	$R_L$	500 Ω	500 Ω	1 kΩ



# **Package Dimensions**

Unit: mm



Weight: 0.054 g (typ.)

	Package Name(s)
Nickname: TSSOP14B	

Rev.3.0



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