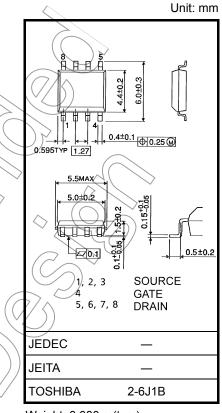
TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS III)

TPC8110

Lithium Ion Battery Applications
Notebook PC Applications
Portable Equipment Applications

- Small footprint due to small and thin package
- Low drain-source ON resistance: RDS (ON) = 17 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 16 \mathrm{S}$ (typ.)
- Low leakage current: $IDSS = -10 \mu A \text{ (max) (VDS} = -40 \text{ V)}$
- Enhancement mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V } (V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA})$

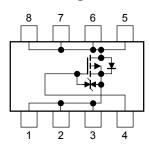


Weight: 0.080 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DS} S	-4 0	X	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	-4 0	V	
Gate-source voltage		VGSS	±20	\ v	
Drain current	DC (Note 1)	(AD)	-8	A	
	Pulsed(Note 1)	/\dpp	-32	<u>A</u>	
Drain power dissipation (t ≠ 10 s) (Note 2a)		PD	1.9	W	
Drain power dissipation (t = 10 s) (Note 2b)		PD	1.0	W	
Single pulse avalanche energy (Note 3)		EAS	59.4	mJ	
Avalanche current		I _{AR}	<u></u> −8	Α	
Repetitive avalanche energy (Note 2a) (Note 4)		EAR	0.19	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Circuit Configuration



Note: (Note 1), (Note 2), (Note 3) and (Note 4): See the next page.

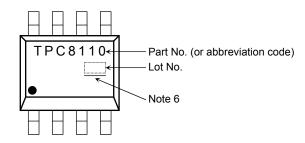
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Please handle with caution.

Thermal Characteristics

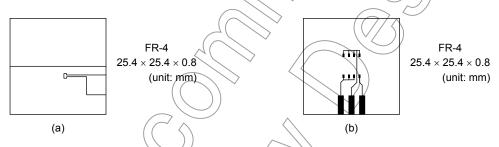
Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W	
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W	

Marking (Note 5)



Note 1: Ensure that the channel temperature does not exceed 150°C

Note 2: (a) Device mounted on a glass-epoxy board (b) Device mounted on a glass-epoxy board (b)



Note 3: $V_{DD} = -24 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 1.0 mH, $R_G = 25 \Omega$, $L_{AR} = -8 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: • on the lower left of the marking indicates Pin 1

* Weekly code: (Three digits)

Week of manufacture
(01 for first week of year, continuing up to 52 or 53)

Year of manufacture
(The last digit of the calendar year)

Note 6: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Rb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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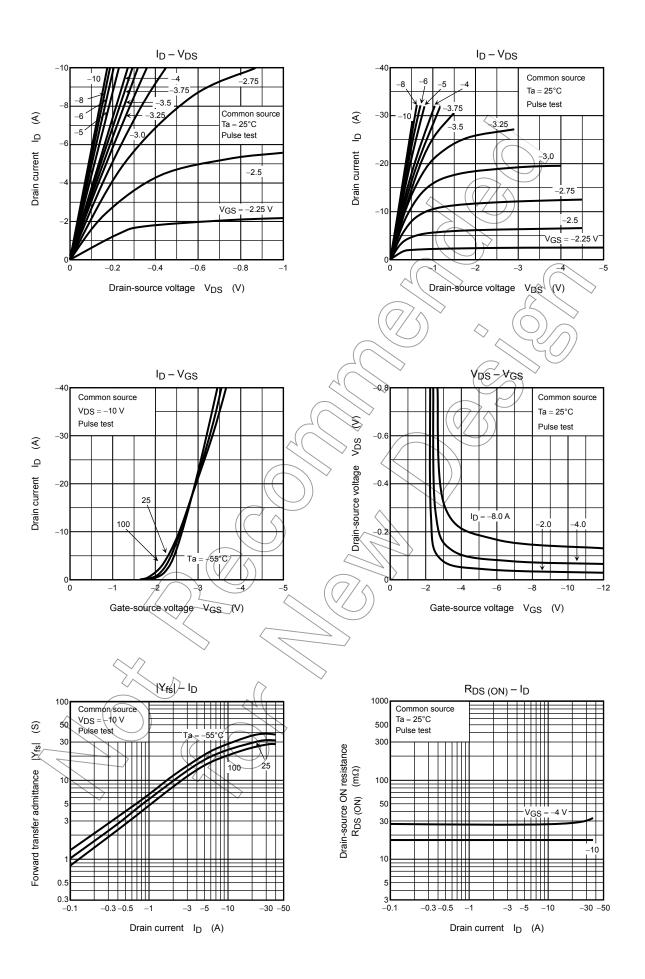
Electrical Characteristics (Ta = 25°C)

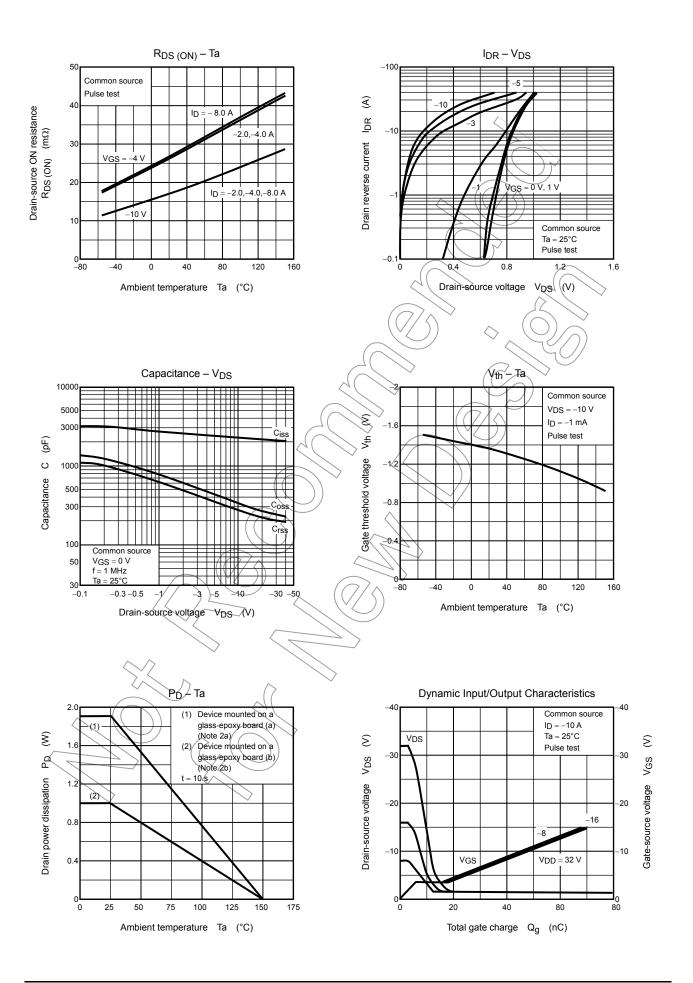
Characte	eristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА	
Drain cut-OFF current		I _{DSS}	$V_{DS} = -40 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА	
Drain-source breakdown voltage		V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$ -40 -		_		V	
		V _{(BR) DSX}	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	25			V	
Gate threshold voltage		V_{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	0.8) >	-2.0	>	
Drain-source ON resistance		R _{DS} (ON)	$V_{GS} = -4 \text{ V}, I_D = -4.0 \text{ A}$) />	27	35	- mΩ	
			$V_{GS} = -10 \text{ V}, I_D = -4.0 \text{ A}$	\rightarrow	17	25		
Forward transfer admi	ttance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -4.0 \text{ A}$	8	16		S	
Input capacitance		C _{iss}			2180			
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		275		pF	
Output capacitance		Coss			330	\checkmark		
Switching time	Rise time	t _r	V _{GS} 0 V 1 10 = -4 A		6.0			
	Turn-ON time	t _{on}	Vout		15	_	- ns	
	Fall time	t _f	A 4 4 4 1 8 1 8 1 8 1 8 1 8 1 8 1 8 1 8 1	(\mathcal{D})	30			
	Turn-OFF time	t _{off}	V _{DD} ≈ 20 V Duty ≤ 1%, t _w = 10 μs) —	115	_		
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD} ≈ -32 V, V _{GS} = -10 V,		48	_	-	
Gate-source charge 1		Q _{gs1}	I _D = -8 A	_	5.5	_	nC	
Gate-drain ("miller") charge		Qgd		_	12	_		

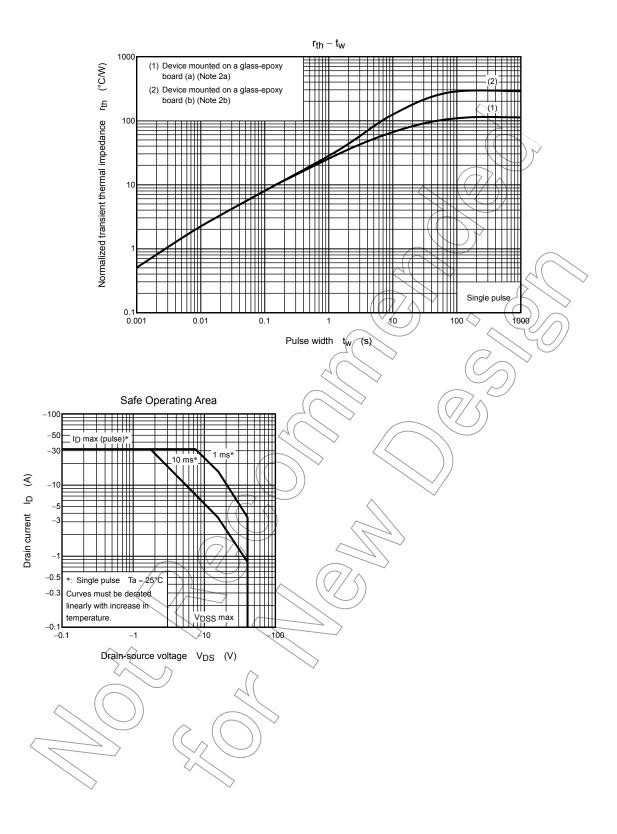
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol Test Condi	ition Min	Тур.	Max	Unit
Drain reverse current Pulse (Note-1)	I _{DRP} —	_	_	-32	Α
Forward voltage (diode)	V_{DSE} $I_{DR} = -8 \text{ A}, V_{GS} = 0 \text{ V}$	V	_	1.2	V









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