Unit: mm

TOSHIBA Field Effect Transistor Silicon P Channel MOS Type

2SJ305

High Speed Switching Applications Analog Applications

- High input impedance
- + Low gate threshold voltage.: V_{th} = –0.5 to –1.5 V

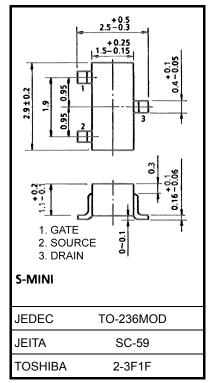
Absolute Maximum Ratings (Ta = 25°C)

• Excellent switching times.: $t_{on} = 0.06 \ \mu s \ (typ.)$

 $t_{off} = 0.15 \ \mu s \ (typ.)$

- Low drain-source ON resistance: R_{DS} (ON) = 2.4 Ω (typ.)
- Small package.
- Complementary to 2SK2009

Characteristics Symbol Rating Unit v -30 Drain-source voltage VDS V Gate-source voltage V_{GSS} ±20 DC drain current I_D -200 mΑ 200 mW Drain power dissipation P_D Channel temperature 150 °C T_{ch} °C Storage temperature range -55 to 150 Tstg



Weight: 0.012 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

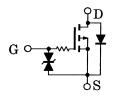
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note: This transistor is electrostatic sensitive device. Please handle with caution.

Marking



Equivalent Circuit

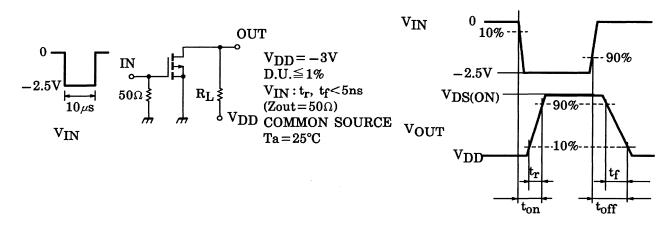


Start of commercial production 1992-04

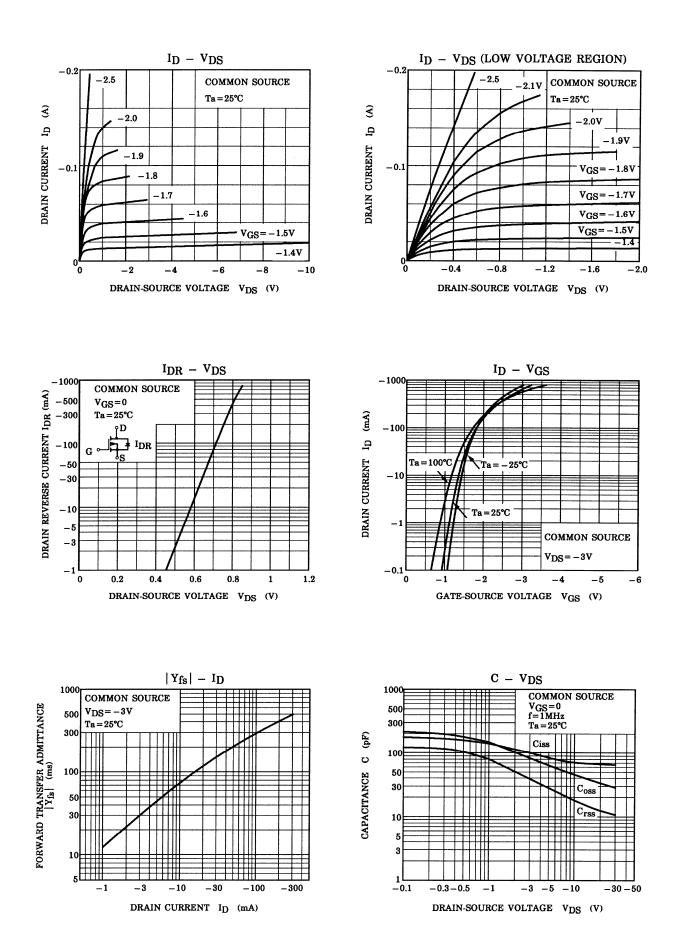
Electrical Characteristics (Ta = 25°C)

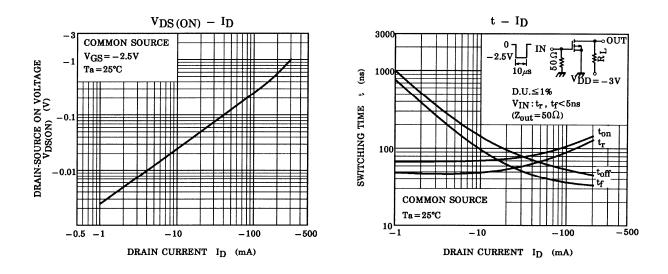
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS}=\pm 10~V,~V_{DS}=0$	_		±0.1	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = -1 \text{ mA}, V_{GS} = 0$	-30	_	_	V
Drain cut-off current		I _{DSS}	$V_{DS} = -30 V, V_{GS} = 0$	_	_	-10	μA
Gate threshould voltage		V _{th}	$V_{DS} = -3 V, I_D = -0.1 mA$	-0.5		-1.5	V
Forward transfer admittance		Y _{fs}	$V_{DS} = -3 \text{ V}, \text{ I}_{D} = -50 \text{ mA}$	100			mS
Drain-source ON resistance		R _{DS (ON)}	$I_D = -50$ mA, $V_{GS} = -2.5$ V	_	2.4	4	Ω
Input capacitance		C _{iss}	$V_{DS} = -3 \text{ V}, V_{GS} = 0, f = 1 \text{MHz}$	_	92		pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = -3 \text{ V}, V_{GS} = 0, f = 1 \text{MHz}$	_	36	_	pF
Output capacitance		C _{oss}	$V_{DS} = -3 \text{ V}, V_{GS} = 0, f = 1 \text{MHz}$	_	80	_	pF
Switching time	Turn-on time	t _{on}	$V_{DD} = -3 V$, $I_D = -10 mA$ $V_{GS} = 0 to -2.5 V$	_	0.06	_	μs
	Turn-off time	t _{off}	$V_{DD} = -3 \text{ V}, \text{ I}_D = -10 \text{ mA}$ $V_{GS} = 0 \text{ to } -2.5 \text{ V}$	_	0.15	_	

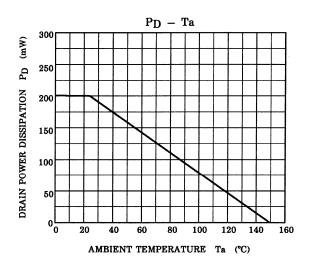
Switching Time Test Circuit



TOSHIBA







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