TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC4584BP, TC4584BF

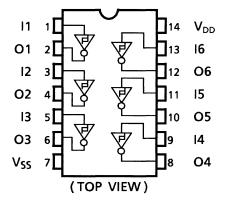
## TC4584B Hex Schmitt Trigger

The TC4584B is the 6-circuit inverter having the Schmitt trigger function at the input terminal.

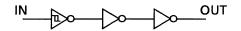
That is, since the circuit threshold level voltages at the leading and trailing edges of input waveform are different ( $V_P$ ,  $V_N$ ), the TC4584B can be used in the broad range application including line receiver, waveform shaping circuit, astable multivibrator, monostable multivibrator in addition to ordinary inverter.

Since the pins are compatible with the TC4069UB, the substitution is also possible.

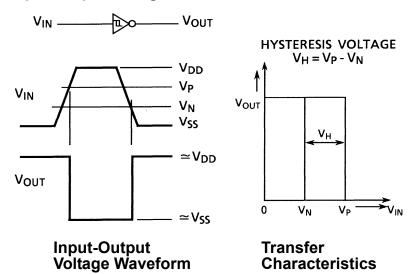
#### **Pin Assignment**

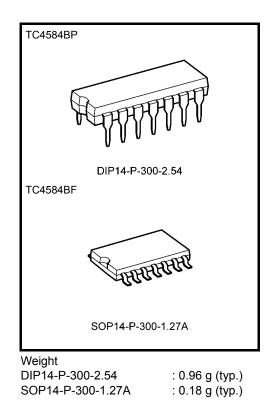


### Logic Diagram



### Input/Output Voltage Characteristic





Start of commercial	production
	1978-05

## **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
DC supply voltage	V <sub>DD</sub>	$V_{SS}{-}0.5$ to $V_{SS}{+}20$	V
Input voltage	V <sub>IN</sub>	$V_{\mbox{\scriptsize SS}}$ – 0.5 to $V_{\mbox{\scriptsize DD}}$ + 0.5	V
Output voltage	V <sub>OUT</sub>	$V_{SS}-0.5$ to $V_{DD}+0.5$	V
DC input current	I <sub>IN</sub>	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T <sub>opr</sub>	-40 to 85	°C
Storage temperature range	T <sub>stg</sub>	–65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

## **Operating Ranges (V<sub>SS</sub> = 0 V) (Note)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V <sub>DD</sub>	—	3	_	18	V
Input voltage	V <sub>IN</sub>	_	0		V <sub>DD</sub>	V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{DD}$  or  $V_{SS}$ .

# Static Electrical Characteristics ( $V_{SS} = 0 V$ )

		Sym-	Test Condition		-40°C		25°C			85°C		
Charac	teristics	bol		V <sub>DD</sub> (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
				5	4.95	_	4.95	5.00		4.95	_	
High-level voltage	output	V <sub>OH</sub>	I <sub>OUT</sub>   < 1 μΑ	10	9.95	—	9.95	10.00	—	9.95	—	V
			$V_{IN} = V_{SS}, V_{DD}$	15	14.95	—	14.95	15.00	—	14.95	—	
			I <sub>OUT</sub>   < 1 μΑ	5	_	0.05		0.00	0.05	_	0.05	
Low-level voltage	output	V <sub>OL</sub>	$V_{IN} = V_{SS}, V_{DD}$	10	—	0.05	—	0.00	0.05	—	0.05	V
· · · · · · · · · · · · · · · · · · ·			$v_{IN} = v_{SS}, v_{DD}$	15	—	0.05	—	0.00	0.05	—	0.05	
			V <sub>OH</sub> = 4.6 V	5	-0.61	_	-0.51	-1.0	_	-0.42	_	
			V <sub>OH</sub> = 2.5 V	5	-2.50	—	-2.10	-4.0	—	-1.70	—	
Output hig	h current	I <sub>OH</sub>	V <sub>OH</sub> = 9.5 V	10	-1.50	—	-1.30	-2.2	—	-1.10	—	mA
			V <sub>OH</sub> = 13.5 V	15	-4.00	—	-3.40	-9.0	—	-2.80	—	
			$V_{IN} = V_{SS}$									
		IOL	$V_{OL} = 0.4 V$	5	0.61	_	0.51	1.5		0.42		mA
Output			$V_{OL} = 0.5 V$	10	1.50	_	1.30	3.8	_	1.10	_	
Output low	v current		V <sub>OL</sub> = 1.5 V	15	4.00	—	3.40	15.0	_	2.80	—	
			$V_{IN} = V_{DD}$									
			V <sub>OUT</sub> = 0.5 V	5	2.05	3.75	2.15	3.0	3.75	2.15	3.85	
Positive tri		VP	V <sub>OUT</sub> = 1.0 V	10	4.80	7.60	4.90	6.4	7.60	4.90	7.70	V
	renage		V <sub>OUT</sub> = 1.5 V	15	7.80	11.60	7.90	9.9	11.60	7.90		
			V <sub>OUT</sub> = 4.5 V	5	1.25	2.95	1.25	2.3	2.85	1.15	2.85	
Negative t		V <sub>N</sub>	V <sub>OUT</sub> = 9.0 V	10	2.40	5.20	2.40	3.8	5.10	2.30	5.10	V
	renage		V <sub>OUT</sub> = 13.5 V	15	3.40	7.20	3.40	5.2	7.10	3.30	7.10	
				5	0.10	1.25	0.25	0.65	1.25	0.25	1.40	
Hysteresis voltage		VH	_	10	1.80	3.50	1.90	2.60	3.50	1.90	3.60	V
				15	3.70	5.60	3.80	4.70	5.60	3.80	5.70	
Input	"H" level	IIH	V <sub>IH</sub> = 18 V	18	—	0.1		10 <sup>-5</sup>	0.1	—	1.0	
current	"L" level	١ <sub>١L</sub>	$V_{IL} = 0 V$	18	—	-0.1		-10 <sup>-5</sup>	-0.1	_	-1.0	μA
	•		V <sub>IN</sub> = V <sub>SS</sub> , V <sub>DD</sub> (Note)	5	—	1		0.001	1	_	7.5	
Quiescent current	supply	I <sub>DD</sub>		10	—	2		0.002	2	—	15.0	μA
				15	—	4		0.004	4	—	30.0	

Note: All valid input combinations.

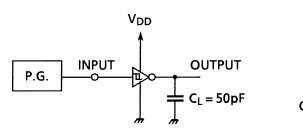
# Dynamic Electrical Characteristics ( $Ta = 25^{\circ}C$ , $V_{SS} = 0 V$ , $C_{L} = 50 pF$ )

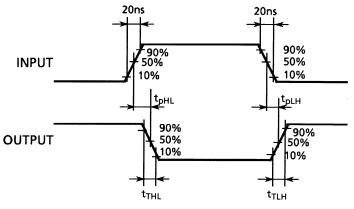
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Characteristics	Gymbol		V <sub>DD</sub> (V)	IVIIII	тур.	IVIAX	Unit
Output transition time			5	_	80	200	
(low to high)	t <sub>TLH</sub>	—	10	—	50	100	ns
(low to high)			15	—	40	80	
Output transition time	t⊤н∟	—	5	_	80	200	
Output transition time			10	—	50	100	ns
(high to low)			15	_	40	80	
	<sup>t</sup> pLH t <sub>pHL</sub>		5		170	340	
Propagation delay time		—	10	—	80	160	ns
			15	—	60	120	
Input capacitance	C <sub>IN</sub>			_	5	7.5	pF

## **Circuit and Waveform for Measurement of Dynamic Characteristics**

Circuit

Waveform

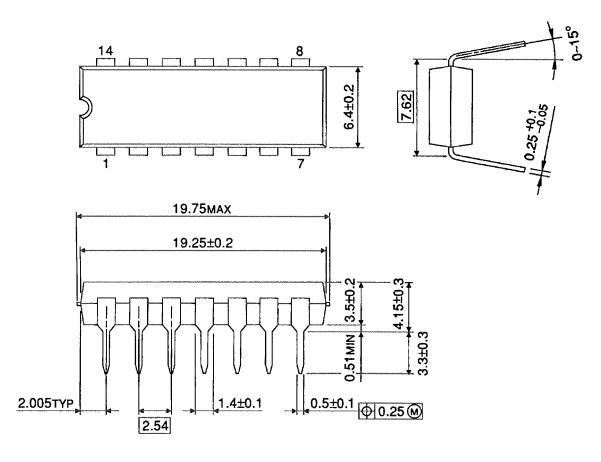




## **Package Dimensions**

DIP14-P-300-2.54

Unit : mm



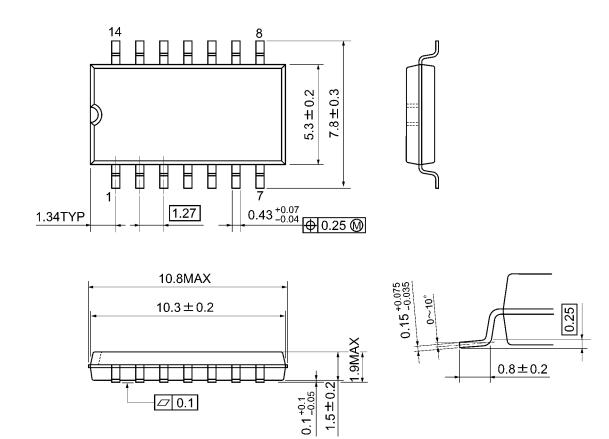
Weight: 0.96 g (typ.)



# **Package Dimensions**

SOP14-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

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