

Photocouplers GaAlAs Infrared LED & Photo IC

TLP109(IGM)

1. Applications

- Intelligent Power Module Signal Isolation
- High-Speed Digital Interfacing for Instrumentation and Control Devices
- **Industrial Inverters**

2. General

 $The\ To shiba\ TLP 109 (IGM)\ mini-flat\ coupler\ is\ a\ small-outline\ coupler\ suitable\ for\ surface-mount\ assembly.\ The$ TLP109(IGM) consists of a high-output GaAlAs light-emitting diode optically coupled to a high-speed photodiodetransistor chip. The TLP109(IGM) is housed in the SO6 package and guarantees a creepage distance of ≥5.0 mm, a clearance of ≥5.0 mm and an insulation thickness of ≥0.4mm. Therefore, the TLP109(IGM) meets the reinforced insulation class requirements of international safety standards. The TLP109(IGM) guarantees minimum and maximum of propagation delay time, switching time dispersion, and high common mode transient immunity. Therefore TLP109(IGM) is suitable for isolation interface between IPM(Intelligent Power Module) and control IC circuits in motor control application.

3. Features

- (1) Isolation voltage: 3750 Vrms (min)
- Common-mode transient immunity: 10 kV/µs (min)

$$@V_{CM} = 1500 \text{ V}$$

(3)Propagation delay time $t_{pHL}/t_{pLH} = 0.1 \mu s$ (min)

$$= 0.8 \, \mu s \, (max)$$

@
$$I_F = 10$$
 mA, $V_{CC} = 15$ V,

$$R_L = 20 \text{ k}\Omega$$
, $T_a = 25 \text{ °C}$

- Pulse width distortion: 0.7 μs (max) (|t_{pHL}-t_{pLH}|) (4)
- (5) TTL compatible
- Safety standards

UL-approved: UL1577, File No.E67349

cUL-approved: CSA Component Acceptance Service No.5A File No.E67349

VDE-approved: EN60747-5-5, EN60065 or EN60950-1 (Note 1)

CQC-approved: GB4943.1, GB8898 Thailand Factory





仅适用干海拔 2000m 以下地区安全使用

Note 1: When a VDE approved type is needed, please designate the **Option (V4)**.

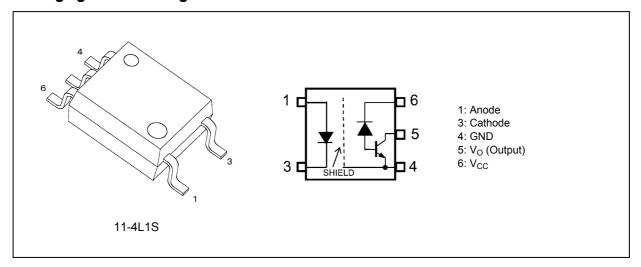
Start of commercial production

Rev.1.0

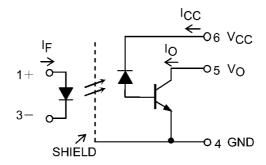
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4. Packaging and Pin Assignment



5. Internal Circuit



6. Principle of Operation

6.1. Mechanical Parameters

Characteristics	Min	Unit
Creepage distances	5.0	mm
Clearance distances	5.0	
Internal isolation thickness	0.4	



7. Absolute Maximum Ratings (Note) (Unless otherwise specified, T_a = 25 °C)

	Characteristics		Symbol	Note	Rating	Unit
LED	Input forward current		I _F		20	mA
	Input forward current derating	$(T_a \ge 95 ^{\circ}C)$	$\Delta I_F/\Delta T_a$		-0.36	mA/°C
	Input forward current (pulsed)		I _{FP}	(Note 1)	40	mA
	Peak transient input forward current		I _{FPT}	(Note 2)	1	Α
	Input reverse voltage		V_R		5	V
	Input power dissipation		P_D		40	mW
	Input power dissipation derating	(T _a ≥ 95 °C)	$\Delta P_D/\Delta T_a$		-0.72	mW/°C
	Junction temperature		Tj		125	°C
Detector	Output current		I _O		8	mA
	Output current derating	(T _a ≥ 95 °C)	$\Delta I_O/\Delta T_a$		-0.3	mA/°C
	Peak output current		I _{OP}		16	mA
	Output voltage		Vo		-0.5 to 20	V
	Supply voltage		V _{CC}		-0.5 to 30	V
	Output power dissipation		Po		100	mW
	Output power dissipation derating	$(T_a \ge 95 ^{\circ}C)$	$\Delta P_{O}/\Delta T_{a}$		-1.8	mW/°C
	Junction temperature		Tj		125	°C
Common	Operating temperature		T _{opr}		-55 to 125	
	Storage temperature		T _{stg}		-55 to 125	
	Lead soldering temperature	(10 s)	T _{sol}		260	
	Isolation voltage	(AC, 60 s, R.H. ≤ 60 %)	BV _S	(Note 3)	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width (PW) \leq 1 ms, duty = 50 %

Note 2: Pulse width (PW) \leq 1 $\mu s,\,300~pps$

Note 3: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4, 5 and 6 are shorted together.

8. Electrical Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input forward voltage	V _F			I _F = 16 mA	1.5	1.64	1.85	V
Input forward voltage temperature coefficient	$\Delta V_F/\Delta T_a$			I _F = 16 mA		-1.8		mV/°C
Input reverse current	I _R			V _R = 3 V	_	_	10	μА
Input capacitance	Ct			V = 0 V, f = 1 MHz	_	60	_	pF
High-level output current	I _{OH}			$I_F = 0 \text{ mA}, V_O = V_{CC} = 5.5 \text{ V}$	_	3	500	nA
				I _F = 0 mA, V _O = 20 V, V _{CC} = 30 V		_	5	μА
				$I_F = 0 \text{ mA}, V_O = 20 \text{ V},$ $V_{CC} = 30 \text{ V}, T_a = 100 ^{\circ}\text{C}$	_	_	50	
High-level supply current	I _{CCH}			I _F = 0 mA, V _{CC} = 30 V	_	0.01	1	μА
Supply voltage	V _{CC}			I _{CC} = 0.01 mA	30	_	_	V
Output voltage	Vo			I _O = 0.5 mA	20	_		V



9. Coupled Electrical Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Current transfer ratio	I _O /I _F	I_F = 10 mA, V_{CC} = 4.5 V, V_O = 0.4 V	25	35	75	%
		I_F = 10 mA, V_{CC} = 4.5 V, V_O = 0.4 V, T_a = -25 to 100 °C	15			
Low-level output voltage	V _{OL}	I_F = 16 mA, V_{CC} = 4.5 V, I_O = 2.4 mA			0.4	V

10. Isolation Characteristics (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Test Conditions	Min	Тур.	Max	Unit
Total capacitance (input to output)	C _S	(Note 1)	V _S = 0 V, f = 1 MHz		0.8		pF
Isolation resistance	R _S	(Note 1)	V _S = 500 V, R.H. ≤ 60 %	1 × 10 ¹²	1014		Ω
Isolation voltage	BVS	(Note 1)	AC, 60 s	3750	_	_	Vrms
			AC, 1 s in oil		10000	_	
			DC, 60 s in oil		10000		Vdc

Note 1: This device is considered as a two-terminal device: Pins 1 and 3 are shorted together, and pins 4, 5 and 6 are shorted together.

11. Switching Characteristics (Unless otherwise specified, T_a = 25 °C, V_{CC} = 15 V)

Characteristics	Symbol	Note	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Propagation delay time (H/L)	t _{pHL}		Fig.13.1.1	I_F = 10 mA, R_L = 20 kΩ	0.1	0.45	0.8	μS
				I_F = 10 mA, R_L = 20 k Ω , T_a = 0 to 85 °C	0.1	0.45	0.9	
				I_F = 10 mA, R_L = 20 kΩ, T_a = -25 to 100 °C	0.1	0.45	1.0	
Propagation delay time (L/H)	t _{pLH}		Fig.13.1.1	I_F = 10 mA, R_L = 20 k Ω	0.1	0.45	0.8	μS
				I_F = 10 mA, R_L = 20 kΩ, T_a = 0 to 85 °C	0.1	0.45	0.9	
				I_F = 10 mA, R_L = 20 kΩ, T_a = -25 to 100 °C	0.1	0.45	1.0	
Pulse width distortion	t _{pHL} -t _{pLH}		Fig.13.1.1	I_F = 10 mA, R_L = 20 kΩ	_	0.15	0.7	μS
				I_F = 10 mA, R _L = 20 kΩ, T _a = 0 to 85 °C	_	0.25	0.8	
				I_F = 10 mA, R_L = 20 kΩ, T_a = -25 to 100 °C	1	0.25	0.9	
Common-mode transient immunity at output high	CM _H	(Note 1)		$V_{CM} = 1500 V_{p-p}, I_F = 0 \text{ mA},$ $R_L = 20 \text{ k}\Omega$	10	15		kV/μs
Common-mode transient immunity at output low	CM _L	(Note 2)		V_{CM} = 1500 V_{p-p} , I_F = 10 mA, R_L = 20 k Ω	-10	-15		kV/μs

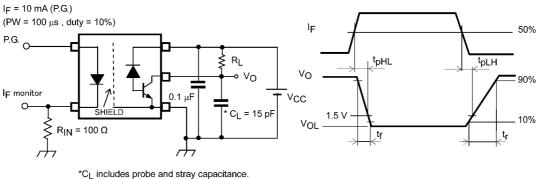
Note 1: CM_H is the maximum rate of rise of the common mode voltage that can be sustained with the output voltage in the logic high state ($V_O > 4 V$).

Note 2: CM_L is the maximum rate of fall of the common mode voltage that can be sustained with the output voltage in the logic low state ($V_O < 1 \text{ V}$).



12. Test Circuits and Characteristics Curves

12.1. Test Circuits



P.G.: Pulse generator

Fig. 12.1.1 Switching Time Test Circuit and Waveform

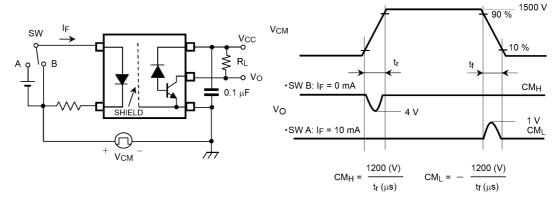


Fig. 12.1.2 Common-Mode Transient Immunity Test Circuit and Waveform



13. Soldering and Storage

13.1. Precautions for Soldering

The soldering temperature should be controlled as closely as possible to the conditions shown below, irrespective of whether a soldering iron or a reflow soldering method is used.

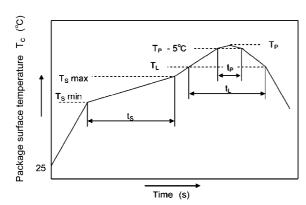
· When using soldering reflow.

The soldering temperature profile is based on the package surface temperature.

(See the figure shown below, which is based on the package surface temperature.)

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.



	Symbol	Min	Max	Unit
Preheat temperature	Ts	150	200	°C
Preheat time	ts	60	120	S
Ramp-up rate (T _L to T _P)			3	°C/s
Liquidus temperature	TL	217		°C
Time above T _L	t _L	60	150	s
Peak temperature	T _P		260	°C
Time during which T_c is between $(T_P - 5)$ and T_P	t _P		30	s
Ramp-down rate (T _P to T _L)			6	°C/s

An Example of a Temperature Profile When Lead(Pb)-Free Solder Is Used

· When using soldering flow

Preheat the device at a temperature of 150 $^{\circ}\text{C}$ (package surface temperature) for 60 to 120 seconds.

Mounting condition of 260 $^{\circ}\text{C}$ within 10 seconds is recommended.

Flow soldering must be performed once.

· When using soldering Iron

Complete soldering within 10 seconds for lead temperature not exceeding 260 $^{\circ}$ C or within 3 seconds not exceeding 350 $^{\circ}$ C

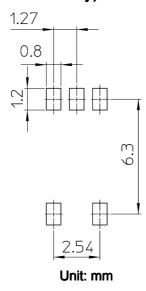
Heating by soldering iron must be done only once per lead.

13.2. Precautions for General Storage

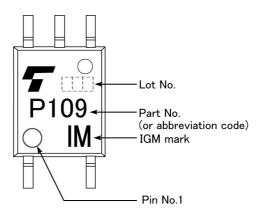
- Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- Follow the precautions printed on the packing label of the device for transportation and storage.
- Keep the storage location temperature and humidity within a range of 5 °C to 35 °C and 45 % to 75 %, respectively.
- Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- · When restoring devices after removal from their packing, use anti-static containers.
- · Do not allow loads to be applied directly to devices while they are in storage.
- If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.



14. Land Pattern Dimensions (for reference only)



15. Marking



Rev.1.0



16. EN60747-5-5 Option (V4) Specification

Part number: TLP109 (Note 1)

The following part naming conventions are used for the devices that have been qualified according to option (V4) of EN60747.

Example: TLP109(V4IGMTL,E(T

V4: EN60747 option IGM: IGM spec TL: Tape type

E: [[G]]/RoHS COMPATIBLE (Note 2)

T: Domestic ID (Country/Region of origin: Thailand)

Note 1: Use TOSHIBA standard type number for safety standard application.

e.g., TLP109(V4IGMTL,E(T \rightarrow TLP109

Note 2: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Description	Symbol	Rating	Unit
Application classification			
for rated mains voltage \leq 150 Vrms for rated mains voltage \leq 300 Vrms		I-IV I-III	_
Climatic classification		55 / 125 / 21	_
Pollution degree		2	_
Maximum operating insulation voltage	VIORM	707	Vpeak
Input to output test voltage, Method A $V_{pr} = 1.6 \times \text{VIORM, type and sample test} \\ t_p = 10 \text{ s, partial discharge} < 5 \text{ pC}$	Vpr	1131	Vpeak
Input to output test voltage, Method B Vpr =1.875 × VIORM, 100 % production test tp = 1 s, partial discharge < 5 pC	Vpr	1330	Vpeak
Highest permissible overvoltage (transient overvoltage, tpr = 60 s)	VTR	6000	Vpeak
Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve) current (input current IF, Pso = 0) power (output or total power dissipation) temperature	Isi Pso Ts	250 400 150	mA mW °C
Insulation resistance $VIO = 500 \text{ V}, T_a = 25 \text{ °C}$ $VIO = 500 \text{ V}, T_a = 100 \text{ °C}$ $VIO = 500 \text{ V}, T_a = T_s$	Rsi	≥ 10 ¹² ≥ 10 ¹¹ ≥ 10 ⁹	Ω

Fig. 16.1 EN60747 Isolation Characteristics



Minimum creepage distance	Cr	5.0 mm
Minimum clearance	Cl	5.0 mm
Minimum insulation thickness	ti	0.4 mm
Comparative tracking index	CTI	175

Fig. 16.2 Insulation Related Specifications (Note)

Note: This photocoupler is suitable for **safe electrical isolation** only within the safety limit data.

Maintenance of the safety data shall be ensured by means of protective circuits.



Fig. 16.3 Marking on Packing

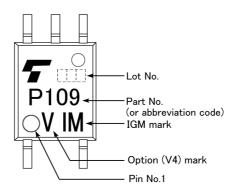
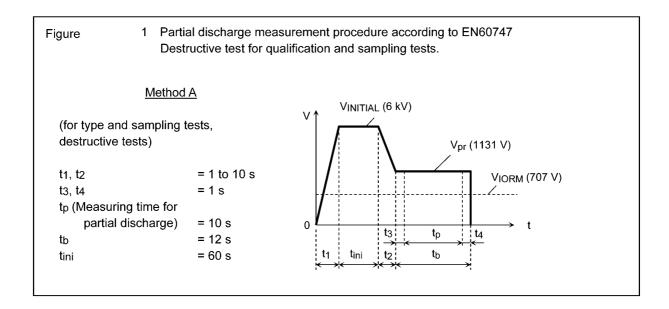
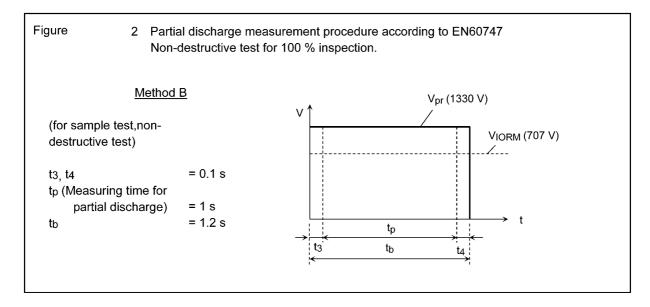


Fig. 16.4 Marking Example (Note)

Note: The above marking is applied to the photocouplers that have been qualified according to option (V4) of EN60747.







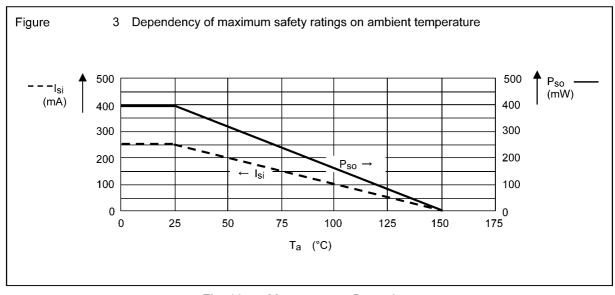


Fig. 16.5 Measurement Procedure



17. Specifications for Embossed-Tape Packing

17.1. Applicable Package

Package Name	Product Type
SO6	Mini flat coupler

17.2. Product Naming Conventions

Type of package used for shipment is denoted by a symbol suffix after a part number. The method of classification is as below.

Example) TLP109(IGM-TPL,E(T

Part number: TLP109

IGM spec: IGM Tape type: TPL

[[G]]/RoHS COMPATIBLE: E (Note 1)

Domestic ID (Country/Region of origin: Thailand): T

Note 1: Please contact your Toshiba sales representative for details on environmental information such as the product's RoHS compatibility.

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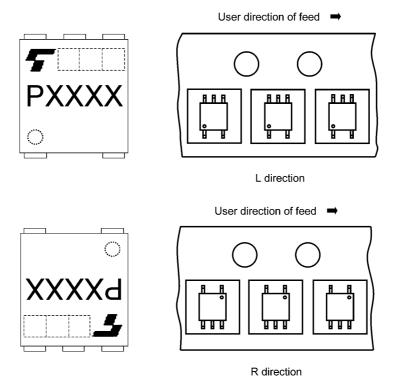
17.3. Tape Dimensions Specification

Таре Туре	Division	Packing Amount (A unit per reel)
TPL	L direction	3000
TPR	R direction	3000



17.3.1. Orientation of Device in Relation to Direction of Feed

Device orientation in the carrier cavities as shown in the following figure.



Device Orientation

17.3.2. Empty Cavities

Characteristics	Criterion	Remarks
Occurrences of 2 or more successive empty cavities	0 device	Within any given 40-mm section of tape, not including leader and trailer
Single empty cavity	6 devices (max) per reel	Not including leader and trailer

17.3.3. Tape Leader and Trailer

The start of the tape has 50 or more empty holes. The end of the tape has 50 or more empty holes and a cover tape of 30 mm or longer.



17.3.4. Tape Dimensions

Tape material: Plastic (for protection against static electricity)

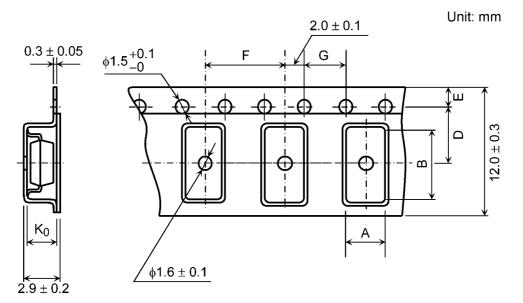


Table Tape Dimensions (unit: mm, tolerance: ±0.1)

Symbol	Dimension	Remark
Α	4.0	_
В	7.6	_
D	5.5	Center line of embossed cavity and sprocket hole
E	1.75	Distance between tape edge and sprocket hole center
F	8.0	Cumulative error +0.1/-0.3 (max) per 10 empty cavities holes
G	4.0	Cumulative error +0.1/-0.3 (max) per 10 sprocket holes
K ₀	2.6	Internal space



17.3.5. Reel Specification

Material: Plastic (for protection against static electricity)

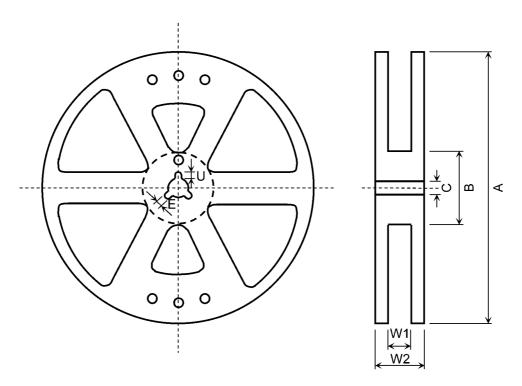
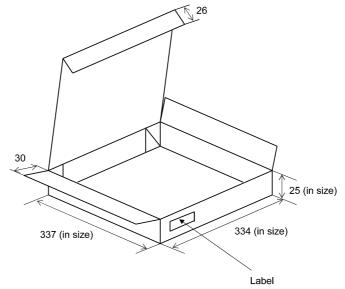


Table Reel Dimensions (unit: mm)

Symbol	Dimension
Α	$\phi 330 \pm 2.0$
В	ф80 ± 1.0
С	$\varphi 13 \pm 0.5$
E	2.0 ± 0.5
U	4.0 ± 0.5
W1	13.5 ± 0.5
W2	17.5 ± 1.0



17.4. Packing (Note)



1 reel/carton (unit: mm)

Note: Taping reel diameter: \$\phi330 mm

17.5. Label Format

- (1) Carton: The label provides the part number, quantity, lot number, the Toshiba logo, etc.
- (2) Reel: The label provides the part number, the taping name, quantity, lot number, etc.

17.6. Ordering Information

When placing an order, please specify the part number, tape type and quantity as shown in the following example.

Example) TLP109(IGM-TPL,E(T 3000 pcs

Part number: TLP109 IGM spec: IGM Tape type: TPL

[[G]]/RoHS COMPATIBLE: E (Note 1)

Domestic ID (Country/Region of origin: Thailand): T Quantity (must be a multiple of 3000): 3000 pcs

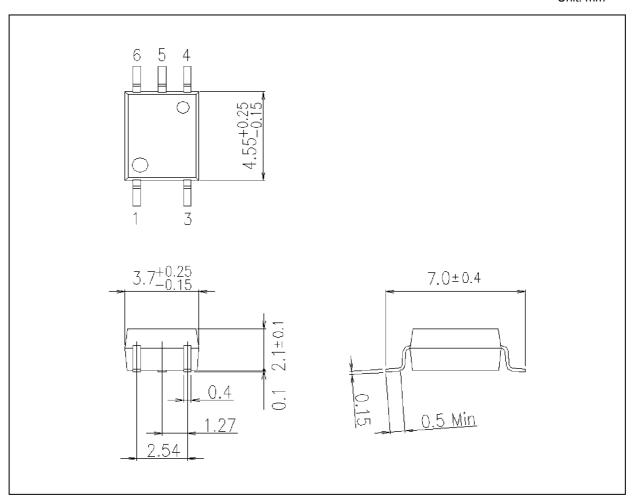
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RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.



Package Dimensions

Unit: mm



Weight: 0.08 g (typ.)

	Package Name(s)
TOSHIBA: 11-4L1S	



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