MOSFETs Silicon N-Channel MOS (U-MOSVII-H)

ТРСС8067-Н

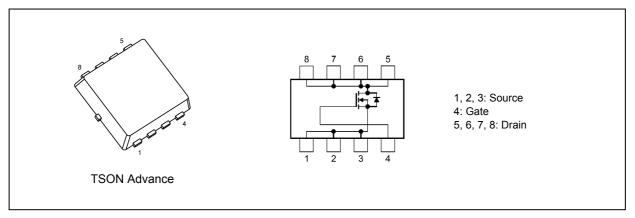
1. Applications

- High-Efficiency DC-DC Converters
- Notebook PCs
- Mobile Handsets

2. Features

- (1) Small, thin package
- (2) High-speed switching
- (3) Small gate charge: $Q_{SW} = 1.9 \text{ nC}$ (typ.)
- (4) Low drain-source on-resistance: $R_{DS(ON)} = 26 \text{ m}\Omega \text{ (typ.)} (V_{GS} = 4.5 \text{ V})$
- (5) Low leakage current: I_{DSS} = 10 μ A (max) (V_{DS} = 30 V)
- (6) Enhancement mode: V_{th} = 1.3 to 2.3 V ($V_{\rm DS}$ = 10 V, $I_{\rm D}$ = 0.1 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteris	Symbol	Rating	Unit		
Drain-source voltage			V _{DSS}	30	V
Gate-source voltage			V _{GSS}	±20	
Drain current (DC)		(Note 1)	ID	9	A
Drain current (pulsed)		(Note 1)	I _{DP}	27	
Power dissipation	(T _c = 25°C)		PD	15	W
Power dissipation	(t = 10 s)	(Note 2)	PD	1.9	W
Power dissipation	(t = 10 s)	(Note 3)	PD	0.7	W
Single-pulse avalanche energy		(Note 4)	E _{AS}	21	mJ
Avalanche current			I _{AR}	9	A
Channel temperature			T _{ch}	150	°C
Storage temperature			T _{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production

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5. Thermal Characteristics

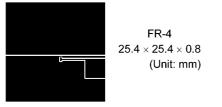
Characteristics				Max	Unit
Channel-to-case thermal resistance	(T _c = 25°C)		R _{th(ch-c)}	8.33	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 2)	R _{th(ch-a)}	65.7	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 3)	R _{th(ch-a)}	178	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Device mounted on a glass-epoxy board (a), Figure 5.1

Note 3: Device mounted on a glass-epoxy board (b), Figure 5.2

Note 4: V_DD = 24 V, T_ch = 25°C (initial), L = 0.2 mH, R_G = 1.2 Ω , I_AR = 9 A



Board (a)



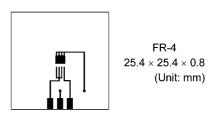


Fig. 5.2 Device Mounted on a Glass-Epoxy Board (b)

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

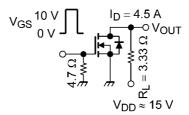
6. Electrical Characteristics

6.1. Static Characteristics (Ta = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±20 V, V_{DS} = 0 V	—		±0.1	μA
Drain cut-off current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	—	_	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	30	_	_	V
	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	15	_	_	
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 0.1 mA	1.3	_	2.3	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 4.5 V, I _D = 4.5 A	—	26	33	mΩ
		V _{GS} = 10 V, I _D = 4.5 A	_	20	25	

6.2. Dynamic Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		690	_	pF
Reverse transfer capacitance	C _{rss}]		28	_	
Output capacitance	C _{oss}]	_	120	_	
Gate resistance	r _g	V_{DS} = 10 V, V_{GS} = 0 V, f = 5 MHz	_	3.4	5.1	Ω
Switching time (rise time)	tr	See Figure 6.2.1.	_	2.1	_	ns
Switching time (turn-on time)	t _{on}]	_	6.5	_	
Switching time (fall time)	t _f]		2.0		
Switching time (turn-off time)	t _{off}]		14	_	



Duty \leq 1%, t_w = 10 μ s

Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

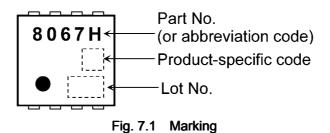
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus	Qg	$V_{DD} \approx 24$ V, V_{GS} = 10 V, I_D = 9 A	_	9.5	_	nC
gate-drain)		$V_{DD} \approx 24$ V, V_{GS} = 5 V, I_D = 9 A	_	4.7	_	
Gate-source charge 1	Q _{gs1}	$V_{DD} \approx 24 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 9 \text{ A}$	_	2.2	_	
Gate-drain charge	Q _{gd}		_	0.9	—	
Gate switch charge	Q _{SW}		_	1.9	_	

6.4. Source-Drain Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed) (Note 5)	I _{DRP}	—	_	_	27	А
Diode forward voltage	V _{DSF}	I _{DR} = 9 A, V _{GS} = 0 V		_	-1.2	V

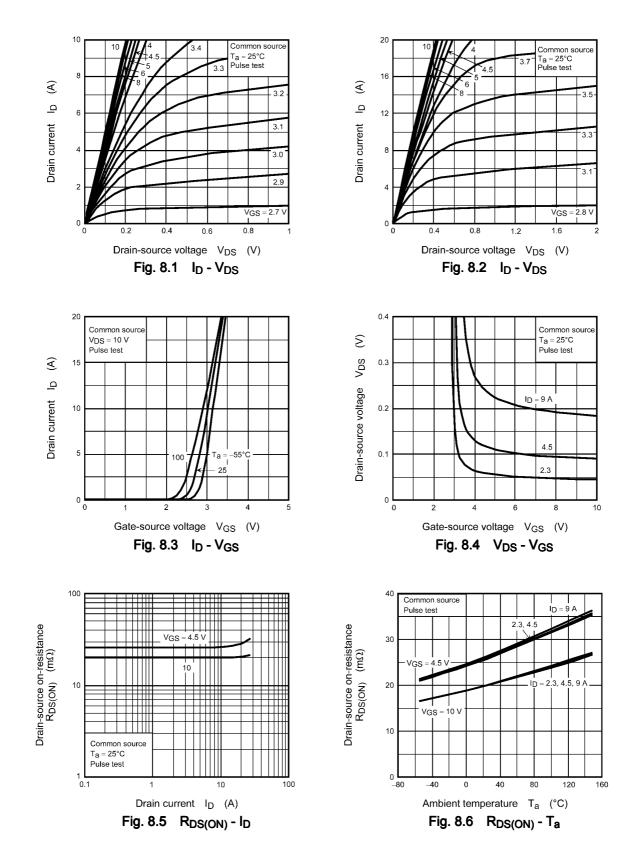
Note 5: Ensure that the channel temperature does not exceed 150°C.

7. Marking

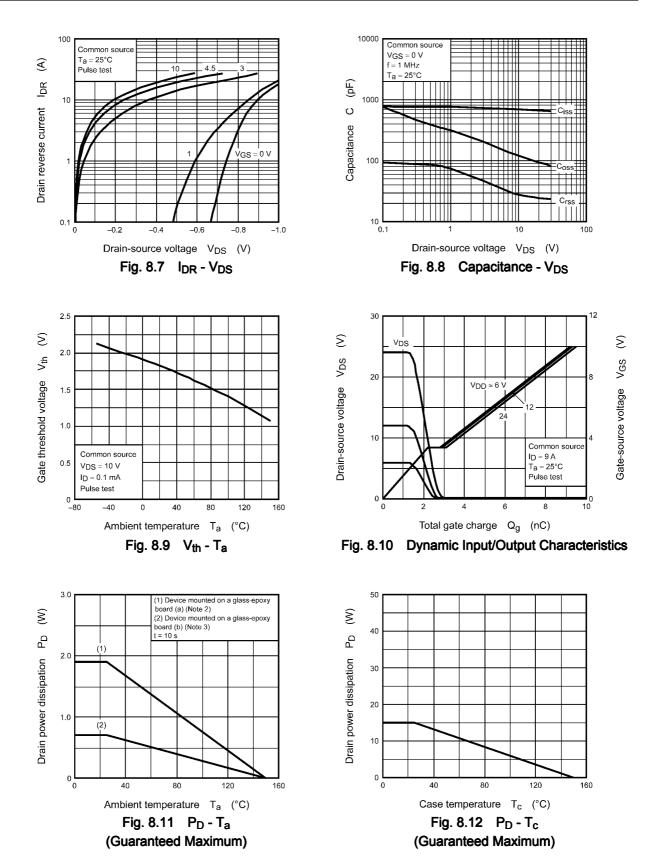


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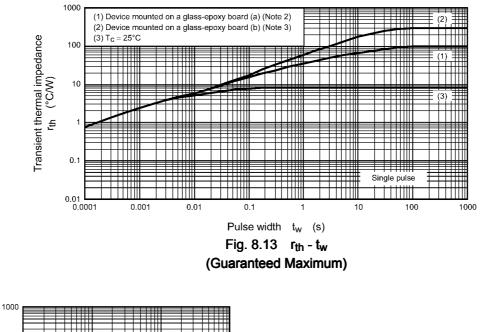
8. Characteristics Curves (Note)

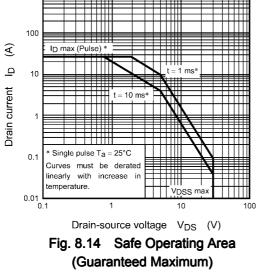


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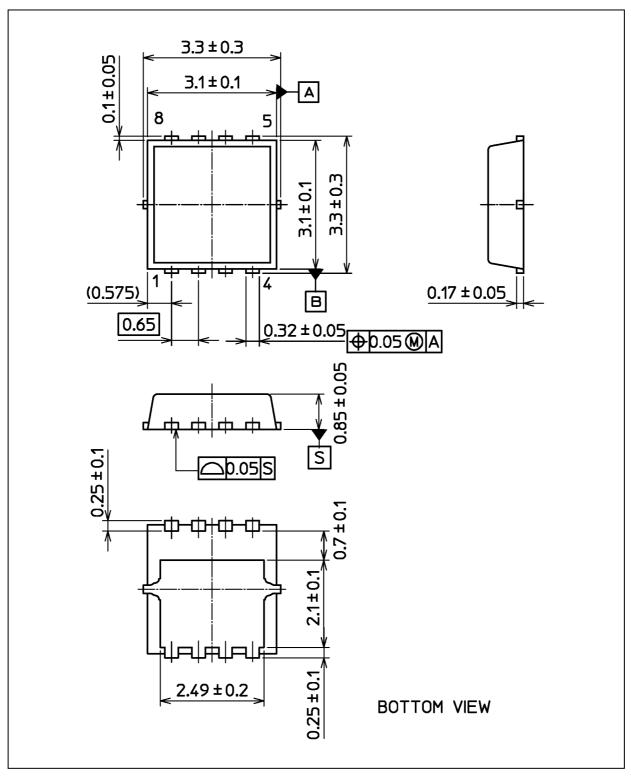


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.02 g (typ.)

Package Name(s)

TOSHIBA: 2-3X1S

Nickname: TSON Advance

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