TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOSVI-H)

TPCA8048-H

Switching Regulator Applications Motor Drive Applications DC-DC Converter Applications

- Small footprint due to a small and thin package
- · High-speed switching
- Small gate charge: Q_{SW} = 19 nC (typ.)
- Low drain-source ON-resistance: RDS (ON) = 4.3 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 118 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \mu A (max) (V_{DS} = 60 V)$
- Enhancement mode: V_{th} = 1.3 to 2.3 V (V_{DS} = 10 V, I_D = 1.0 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characte	eristic	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	60	V	
Drain-gate voltage (R	GS = 20 kΩ)	V_{DGR}	60	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	ID	35	А	
Drain current	Pulsed (Note 1) I _{DP}	I_{DP}	105		
Drain power dissipati	on (Tc = 25°C)	P_{D}	45	W	
Drain power dissipati	on (t = 10 s) (Note 2a)	P_{D}	2.8	W	
Drain power dissipati	on (t = 10 s) (Note 2b)	P _D	1.6	W	
Single-pulse avalance	ne energy (Note 3)	E _{AS}	88	mJ	
Avalanche current		I _{AR}	35	Α	
Repetitive avalanche (To	energy c = 25°C) (Note 4)	E _{AR}	2.46	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	-55 to 150	°C	

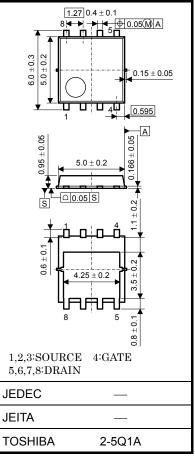
Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.

operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

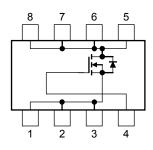
This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm



Weight: 0.069 g (typ.)

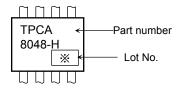
Circuit Configuration



Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc = 25°C)	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	78.1	°C/W

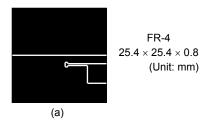
Marking (Note 5)

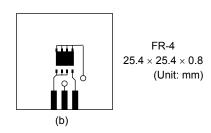


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)

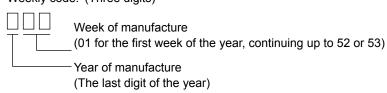




Note 3: $V_{DD} = 24~V,~T_{Ch} = 25^{\circ}C$ (initial), L = 100 $\mu H,~R_G = 25~\Omega,~I_{AR} = 35~A$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: * Weekly code: (Three digits)



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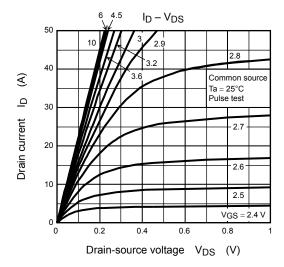
Electrical Characteristics (Ta = 25°C)

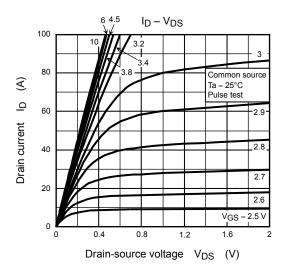
Ch	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V		_	10	μА
Drain agurag bro	akdowa voltago	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	60	_	_	V
Drain-source brea	akdown voltage	V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	45	_	_	V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1.0 mA	1.3	_	2.3	V
Drain-source ON	registance	P== (=1)	V _{GS} = 4.5 V, I _D = 18 A	_	4.8	7.1	0
Drain-source ON	-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 18 A	— — 10 60 — — 45 — — 1.3 — 2.3 — 4.8 7.1 — 4.3 6.6 59 118 — — 5800 7540 — 210 315 — 650 — — 1.0 1.5 — 3.6 — — 13 — — 7.3 — — 66 — — 90 — — 46 —	mΩ		
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 18 A	59	118	_	S
Input capacitance	•	C _{iss}			5800	7540	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	210	315	pF
Output capacitance		Coss			650	_	
Gate resistance		rg	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	1.0	1.5	Ω
Rise time Turn-on time Fall time Turn-off time	Rise time	t _r	V _{GS} 10 V	_	3.6	_	ns
	Turn-on time	t _{on}		_	13	_	
	Fall time	t _f		_	7.3	_	
	Turn-off time	t _{off}	$V_{DD} \approx 30 \text{ V}$ Duty \leq 1%, $t_W = 10 \mu\text{s}$	_	66	_	
Total gate charge	otal gate charge		$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 35 \text{ A}$		90	_	
(gate-source plus		Qg	$V_{DD} \approx 48 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 35 \text{ A}$	_			
Gate-source charge 1		Q _{gs1}		_	16	_	nC
Gate-drain ("Miller") charge		Q _{gd}	$V_{DD} \approx 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 35 \text{ A}$		12	_	
Gate switch char	ge	Q _{SW}]	_	19	_	

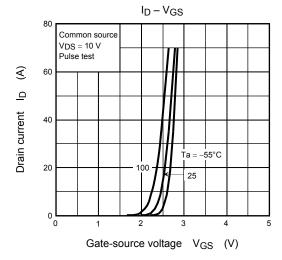
Source-Drain Ratings and Characteristics (Ta = 25°C)

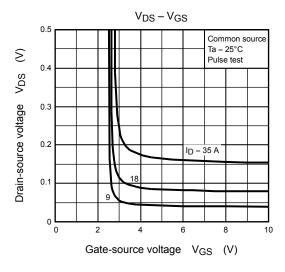
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	105	Α
Forward voltage (diode)			V_{DSF}	$I_{DR} = 35 \text{ A}, V_{GS} = 0 \text{ V}$		_	-1.2	V

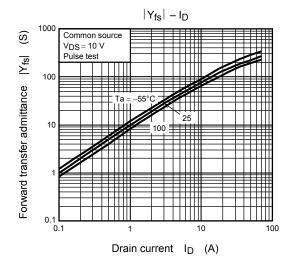
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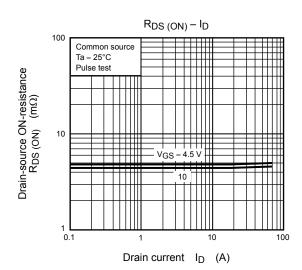


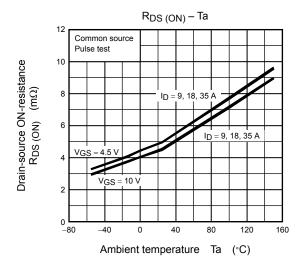


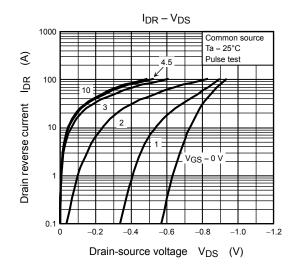


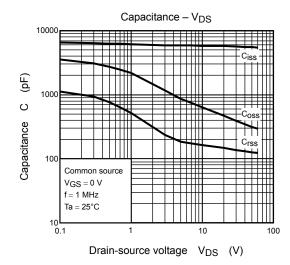


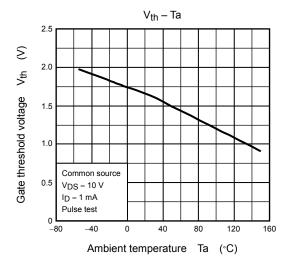


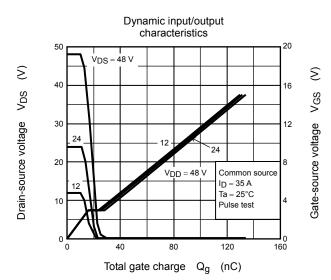




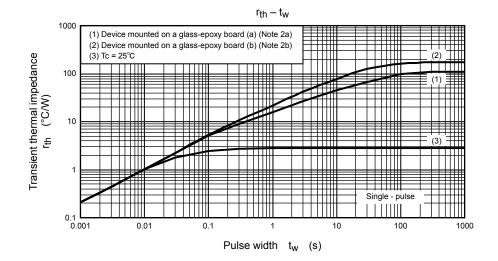


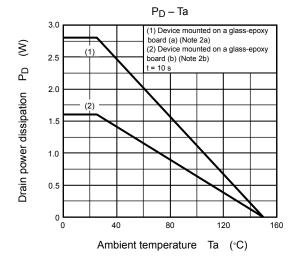


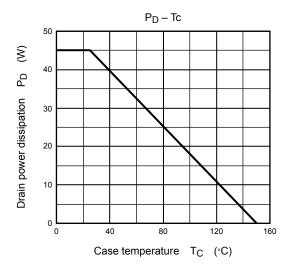


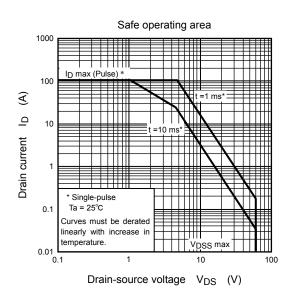


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