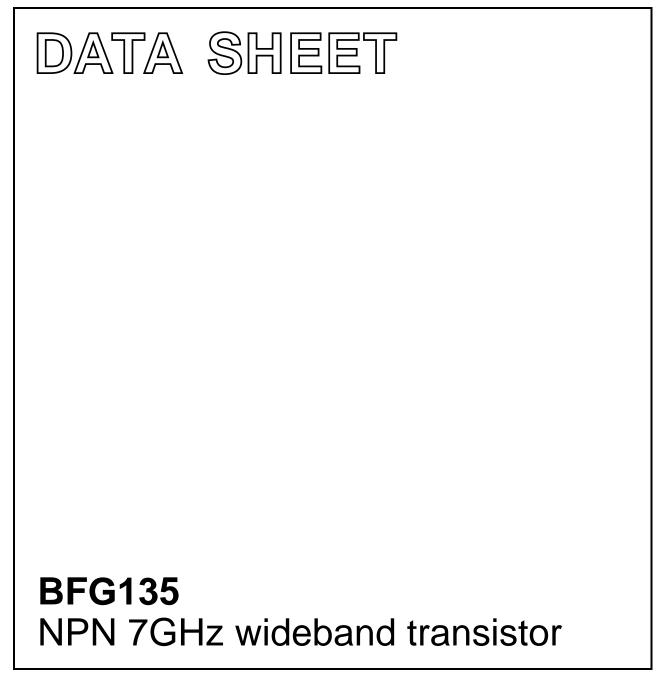
DISCRETE SEMICONDUCTORS



Product specification

1995 Sep 13

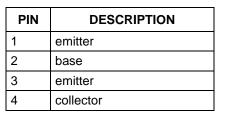


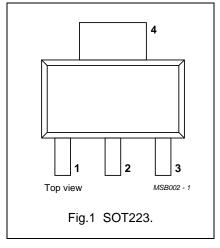
DESCRIPTION

NPN silicon planar epitaxial transistor in a plastic SOT223 envelope, intended for wideband amplifier applications. The small emitter structures, with integrated emitter-ballasting resistors, ensure high output voltage capabilities at a low distortion level.

The distribution of the active areas across the surface of the device gives an excellent temperature profile.

PINNING





QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	-	-	25	V
V _{CEO}	collector-emitter voltage	open base	-	-	15	V
I _C	DC collector current		-	-	150	mA
P _{tot}	total power dissipation	up to $T_s = 145 \ ^{\circ}C$ (note 1)	_	_	1	W
h _{FE}	DC current gain	I_{C} = 100 mA; V_{CE} = 10 V; T_{j} = 25 °C	80	130	-	
f _T	transition frequency	I _C = 100 mA; V _{CE} = 10 V; f = 1 GHz; T _{amb} = 25 °C	_	7	-	GHz
G _{UM}	maximum unilateral power gain	I_{C} = 100 mA; V_{CE} = 10 V; f = 500 MHz; T_{amb} = 25 °C	_	16	-	dB
		I_{C} = 100 mA; V_{CE} = 10 V; f = 800 MHz; T_{amb} = 25 °C	-	12	-	dB
Vo	output voltage	$\begin{array}{l} d_{im} = -60 \text{ dB; } I_{C} = 100 \text{ mA; } V_{CE} = 10 \text{ V;} \\ R_{L} = 75 \ \Omega; \ T_{amb} = 25 \ ^{\circ}\text{C;} \\ f_{(p+q-r)} = 793.25 \text{ MHz} \end{array}$	-	850	_	mV

LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	-	25	V
V _{CEO}	collector-emitter voltage	open base	-	15	V
V _{EBO}	emitter-base voltage	open collector	-	2	V
I _C	DC collector current		-	150	mA
P _{tot}	total power dissipation	up to $T_s = 145 \text{ °C}$ (note 1)	_	1	W
T _{stg}	storage temperature		-65	150	°C
Tj	junction temperature		_	175	°C

Note

1. T_s is the temperature at the soldering point of the collector tab.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	THERMAL RESISTANCE	
R _{th j-s}	thermal resistance from junction to soldering point	up to $T_s = 145 \text{ °C}$ (note 1)	30 K/W	

Note

1. T_s is the temperature at the soldering point of the collector tab.

CHARACTERISTICS

 $T_i = 25 \ ^{\circ}C$ unless otherwise specified.

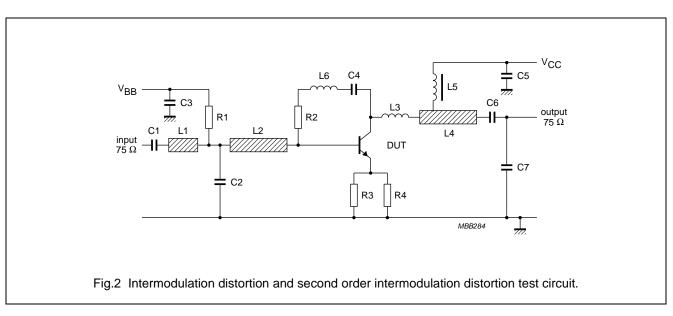
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 10 V	_	_	1	μA
h _{FE}	DC current gain	I _C = 100 mA; V _{CE} = 10 V	80	130	-	
Cc	collector capacitance	I _E = i _e = 0; V _{CB} = 10 V; f = 1 MHz	_	2	_	pF
Ce	emitter capacitance	$I_{C} = i_{c} = 0; V_{EB} = 0.5 V; f = 1 MHz$	-	7	-	pF
C _{re}	feedback capacitance	I _C = 0; V _{CE} = 10 V; f = 1 MHz	-	1.2	-	pF
f _T	transition frequency	I_{C} = 100 mA; V _{CE} = 10 V; f = 1 GHz; T _{amb} = 25 °C	-	7	-	GHz
G _{UM}	maximum unilateral power gain	I _C = 100 mA; V _{CE} = 10 V; f = 500 MHz; T _{amb} = 25 °C	-	16	-	dB
		I _C = 100 mA; V _{CE} = 10 V; f = 800 MHz; T _{amb} = 25 °C	-	12	-	dB
Vo	output voltage	note 1	_	900	_	mV
		note 2	_	850	_	mV
d ₂	second order intermodulation distortion	$ I_{C} = 90 \text{ mA}; V_{CE} = 10 \text{ V}; V_{O} = 50 \text{ dBmV}; T_{amb} = 25 \text{ °C}; f_{(p+q)} = 450 \text{ MHz}; f_{p} = 50 \text{ MHz}; f_{q} = 400 \text{ MHz} $	_	-58	-	dB
		$ I_{C} = 90 \text{ mA}; V_{CE} = 10 \text{ V}; V_{O} = 50 \text{ dBmV}; T_{amb} = 25 \text{ °C}; f_{(p+q)} = 810 \text{ MHz}; f_{p} = 250 \text{ MHz}; f_{q} = 560 \text{ MHz} $	_	-53	_	dB

Notes

1. $d_{im} = -60 \text{ dB} (\text{DIN } 45004\text{B}); I_C = 100 \text{ mA}; V_{CE} = 10 \text{ V}; \text{ R}_L = 75 \Omega; \text{ T}_{amb} = 25 \text{ °C};$ $V_p = V_0 \text{ at } d_{im} = -60 \text{ dB}; f_p = 445.25 \text{ MHz};$ $V_q = V_0 - 6 \text{ dB}; f_q = 453.25 \text{ MHz};$ $V_r = V_0 - 6 \text{ dB}; f_r = 455.25 \text{ MHz};$ measured at $f_{(p+q-r)} = 443.25 \text{ MHz}.$ 2. $d_{resc} = -60 \text{ dB} (\text{DIN } 45004\text{ B}); I_r = -400 \text{ mA}; V_r = -40 \text{ V}; \text{ B}_r = -75 \Omega; \text{ T}_{resc} = -25 \text{ °C};$

2. $d_{im} = -60 \text{ dB} \text{ (DIN 45004B)}; I_C = 100 \text{ mA}; V_{CE} = 10 \text{ V}; R_L = 75 \Omega; T_{amb} = 25 \text{ °C};$ $V_p = V_o \text{ at } d_{im} = -60 \text{ dB}; f_p = 795.25 \text{ MHz};$ $V_q = V_o -6 \text{ dB}; f_q = 803.25 \text{ MHz};$ $V_r = V_o -6 \text{ dB}; f_r = 805.25 \text{ MHz};$ measured at $f_{(p+q-r)} = 793.25 \text{ MHz}.$

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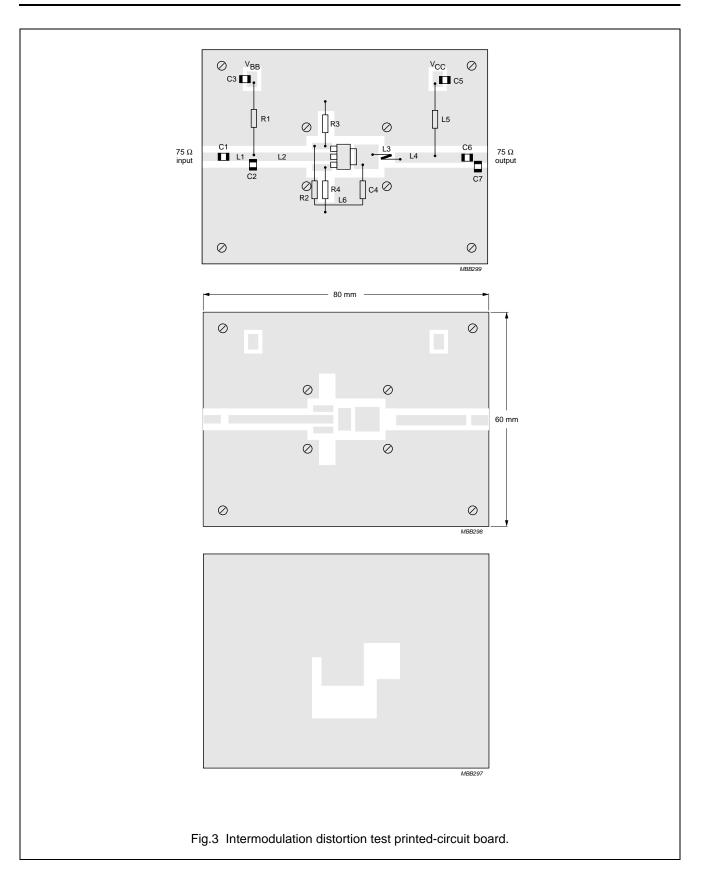
DESIGNATION	DESCRIPTION	VALUE	UNIT	DIMENSIONS	CATALOGUE NO.
C1, C3, C5, C6	multilayer ceramic capacitor	10	nF		2222 590 08627
C2, C7	multilayer ceramic capacitor	1	pF		2222 851 12108
C4 (note 1)	miniature ceramic plate capacitor	10	nF		2222 629 08103
L1	microstripline	75	Ω	length 7 mm; width 2.5 mm	
L2	microstripline	75	Ω	length 22mm; width 2.5 mm	
L3 (note 1)	1.5 turns 0.4 mm copper wire			int. dia. 3 mm; winding pitch 1 mm	
L4	microstripline	75	Ω	length 19 mm; width 2.5 mm	
L5	Ferroxcube choke	5	μH		3122 108 20153
L6 (note 1)	0.4 mm copper wire	≈25	nH	length 30 mm	
R1	metal film resistor	10	kΩ		2322 180 73103
R2 (note 1)	metal film resistor	200	Ω		2322 180 73201
R3, R4	metal film resistor	27	Ω		2322 180 73279

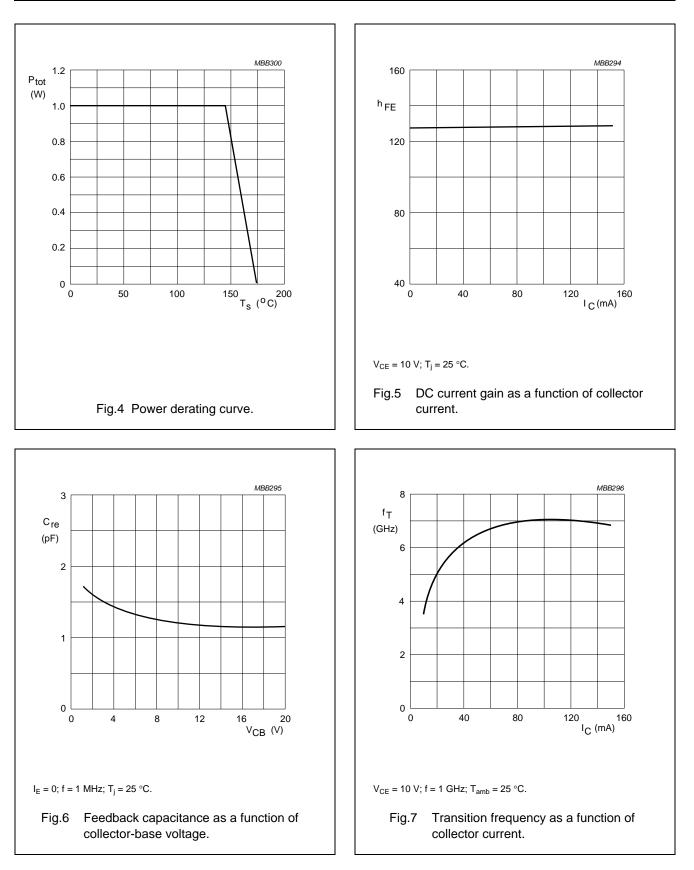
List of components (see test circuit)

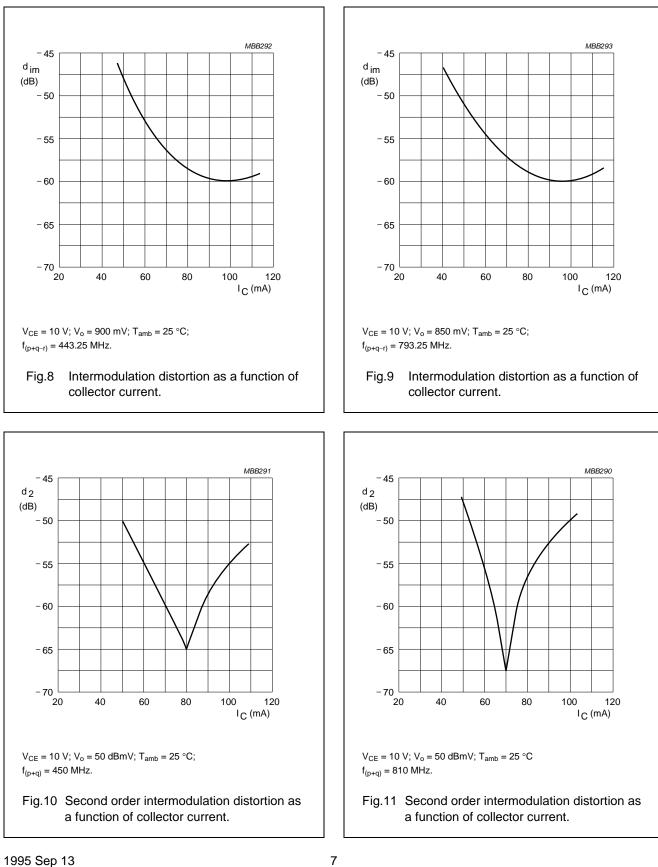
Note

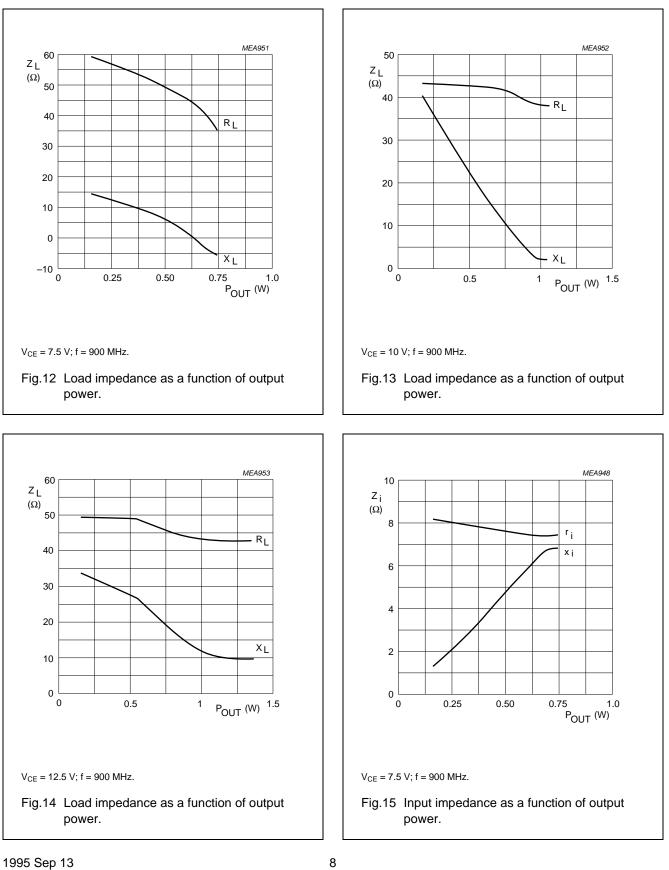
1. Components C4, L3, L6 and R2 are mounted on the underside of the PCB.

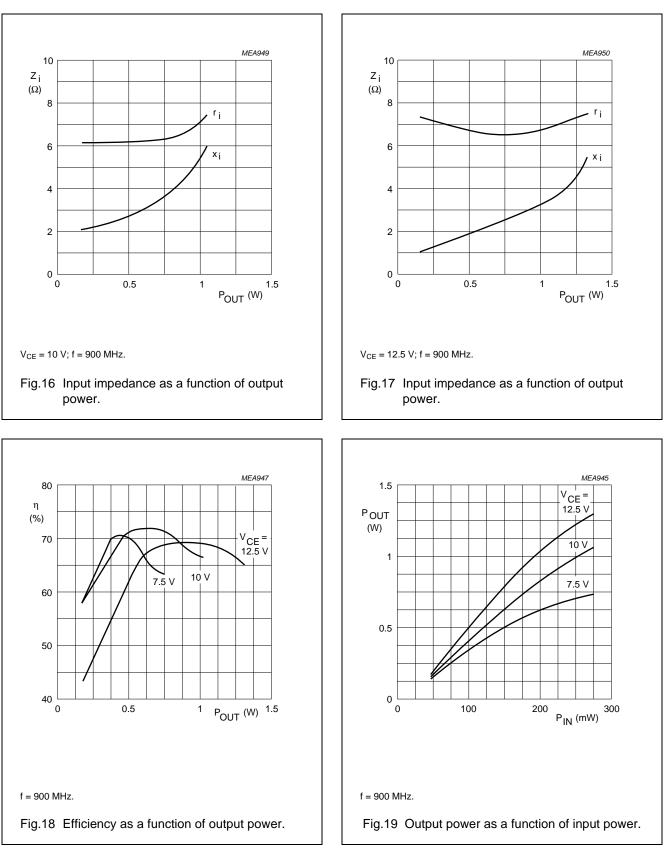
The circuit is constructed on a double copper-clad printed circuit board with PTFE dielectric ($\varepsilon_r = 2.2$); thickness $\frac{1}{16}$ inch; thickness of copper sheet $\frac{1}{32}$ inch.

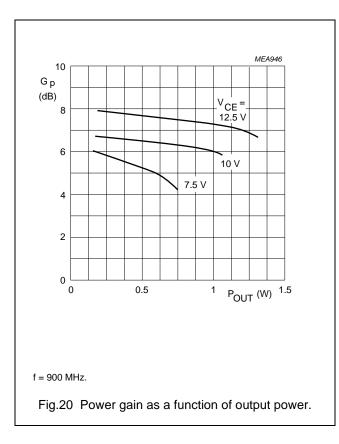


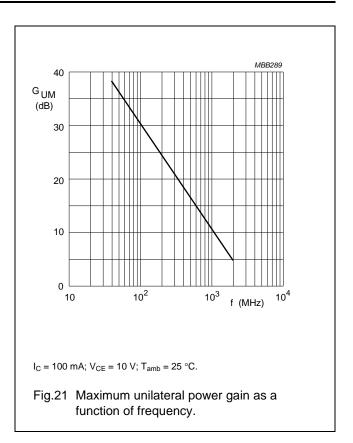


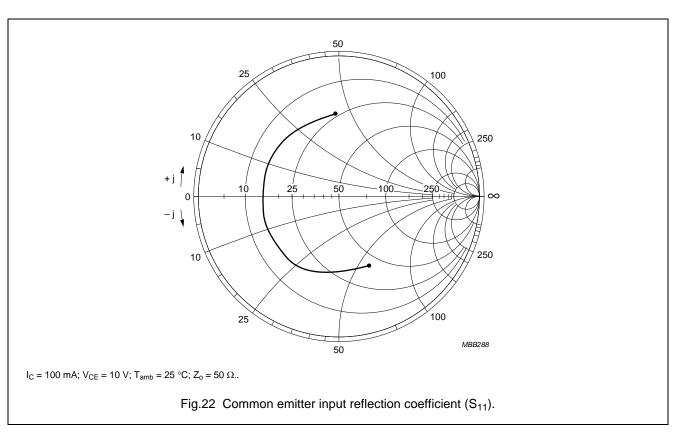


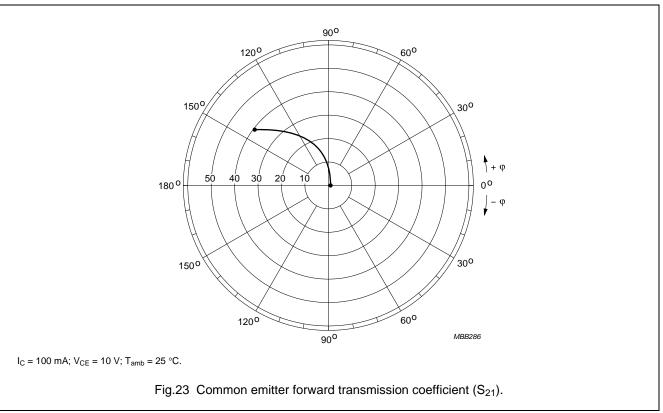


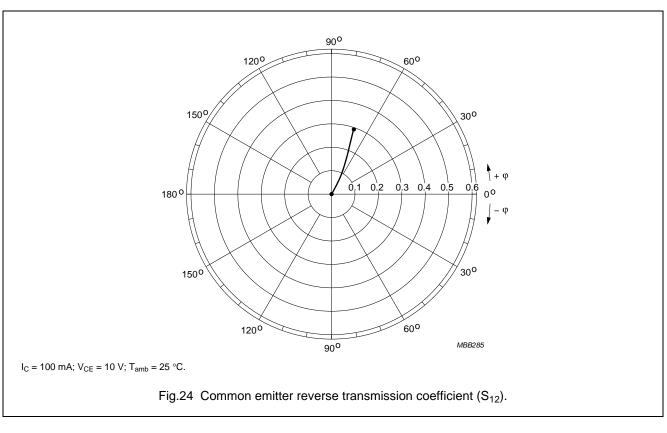


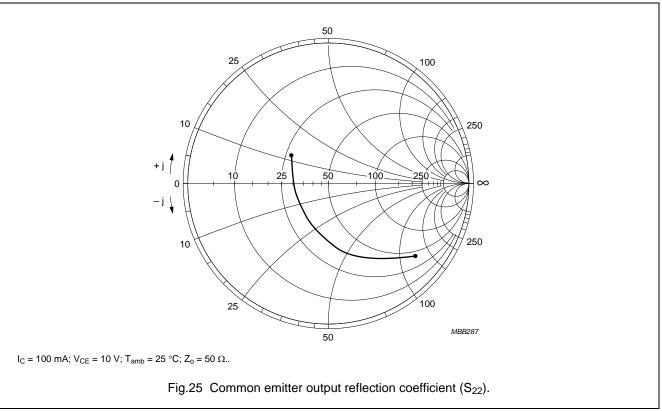




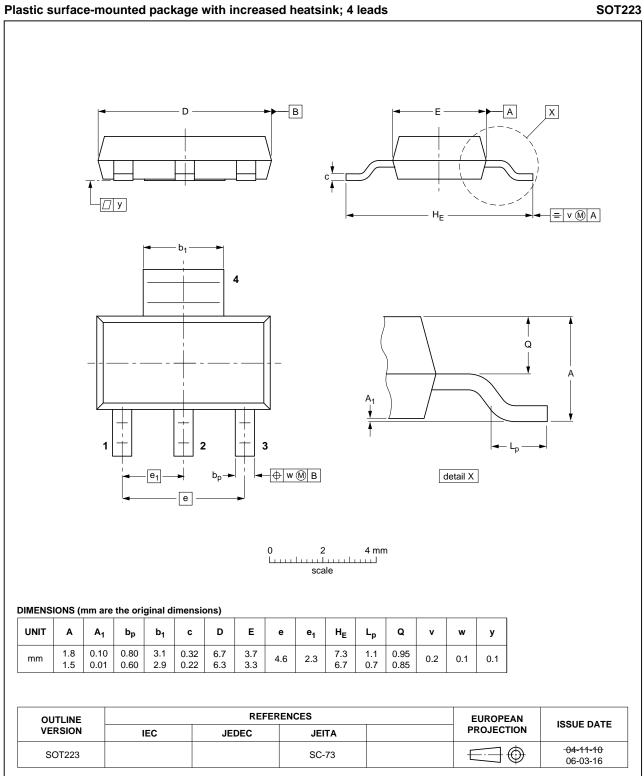








PACKAGE OUTLINE



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DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

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Contact information

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