

PowerMOS transistor
TOPFET high side switch

BUK210-50Y

DESCRIPTION

Monolithic single channel high side protected power switch in TOPFET2 technology assembled in a 5 pin plastic package.

APPLICATIONS

General controller for driving lamps, motors, solenoids, heaters.

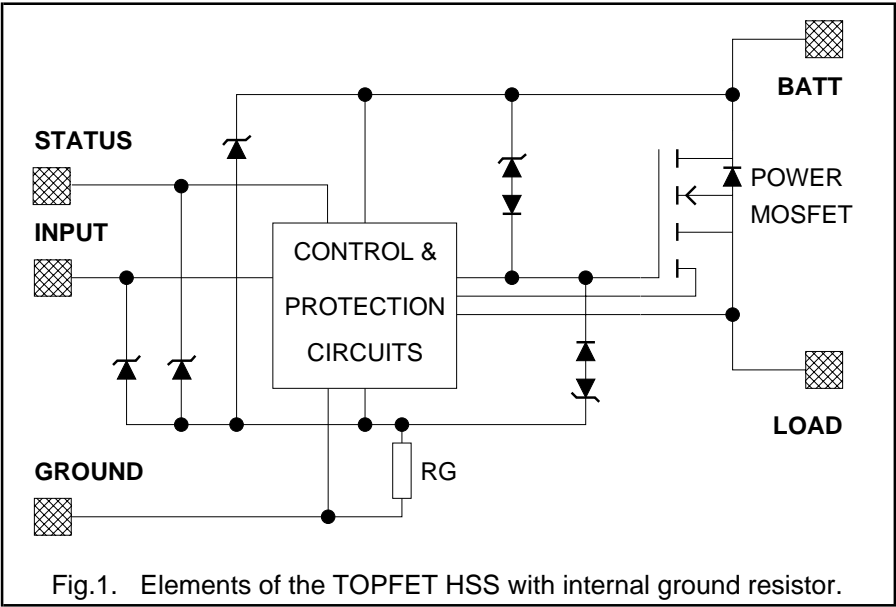
QUICK REFERENCE DATA

| SYMBOL | PARAMETER | MIN. | UNIT |
|----------|--|------|------|
| I_L | Nominal load current (ISO) | 9 | A |
| SYMBOL | PARAMETER | MAX. | UNIT |
| V_{BG} | Continuous off-state supply voltage | 50 | V |
| I_L | Continuous load current | 20 | A |
| T_j | Continuous junction temperature | 150 | °C |
| R_{ON} | On-state resistance $T_j = 25^{\circ}\text{C}$ | 38 | mΩ |

FEATURES

- Vertical power TrenchMOS
- Low on-state resistance
- CMOS logic compatible
- Very low quiescent current
- Overtemperature protection
- Load current limiting
- Latched overload and short circuit protection
- Overvoltage and undervoltage shutdown with hysteresis
- On-state open circuit load detection
- Diagnostic status indication
- Voltage clamping for turn off of inductive loads
- ESD protection on all pins
- Reverse battery, overvoltage and transient protection

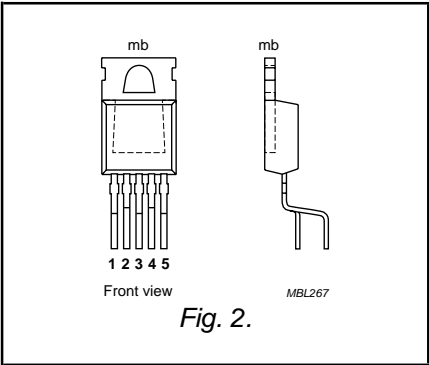
FUNCTIONAL BLOCK DIAGRAM



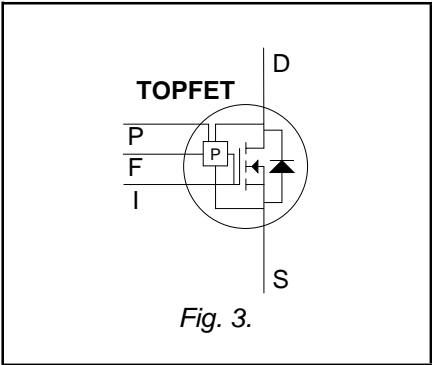
PINNING - SOT263B-01

| PIN | DESCRIPTION |
|-----|-------------------|
| 1 | Input |
| 2 | Flag |
| 3 | Drain |
| 4 | Protection supply |
| 5 | Source |
| tab | Drain |

PIN CONFIGURATION



SYMBOL



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LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134)

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
|------------|--|---|------|------|------------------|
| V_{BG} | Continuous supply voltage | | 0 | 50 | V |
| I_L | Continuous load current | $T_{mb} \leq 95^\circ\text{C}$ | - | 20 | A |
| P_D | Total power dissipation | $T_{mb} \leq 25^\circ\text{C}$ | - | 67 | W |
| T_{stg} | Storage temperature | | -55 | 175 | $^\circ\text{C}$ |
| T_j | Continuous junction temperature ¹ | | - | 150 | $^\circ\text{C}$ |
| T_{sld} | Lead temperature | during soldering | - | 260 | $^\circ\text{C}$ |
| | Reverse battery voltages² | | | | |
| $-V_{BG}$ | Continuous reverse voltage | | - | 16 | V |
| $-V_{BG}$ | Peak reverse voltage | | - | 32 | V |
| | Application information | | | | |
| R_I, R_S | External resistors ³ | to limit input, status currents | 3.2 | - | k Ω |
| | Input and status | | | | |
| I_I, I_S | Continuous currents | | -5 | 5 | mA |
| I_I, I_S | Repetitive peak currents | $\delta \leq 0.1$, $t_p = 300 \mu\text{s}$ | -50 | 50 | mA |
| | Inductive load clamping | | | | |
| E_{BL} | Non-repetitive clamping energy | $I_L = 10 \text{ A}$, $V_{BG} = 16 \text{ V}$ $T_j = 150^\circ\text{C}$ prior to turn-off | - | 150 | mJ |

ESD LIMITING VALUE

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
|--------|---|---|------|------|------|
| V_C | Electrostatic discharge capacitor voltage | Human body model; $C = 250 \text{ pF}$; $R = 1.5 \text{ k}\Omega$ | - | 2 | kV |

THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|---------------|---------------------------------------|-------------|------|------|------|------|
| | Thermal resistance⁴ | | | | | |
| $R_{th j-mb}$ | Junction to mounting base | - | - | 1.52 | 1.86 | K/W |
| $R_{th j-a}$ | Junction to ambient | in free air | - | 60 | 75 | K/W |

¹ For normal continuous operation. A higher T_j is allowed as an overload condition but at the threshold $T_{j(TO)}$ the over temperature trip operates to protect the switch.

² Reverse battery voltage is allowed only with external resistors to limit the input and status currents to a safe value. The connected load must limit the reverse load current. The internal ground resistor limits the reverse battery ground current. Power is dissipated and the T_j rating must be observed.

³ To limit currents during reverse battery and transient overvoltages (positive or negative).

⁴ Of the output power MOS transistor.

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STATIC CHARACTERISTICS

Limits are at $-40^{\circ}\text{C} \leq T_{\text{mb}} \leq 150^{\circ}\text{C}$ and typicals at $T_{\text{mb}} = 25^{\circ}\text{C}$ unless otherwise stated.

| SYMBOL | PARAMETER | CONDITIONS | | | | MIN. | TYP. | MAX. | UNIT |
|------------------|-------------------------------------|---|----------------|-----------------------------|-----------------|------|------|------|------|
| | Clamping voltages | | | | | | | | |
| V _{BG} | Battery to ground | I _G = 1 mA | | | | 50 | 55 | 65 | V |
| V _{BL} | Battery to load | I _L = I _G = 1 mA | | | | 50 | 55 | 65 | V |
| -V _{LG} | Negative load to ground | I _L = 10 mA | | | | 18 | 23 | 28 | V |
| -V _{LG} | Negative load voltage ¹ | I _L = 10 A; t _p = 300 μs | | | | 20 | 25 | 30 | V |
| | Supply voltage | battery to ground | | | | | | | |
| V _{BG} | Operating range ² | | | | | 5.5 | - | 35 | V |
| | Currents | | | | | | | | |
| I _B | Quiescent current ³ | 9 V ≤ V _{BG} ≤ 16 V V _{LG} = 0 V T _{mb} = 25°C | | | | - | - | 20 | μA |
| | | | | | | - | 0.1 | 2 | μA |
| I _L | Off-state load current ⁴ | V _{BL} = V _{BG} T _{mb} = 25°C | | | | - | - | 20 | μA |
| | | | | | | - | 0.1 | 1 | μA |
| I _G | Operating current ⁵ | I _L = 0 A T _{mb} = 25°C | | | | - | 2 | 4 | mA |
| I _L | Nominal load current ⁶ | V _{BL} = 0.5 V T _{mb} = 85°C | | | | 9 | - | - | A |
| | Resistances | | | | | | | | |
| | | V _{BG} | I _L | t _p ⁷ | T _{mb} | | | | |
| R _{ON} | On-state resistance | 9 to 35 V | 10 A | 300 μs | 25°C | - | 28 | 38 | mΩ |
| | | | | | 150°C | - | - | 70 | mΩ |
| R _{ON} | On-state resistance | 6 V | 10 A | 300 μs | 25°C | - | 36 | 48 | mΩ |
| | | | | | 150°C | - | - | 88 | mΩ |
| R _G | Internal ground resistance | I _G = 10 mA | | | | 95 | 150 | 190 | Ω |

¹ For a high side switch, the load pin voltage goes negative with respect to ground during the turn-off of an inductive load.

² On-state resistance is increased if the supply voltage is less than 9 V.

³ This is the continuous current drawn from the supply when the input is low and includes leakage current to the load.

⁴ The measured current is in the load pin only.

⁵ This is the continuous current drawn from the supply with no load connected, but with the input high.

⁶ Defined as in ISO 10483-1. For comparison purposes only. This parameter will not be characterised for automotive PPAP.

⁷ The supply and input voltage for the R_{ON} tests are continuous. The specified pulse duration t_{p} refers only to the applied load current.

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INPUT CHARACTERISTICS

9 V ≤ V_{BG} ≤ 16 V. Limits are at -40°C ≤ T_{mb} ≤ 150°C and typicals at T_{mb} = 25 °C unless otherwise stated.

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|----------------------|----------------------------------|-------------------------|------|------|------|------|
| I _I | Input current | V _{IG} = 5 V | 20 | 90 | 160 | μA |
| V _{IG} | Input clamping voltage | I _I = 200 μA | 5.5 | 7 | 8.5 | V |
| V _{IG(ON)} | Input turn-on threshold voltage | | - | 2.4 | 3 | V |
| V _{IG(OFF)} | Input turn-off threshold voltage | | 1.5 | 2.1 | - | V |
| ΔV _{IG} | Input turn-on hysteresis | | - | 0.3 | - | V |
| I _{I(ON)} | Input turn-on current | V _{IG} = 3 V | - | - | 100 | μA |
| I _{I(OFF)} | Input turn-off current | V _{IG} = 1.5 V | 10 | - | - | μA |

STATUS CHARACTERISTICS

The status output is an open drain transistor, and requires an external pull-up circuit to indicate a logic high. Limits are at -40°C ≤ T_{mb} ≤ 150°C and typicals at T_{mb} = 25 °C unless otherwise stated. Refer to TRUTH TABLE.

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|-----------------|---|-------------------------|------|------|------|------|
| V _{SG} | Status clamping voltage | I _S = 100 μA | 5.5 | 7 | 8.5 | V |
| V _{SG} | Status low voltage | I _S = 100 μA | - | - | 1 | V |
| | | T _{mb} = 25°C | - | 0.7 | 0.8 | V |
| I _S | Status leakage current | V _{SG} = 5 V | - | - | 15 | μA |
| | | T _{mb} = 25°C | - | 0.1 | 1 | μA |
| I _S | Status saturation current ¹ | V _{SG} = 5 V | 2 | 7 | 12 | mA |
| R _S | Application information External pull-up resistor | | - | 47 | - | kΩ |

OPEN CIRCUIT DETECTION CHARACTERISTICS

An open circuit load can be detected in the on-state. Refer to TRUTH TABLE.

Limits are at -40°C ≤ T_{mb} ≤ 150°C and typical is at T_{mb} = 25 °C.

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|---------------------|-------------------------------|------------------------------|------|------|------|------|
| | Open circuit detection | 9 V ≤ V _{BG} ≤ 35 V | | | | |
| I _{L(TO)} | Low current detect threshold | | 0.24 | - | 1.6 | A |
| | | T _J = 25°C | 0.4 | 0.8 | 1.2 | A |
| ΔI _{L(TO)} | Hysteresis | | - | 0.16 | - | A |

¹ In a fault condition with the pull-up resistor short circuited while the status transistor is conducting. This condition should be avoided in order to prevent possible interference with normal operation of the device.

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UNDERVOLTAGE & OVERVOLTAGE CHARACTERISTICS

 Limits are at $-40^{\circ}\text{C} \leq T_{\text{mb}} \leq 150^{\circ}\text{C}$ and typicals at $T_{\text{mb}} = 25^{\circ}\text{C}$. Refer to TRUTH TABLE.

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|----------------------------|--|------------|------|------|------|------|
| $V_{\text{BG(UV)}}$ | Undervoltage Low supply threshold voltage ¹ | | 2 | 4.2 | 5.5 | V |
| $\Delta V_{\text{BG(UV)}}$ | Hysteresis | | - | 0.5 | - | V |
| $V_{\text{BG(OV)}}$ | Overvoltage High supply threshold voltage ² | | 40 | 45 | 50 | V |
| $\Delta V_{\text{BG(OV)}}$ | Hysteresis | | - | 1 | - | V |

TRUTH TABLE

| INPUT | ABNORMAL CONDITIONS DETECTED | | | | | LOAD OUTPUT | STATUS | DESCRIPTION |
|-------|------------------------------|----|------|----|----|-------------|--------|-----------------------------|
| | SUPPLY | | LOAD | | | | | |
| | UV | OV | LC | SC | OT | | | |
| L | X | X | X | X | X | OFF | H | off |
| H | 0 | 0 | 0 | 0 | 0 | ON | H | on & normal |
| H | 0 | 0 | 1 | 0 | 0 | ON | L | on & low current detect |
| H | 1 | 0 | X | X | X | OFF | H | supply undervoltage lockout |
| H | 0 | 1 | X | 0 | 0 | OFF | H | supply overvoltage shutdown |
| H | 0 | 0 | 0 | 1 | X | OFF | L | SC tripped |
| H | 0 | 0 | 0 | 0 | 1 | OFF | L | OT shutdown ³ |

KEY TO ABBREVIATIONS

| | | | |
|---|-----------------------|----|----------------------------------|
| L | logic low | UV | undervoltage |
| H | logic high | OV | overvoltage |
| X | don't care | LC | low current or open circuit load |
| 0 | condition not present | SC | short circuit |
| 1 | condition present | OT | overtemperature |

¹ Undervoltage sensor causes the device to switch off and reset.

² Overvoltage sensor causes the device to switch off to protect its load.

³ The status will continue to indicate OT (even if the input goes low) until the device cools below the reset threshold. Refer to OVERLOAD PROTECTION CHARACTERISTICS.

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OVERLOAD PROTECTION CHARACTERISTICS

$5.5 \text{ V} \leq V_{BG} \leq 35 \text{ V}$, limits are at $-40^\circ\text{C} \leq T_{mb} \leq 150^\circ\text{C}$ and typicals at $T_{mb} = 25^\circ\text{C}$ unless otherwise stated.

Refer to TRUTH TABLE.

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|--------------------|---|--|---------|----------|----------|------------------|
| $I_{L(lim)}$ | Overload protection Load current limiting | $V_{BL} = V_{BG}$ $V_{BG} \geq 9 \text{ V}$ | 34 | 45 | 64 | A |
| $V_{BL(TO)}$ | Short circuit load protection Battery load threshold voltage ¹ | $V_{BG} = 16 \text{ V}$ $V_{BG} = 35 \text{ V}$ | 8 15 | 10 20 | 12 25 | V V |
| $t_{d\ sc}$ | Response time ² | $V_{BL} > V_{BL(TO)}$ | - | 180 | 250 | μs |
| $T_{j(TO)}$ | Overtemperature protection Threshold junction temperature ³ | | 150 | 170 | 190 | $^\circ\text{C}$ |
| $\Delta T_{j(TO)}$ | Hysteresis | | - | 10 | - | $^\circ\text{C}$ |

SWITCHING CHARACTERISTICS

$T_{mb} = 25^\circ\text{C}$, $V_{BG} = 13 \text{ V}$, for resistive load $R_L = 13 \Omega$.

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|---------------|--------------------------------------|---------------------------------------|------|------|------|------------------|
| $t_{d\ on}$ | During turn-on Delay time | from input going high to 10% V_L | - | 40 | 60 | μs |
| dV/dt_{on} | Rate of rise of load voltage | 30% to 70% V_L | - | 0.35 | 1 | V/ μs |
| t_{on} | Total switching time | to 90% V_L | - | 140 | 200 | μs |
| $t_{d\ off}$ | During turn-off Delay time | from input going low to 90% V_L | - | 55 | 80 | μs |
| dV/dt_{off} | Rate of fall of load voltage | 70% to 30% V_L | - | 0.6 | 1 | V/ μs |
| t_{off} | Total switching time | to 10% V_L | - | 85 | 120 | μs |

CAPACITANCES

$T_{mb} = 25^\circ\text{C}$; $f = 1 \text{ MHz}$; $V_{IG} = 0 \text{ V}$. *designed in parameters.*

| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|----------|--------------------|-------------------------|------|------|------|------|
| C_{ig} | Input capacitance | $V_{BG} = 13 \text{ V}$ | - | 15 | 20 | pF |
| C_{bl} | Output capacitance | $V_{BL} = 13 \text{ V}$ | - | 250 | 350 | pF |
| C_{sg} | Status capacitance | $V_{SG} = 5 \text{ V}$ | - | 11 | 15 | pF |

1 The battery to load threshold voltage for short circuit protection is proportional to the battery supply voltage. After short circuit protection has operated, the input voltage must be toggled low for the switch to resume normal operation.

2 Measured from when the input goes high.

3 After cooling below the reset temperature the switch will resume normal operation.

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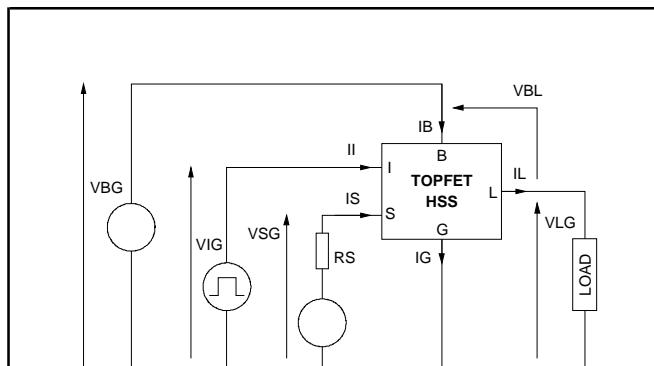


Fig.4. High side switch measurements schematic.
(current and voltage conventions)

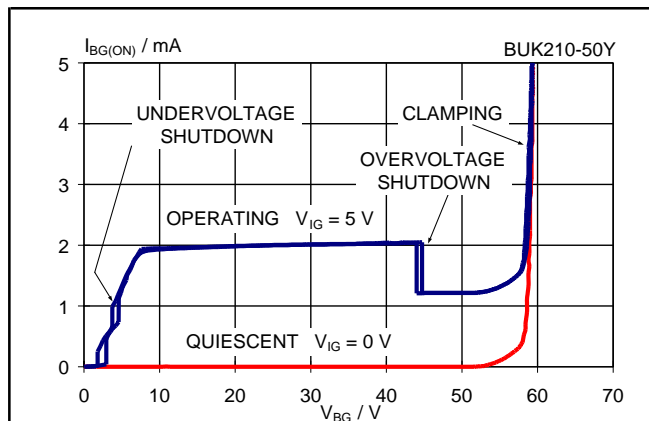


Fig.7. Typical supply characteristics, 25 °C.
 $I_G = f(V_{BG})$; parameter V_{IG}

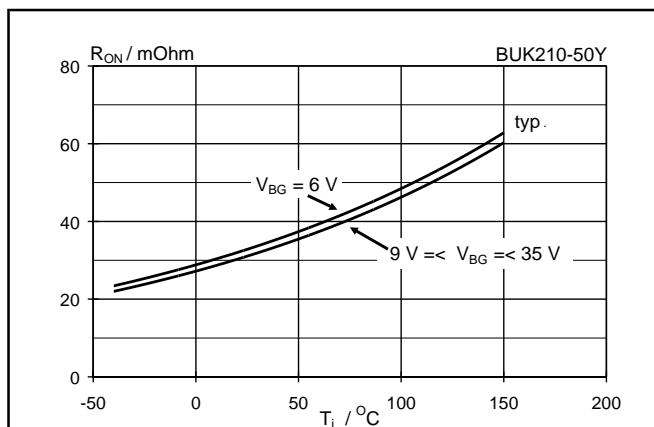


Fig.5. Typical on-state resistance, $t_p = 300 \mu\text{s}$.
 $R_{ON} = f(T_j)$; parameter V_{BG} ; condition $I_L = 10 \text{ A}$

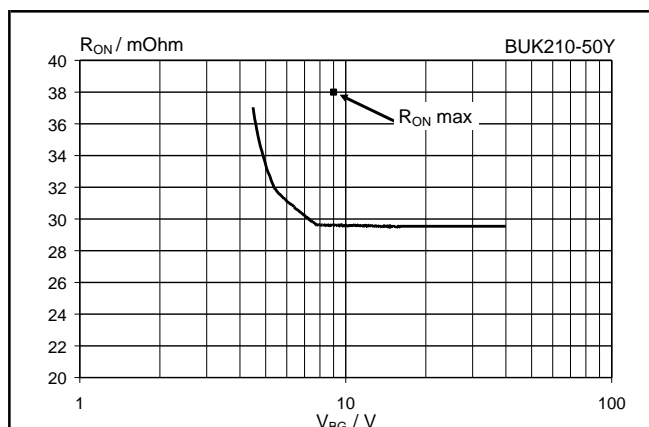


Fig.8. Typical on-state resistance, $T_j = 25 \text{ °C}$.
 $R_{ON} = f(V_{BG})$; condition $I_L = 10 \text{ A}$; $t_p = 300 \mu\text{s}$

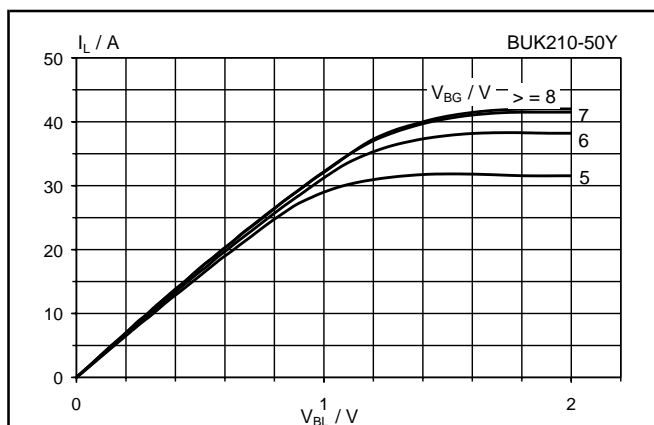


Fig.6. Typical on-state characteristics, $T_j = 25 \text{ °C}$.
 $I_L = f(T_j)$; parameter V_{BG} ; $t_p = 250 \mu\text{s}$

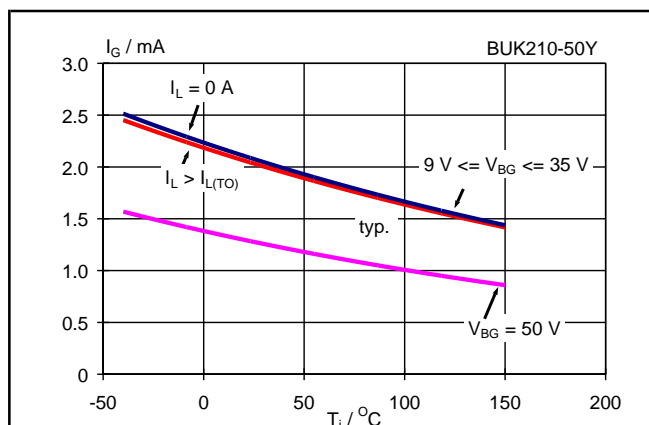
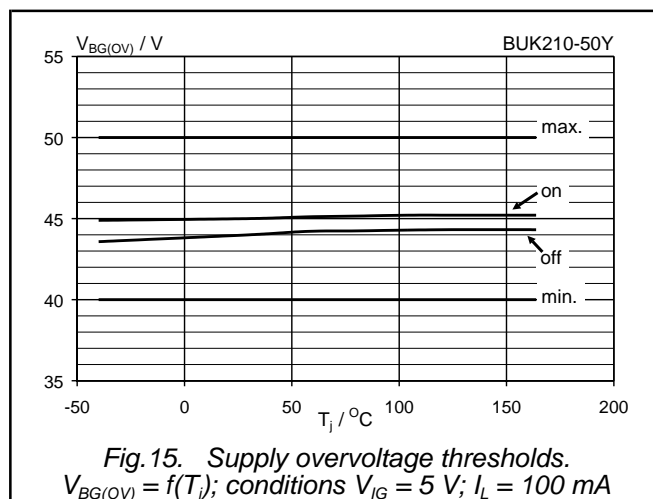
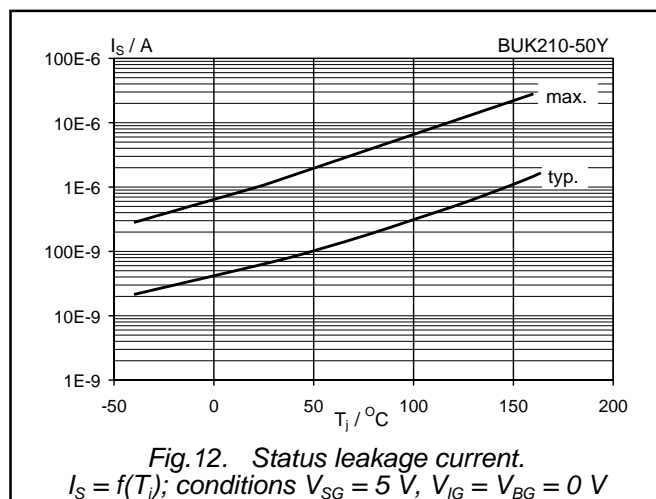
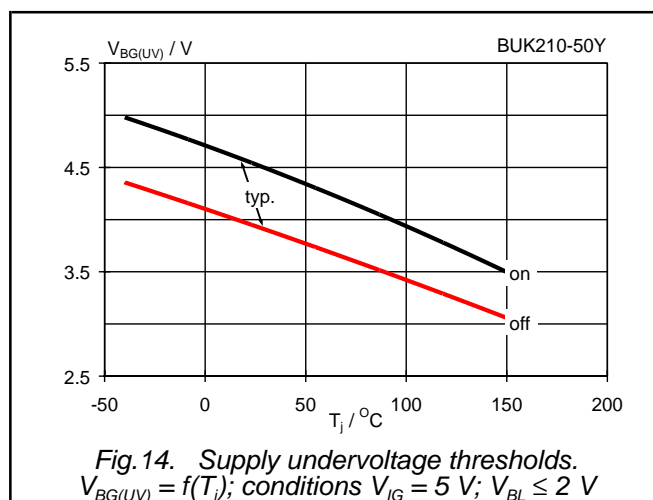
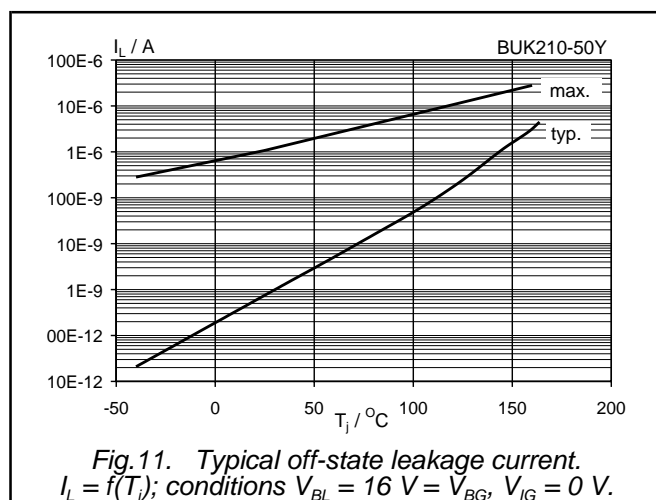
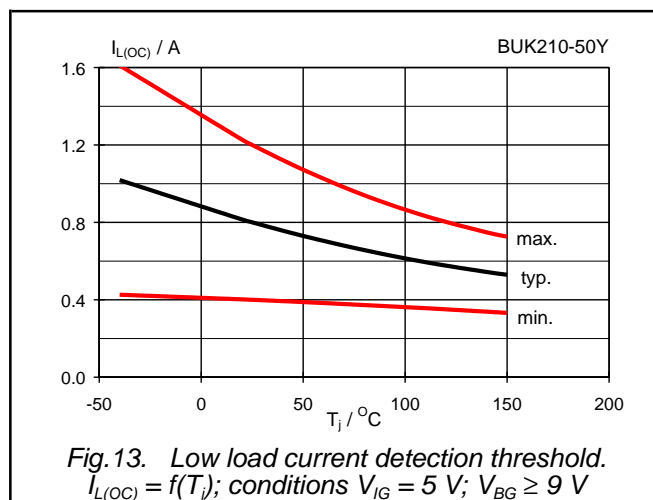
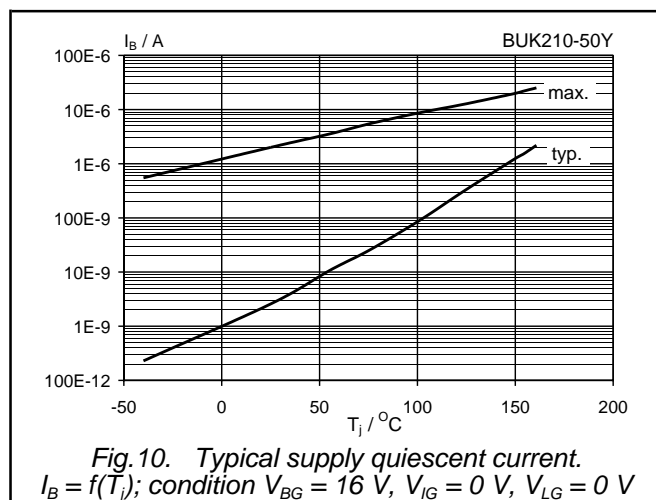


Fig.9. Typical operating supply current.
 $I_G = f(T_j)$; parameters I_L , V_{BG} ; condition $V_{IG} = 5 \text{ V}$

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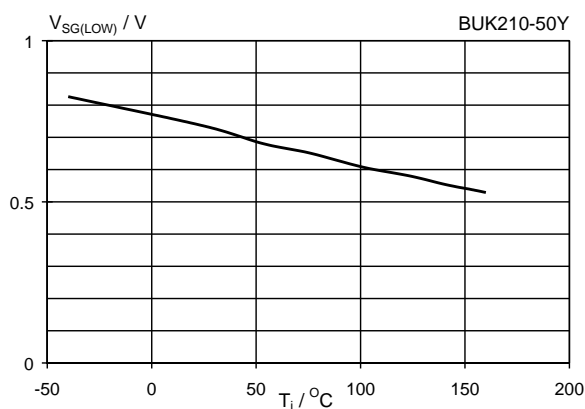


Fig. 16. Typical status low characteristic.
 $V_{SG} = f(T_j)$; conditions $V_{BG} \geq 9 V$, $I_S = 100 \mu A$

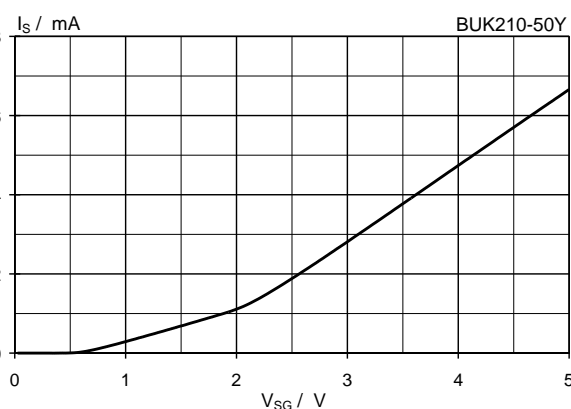


Fig. 19. Typical status low characteristic, $T_j = 25^\circ C$.
 $I_S = f(V_{SG})$; conditions $V_{IG} = 5 V$, $V_{BG} = 13 V$, $I_L = 0 A$

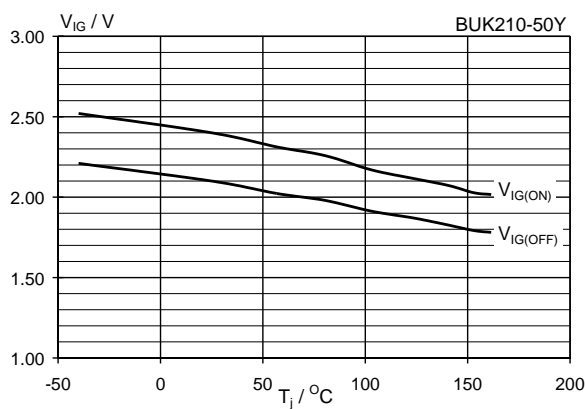


Fig. 17. Typical threshold voltage characteristic.
 $V_{IG} = f(T_j)$; condition $9 V \leq V_{BG} \leq 16 V$

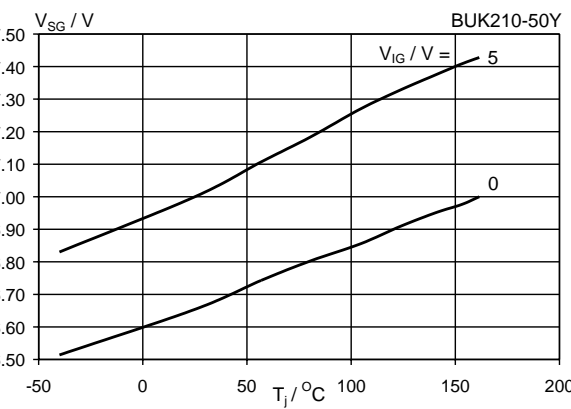


Fig. 20. Typical status clamping voltage.
 $V_{SG} = f(T_j)$; condition $I_S = 100 \mu A$, $V_{BG} = 13 V$

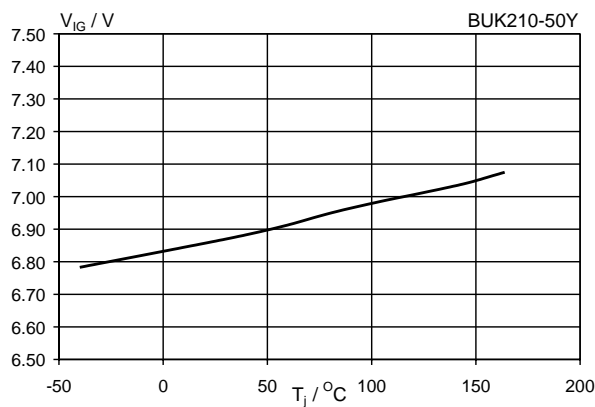


Fig. 18. Typical input clamping voltage.
 $V_{IG} = f(T_j)$; condition $I_L = 200 \mu A$, $V_{BG} = 13 V$

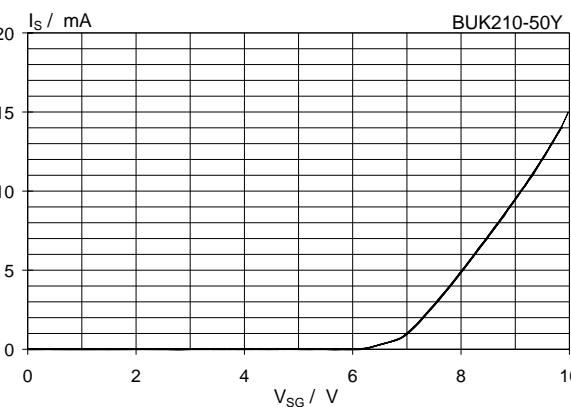
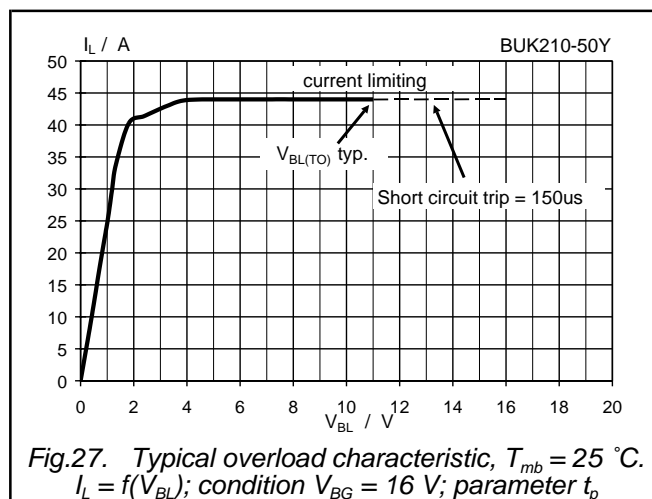
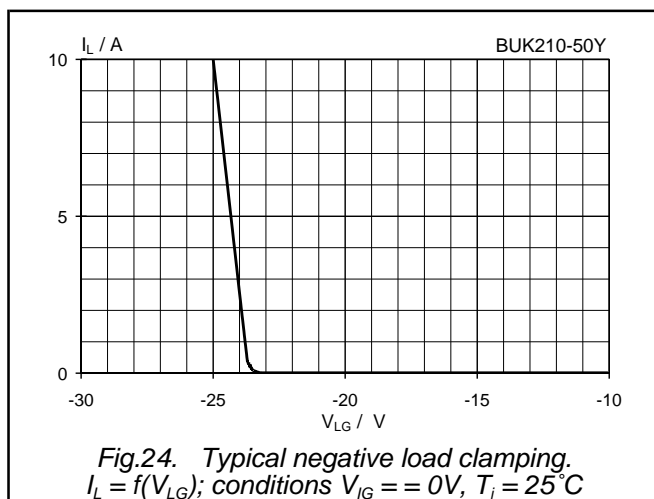
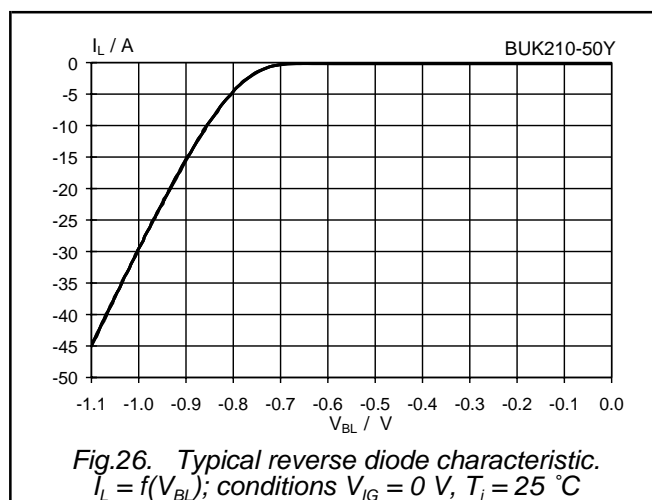
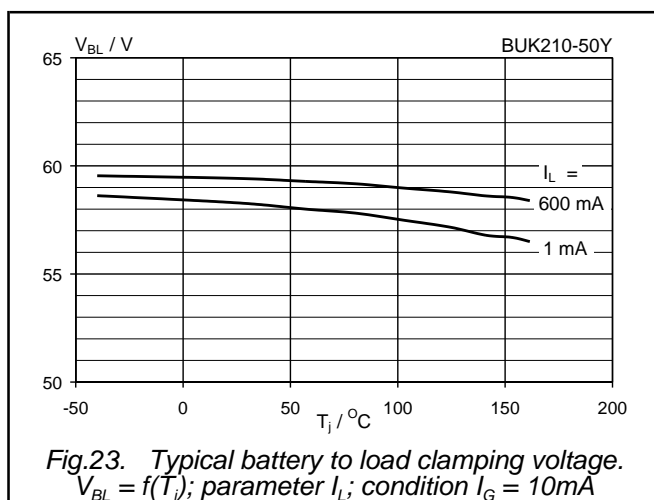
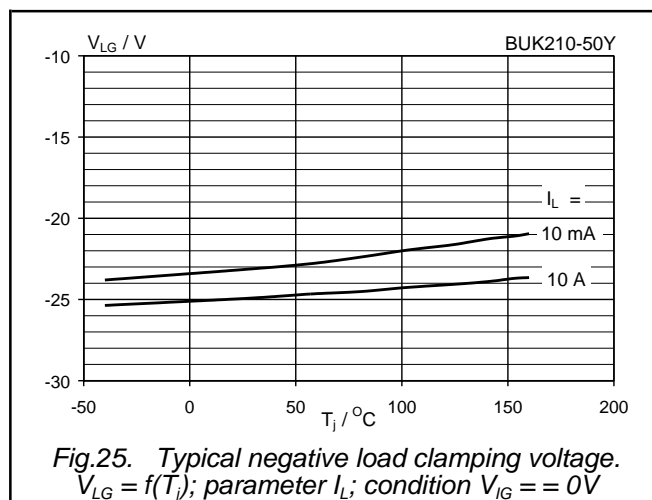
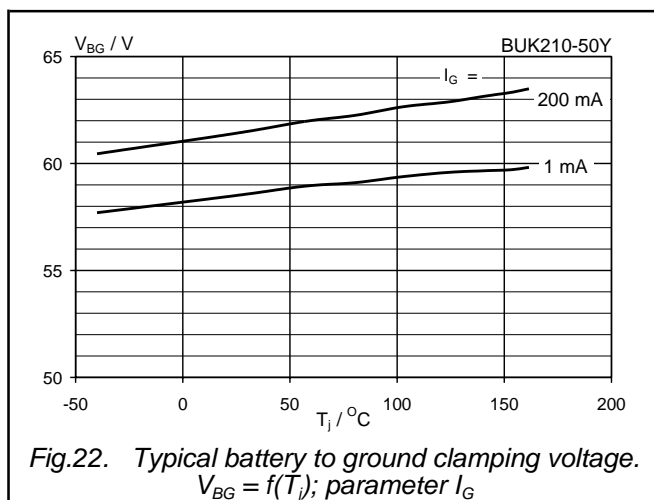


Fig. 21. Typical status characteristic, $T_j = 25^\circ C$.
 $I_S = f(V_{SG})$; conditions $V_{IG} = V_{BG} = 0 V$

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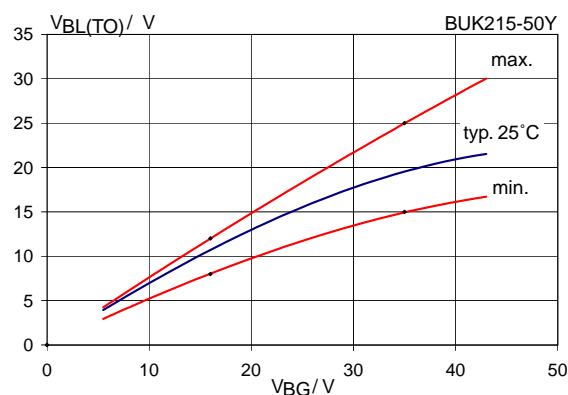


Fig.28. Short circuit load threshold voltage.
 $V_{BL(TO)} = f(V_{BG})$; conditions $-40^{\circ}\text{C} \leq T_{mb} \leq 150^{\circ}\text{C}$

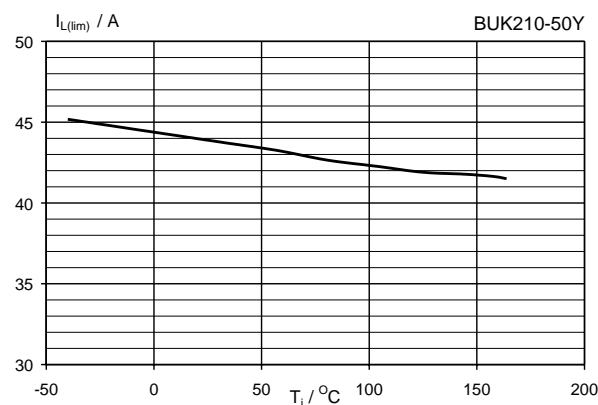


Fig.31. Typical overload current, $V_{BL} = 8\text{V}$.
 $I_L = f(T_j)$; parameter $V_{BG} = 13\text{V}$; $t_p = 300\text{ }\mu\text{s}$

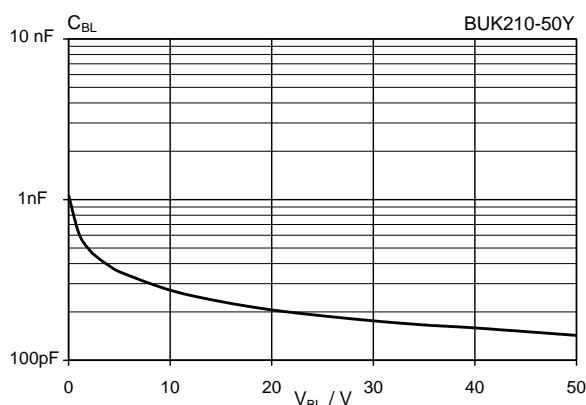


Fig.29. Typical output capacitance. $T_{mb} = 25^{\circ}\text{C}$
 $C_{bl} = f(V_{BL})$; conditions $f = 1\text{ MHz}$, $V_{IG} = 0\text{ V}$

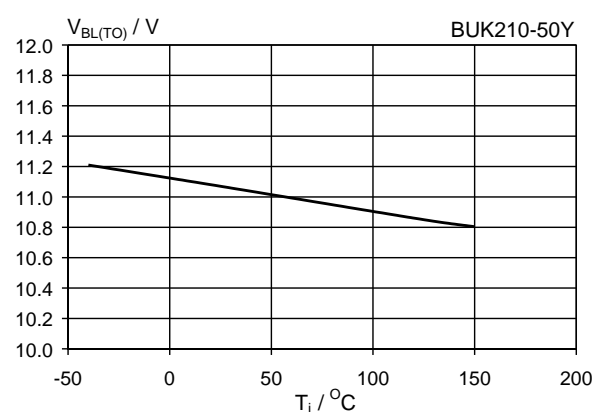


Fig.32. Typical short circuit load threshold voltage.
 $V_{BL(TO)} = f(T_j)$; condition $V_{BG} = 16\text{ V}$

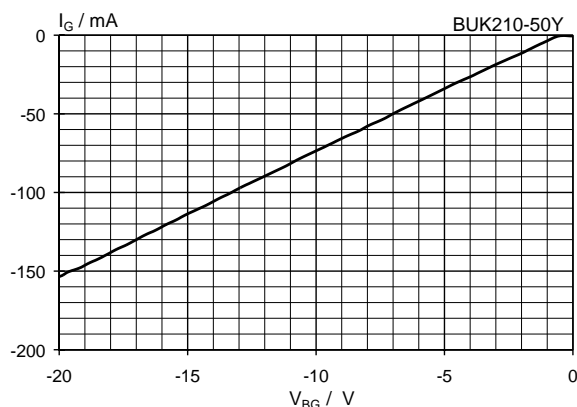


Fig.30. Typical reverse battery characteristic.
 $I_G = f(V_{BG})$; conditions $I_L = 0\text{ A}$, $T_j = 25^{\circ}\text{C}$

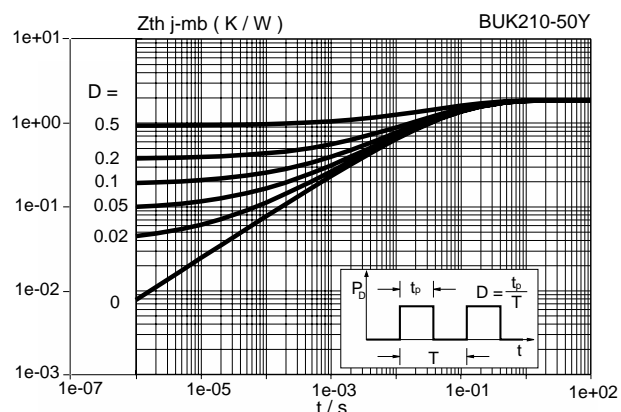


Fig.33. Transient thermal impedance.
 $Z_{th j-mb} = f(t)$; parameter $D = t_p / T$

PowerMOS transistor
TOPFET high side switch

BUK210-50Y

MECHANICAL DATA

Plastic single-ended package; heatsink mounted; 1 mounting hole;
5-lead TO-220 lead form option

SOT263B-01

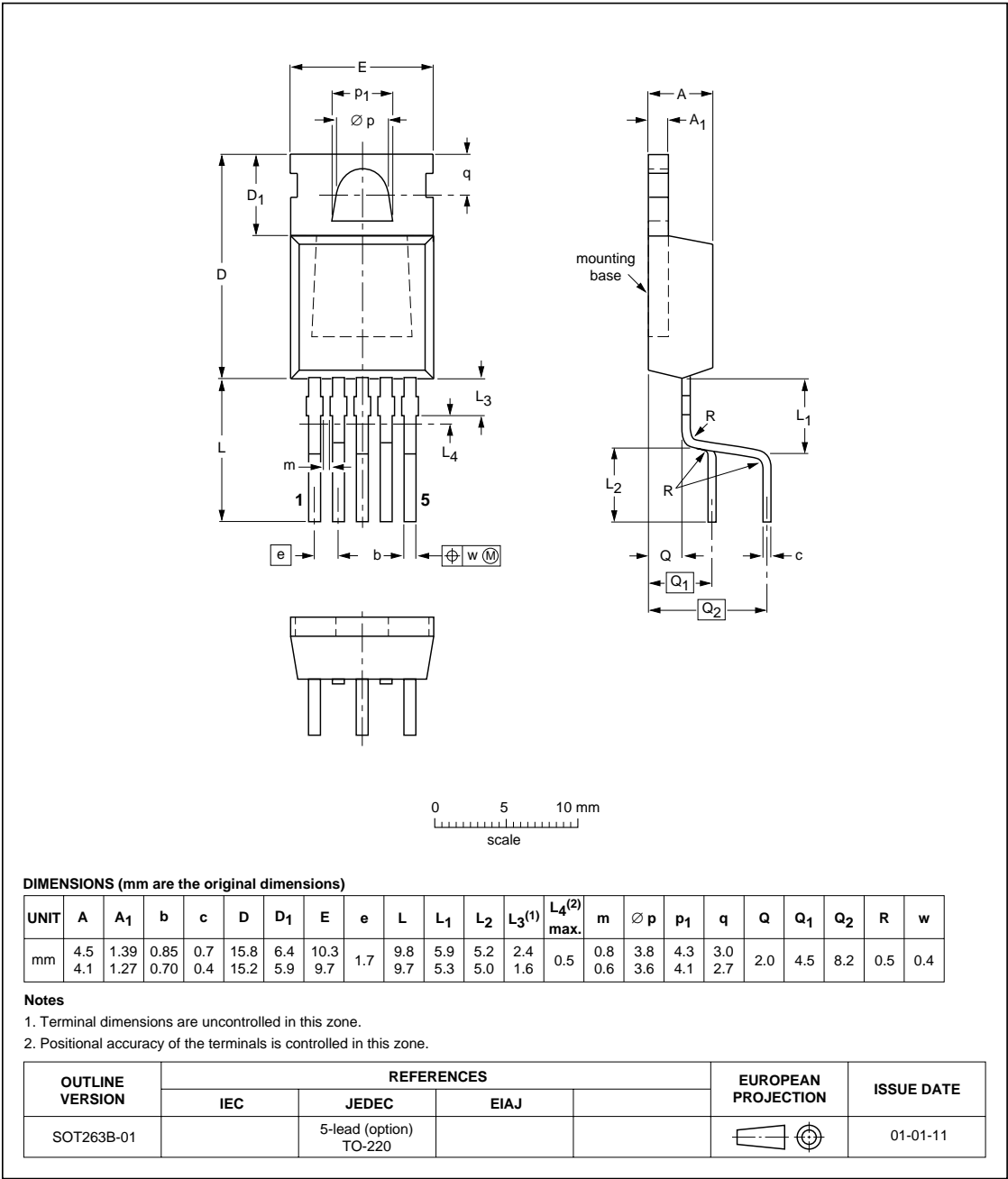


Fig.34. SOT263B package¹ leadform 263B-01, pin 3 connected to mounting base.

1 Refer to mounting instructions for TO220 envelopes. Epoxy meets UL94 VO at 1/8". Net mass: 2 g

PowerMOS transistor TOPFET high side switch

BUK210-50Y

DEFINITIONS

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|--|-----------------------------|---|
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| Application information | | |
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