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OM13054

LPC-Link 2

Demo board description

LPC-Link 2 is a stand-alone debug adapter that can be configured to support various development tools and IDEs by downloadable firmwares.

Available firmware images include:

J-Link by Segger

CMSIS-DAP by ARM

Code Red Technologies Redlink

The order code for this board is **OM13054**

Features

- Compatible with the LPCXpresso-IDE and Code Red Technologies Red Suite via the Redlink interface
- Compatible with tools/IDEs that support the Segger J-Link via downloadable firmware image.
- Compatible with tools/IDEs that support CMSIS-DAP by ARM via downloadable firmware image.

Descriptive summary

LPC-Link 2 stand alone debug adapter.

The LPC-Link 2 is the latest addition to the LPCXpresso development platform. It is an extensible, stand-alone debug adapter, that can be connector to virtually any development board, and that support a broad variety of development tools and IDEs via downloadable firmware images.

The LPC-Link 2 includes a standard 10-pin JTAG/SWD connector; a 20-pin JTAG/SWD/ETM connector; and analog, digital and serial expansion headers, making it a highly extensible platform.

Available downloads

- [Configuration tool for LPC-Link 2.](#)
- [LPCXpresso - Information about the LPCXpresso platform.](#)
- [LPCXpresso Forums](#)
- [Download the LPCXpresso IDE](#)

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