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74AC04, 74ACT04 Hex Inverter

Features

- I_{CC} reduced by 50% on 74AC only
- Outputs source/sink 24mA
- ACT04 has TTL-compatible inputs

General Description

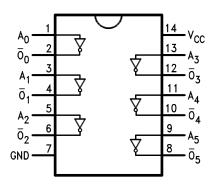
The AC/ACT04 contains six inverters.

Ordering Information								
Order Number	Package Number	Package Description						
74AC04SC	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow						
74AC04SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide						
74AC04MTC	MTC14	14-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide						
74AC04PC	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide						
74ACT04SC	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow						
74ACT04MTC	MTC14	14-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide						
74ACT04PC	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide						

Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering code. (PC not available in Tape and Reel.)

All packages are lead free per JEDEC: J-STD-020B standard.

Connection Diagram

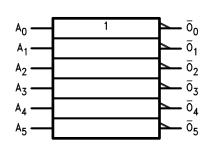


Pin Description

Pin Names	Description
A _n	Inputs
Ōn	Outputs

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Logic Symbol



IEEE/IEC

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Rating
V _{CC}	Supply Voltage	-0.5V to +7.0V
I _{IK}	DC Input Diode Current	
	$V_{I} = -0.5V$	–20mA
	$V_{I} = V_{CC} + 0.5V$	+20mA
VI	DC Input Voltage	-0.5V to V _{CC} + 0.5V
I _{OK}	DC Output Diode Current	
	$V_{O} = -0.5V$	–20mA
	$V_{O} = V_{CC} + 0.5V$	+20mA
Vo	DC Output Voltage	–0.5V to V _{CC} + 0.5V
Ι _Ο	DC Output Source or Sink Current	±50mA
I _{CC} or I _{GND}	DC V _{CC} or Ground Current per Output Pin	±50mA
T _{STG}	Storage Temperature	–65°C to +150°C
TJ	Junction Temperature	
	PDIP	140°C

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Rating
V _{CC}	Supply Voltage	
	AC	2.0V to 6.0V
	ACT	4.5V to 5.5V
VI	Input Voltage	0V to V _{CC}
Vo	Output Voltage	0V to V _{CC}
T _A	Operating Temperature	–40°C to +85°C
$\Delta V / \Delta t$	Minimum Input Edge Rate, AC Devices:	125mV/ns
	$\rm V_{IN}$ from 30% to 70% of $\rm V_{CC}, \rm V_{CC}$ @ 3.3V, 4.5V, 5.5V	
$\Delta V / \Delta t$	Minimum Input Edge Rate, ACT Devices:	125mV/ns
	$\rm V_{IN}$ from 0.8V to 2.0V, $\rm V_{CC}$ @ 4.5V, 5.5V	

				T _A = -	+25°C	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	
Symbol	Parameter	V _{CC} (V)	Conditions	Typ. Gu		uaranteed Limits	Units
V _{IH}	Minimum HIGH Level	3.0	$V_{OUT} = 0.1V$	1.5	2.1	2.1	V
	Input Voltage	4.5	or V _{CC} – 0.1V	2.25	3.15	3.15	
		5.5		2.75	3.85	3.85	
V _{IL}	Maximum LOW Level	3.0	$V_{OUT} = 0.1V$	1.5	0.9	0.9	V
	Input Voltage	4.5	or V _{CC} – 0.1V	2.25	1.35	1.35	
		5.5		2.75	1.65	1.65	
V _{OH}	Minimum HIGH Level	3.0	Ι _{ΟUT} =50μΑ	2.99	2.9	2.9	V
	Output Voltage	4.5		4.49	4.4	4.4	
		5.5		5.49	5.4	5.4	
			$V_{IN} = V_{IL} \text{ or } V_{IH}$:				
		3.0	I _{OH} = -12mA		2.56	2.46	
		4.5	I _{OH} = -24mA		3.86	3.76	
		5.5	$I_{OH} = -24 m A^{(1)}$		4.86	4.76	
V _{OL}	Maximum LOW Level Output Voltage	3.0	Ι _{ΟUT} = 50μΑ	0.002	0.1	0.1	V
		4.5		0.001	0.1	0.1	
		5.5		0.001	0.1	0.1	
			$V_{IN} = V_{IL} \text{ or } V_{IH}$				
		3.0	I _{OL} = 12mA		0.36	0.44	
		4.5	$I_{OL} = 24 \text{mA}$		0.36	0.44	
		5.5	$I_{OL} = 24 m A^{(1)}$		0.36	0.44	
I _{IN} ⁽³⁾	Maximum Input Leakage Current	5.5	$V_I = V_{CC}, \text{ GND}$		±0.1	±1.0	μA
I _{OLD}	Minimum Dynamic	5.5	V _{OLD} = 1.65V Max.			75	mA
I _{OHD}	Output Current ⁽²⁾	5.5	V _{OHD} = 3.85V Min.			-75	mA
I _{CC} ⁽³⁾	Maximum Quiescent Supply Current	5.5	$V_{IN} = V_{CC}$ or GND		2.0	20.0	μA

Notes:

1. All outputs loaded; thresholds on input associated with output under test.

2. Maximum test duration 2.0ms, one output loaded at a time.

3. $I_{\rm IN}$ and $I_{\rm CC}$ @ 3.0V are guaranteed to be less than or equal to the respective limit @ 5.5V $V_{\rm CC}.$

		V _{cc}		T _A = +		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	
Symbol	Parameter	(V)	Conditions	Typ. Guaranteed Limits		Units	
V _{IH}	Minimum HIGH Level	4.5	$V_{OUT} = 0.1V \text{ or}$	1.5	2.0	2.0	V
In	Input Voltage	5.5	V _{CC} – 0.1V	1.5	2.0	2.0	
V _{IL}	Maximum LOW Level	4.5	$V_{OUT} = 0.1V \text{ or}$	1.5	0.8	0.8	V
	Input Voltage	5.5	V _{CC} – 0.1V	1.5	0.8	0.8	
V _{OH}	Minimum HIGH Level	4.5	I _{OUT} =50μA	4.49	4.4	4.4	V
Output Voltage	Output Voltage	5.5]	5.49	5.4	5.4	
			$V_{IN} = V_{IL} \text{ or } V_{IH}$:				
		4.5	I _{OH} = -24mA		3.86	3.76	
		5.5	$I_{OH} = -24 m A^{(4)}$		4.86	4.76	
V _{OL}	Maximum LOW Level	4.5	Ι _{ΟUT} = 50μΑ	0.001	0.1	0.1	V
	Output Voltage	5.5		0.001	0.1	0.1	
			$V_{IN} = V_{IL} \text{ or } V_{IH}$:				
		4.5	I _{OL} = 24mA		0.36	0.44	
		5.5	I _{OL} = 24mA ⁽⁴⁾		0.36	0.44	
I _{IN}	Maximum Input Leakage Current	5.5	$V_I = V_{CC}, GND$		±0.1	±1.0	μA
I _{CCT}	Maximum I _{CC} /Input	5.5	$V_{\rm I} = V_{\rm CC} - 2.1V$	0.6		1.5	mA
I _{OLD}	Minimum Dynamic	5.5	V _{OLD} = 1.65V Max.			75	mA
I _{OHD}	Output Current ⁽⁵⁾	5.5	V _{OHD} = 3.85V Min.			-75	mA
I _{CC}	Maximum Quiescent Supply Current	5.5	$V_{IN} = V_{CC}$ or GND		4.0	40.0	μA

Notes:

4. All outputs loaded; thresholds on input associated with output under test.

5. Maximum test duration 2.0ms, one output loaded at a time.

AC Electrical Characteristics for AC

			T _A = +25°C, C _L = 50pF		$ \begin{array}{l} T_{A}=-40^{\circ}C \text{ to } +85^{\circ}C,\\ C_{L}=50pF \end{array} $			
Symbol	Parameter	V _{CC} (V) ⁽⁶⁾	Min.	Тур.	Max.	Min.	Max.	Units
t _{PLH}	Propagation Delay	3.3	1.5	4.5	9.0	1.0	10.0	ns
		5.0	1.5	4.0	7.0	1.0	7.5	
t _{PHL}	Propagation Delay	3.3	1.5	4.5	8.5	1.0	9.5	ns
		5.0	1.5	3.5	6.5	1.0	7.0	

Note:

6. Voltage range 3.3 is 3.3V \pm 0.3V. Voltage range 5.0 is 5.0V \pm 0.5V.

AC Electrical Characteristics for ACT

			T _A = +25°C, C _L = 50pF		$\label{eq:T_A} \begin{array}{c} T_A = -40^\circ C \text{ to } +85^\circ C, \\ C_L = 50 p F \end{array}$			
Symbol	Parameter	V _{CC} (V) ⁽⁷⁾	Min.	Тур.	Max.	Min.	Max.	Units
t _{PLH}	Propagation Delay	5.0	1.0	6.0	8.5	1.0	9.0	ns
t _{PHL}	Propagation Delay	5.0	1.0	5.5	8.0	1.0	8.5	ns

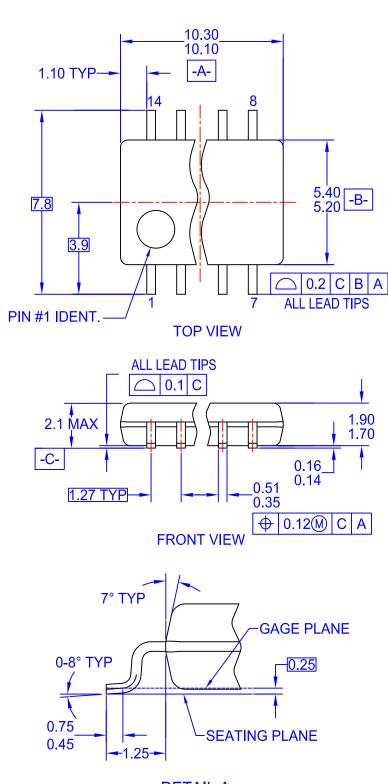
Note:

7. Voltage range 5.0 is $5.0V \pm 0.5V$.

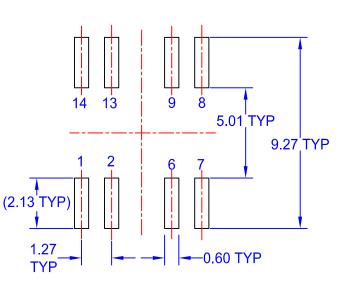
Capacitance

Symbol	Parameter	Conditions	Тур.	Units
C _{IN}	Input Capacitance	V _{CC} = OPEN	4.5	pF
V _{CC}	Power Dissipation Capacitance	$V_{CC} = 5.0V$	30.0	pF

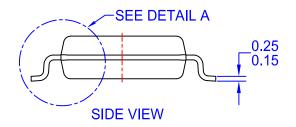








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