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FAN7601

Green Current Mode PWM Controller

Features

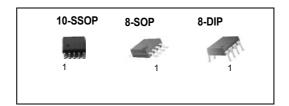
- · Green Current Mode PWM Control
- · Low Operating Current: Max 4mA
- Burst Mode Operation
- Internal High Voltage Start-up Switch
- Under Voltage Lockout (UVLO): 12V/8V
- Latch Protection & Soft Start Function
- Over Voltage Protection: 19V
- Operating Frequency up to 300kHz
- Max Duty Cycle: 95%

Typical Applications

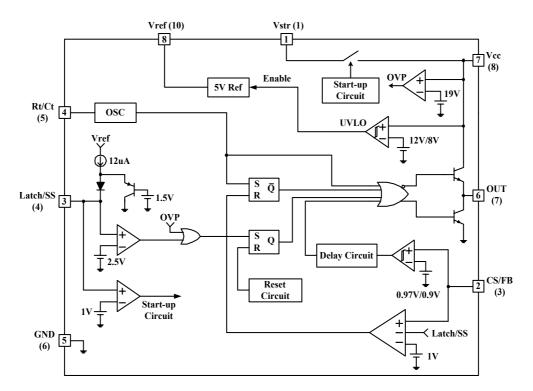
- · Off-Line Adapter Applications
- · Auxiliary Power Supplies

Description

The FAN7601 is a green programmable frequency current mode PWM controller. It is specially designed for the off-line adapter application and the auxiliary power supplies which require high efficiency at a light load and no load. The internal high voltage start-up switch and the burst mode reduce the power loss. The FAN7601 includes some protections such as latch protection and over voltage protection. The latch protection can be used for over voltage protection and/or thermal protection and so on. And the soft start prevents the output voltage over shoot at start up.

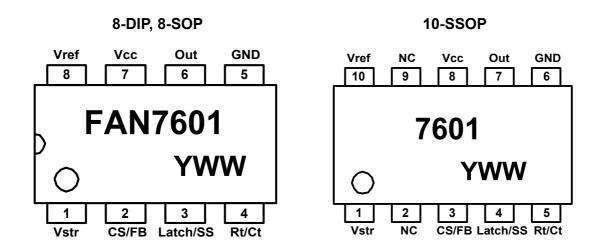


Internal Block Diagram



^{*()} is 10-SSOP PIN Number

Pin Assignments



Pin Definitions

Pin Number	Pin Name	Pin Function Description
1 (1)	Vstr	Start-up
2 (3)	CS/FB	Current Sense and Feedback
3 (4)	Latch/SS	Latch Protection and Soft Start
4 (5)	Rt/Ct	Oscillator Timing
5 (6)	GND	Ground
6 (7)	Out	Gate Drive Output
7 (8)	Vcc	IC Power Supply
8 (10)	V _{ref}	Voltage Reference

^{*()} is 10-SSOP PIN Number

Absolute Maximum Ratings

(Ta = 25°C, unless otherwise specified)

Characteristics	Symbol	Value	Unit	
Supply Voltage	Vcc	20	V	
Input Voltage CS/FB	Vcs/fb	-0.3 to 20	V	
Operating Temperature	Topr	-25 to +125	°C	
Storage Temperature	TSTG	-55 to +150	°C	
Junction Temperature	Tj	150	°C	
Output Current	lo	250	mA	
Vstr Input Voltage	Vstr	500	V	
ESD Capability, HBM Model (All pins except Vcc and Vstr)	-	2.0	kV	
ESD Capability, Machine Model	-	300	V	
	8-DIP		100	
Thermal Resistance, Junction to Air	8-SOP	Rθja	180	°C/W
	10-SSOP		130	1

Electrical Characteristics

 $(T_a = -25^{\circ}C \sim 125^{\circ}C, Vcc = 14V, Rt = 9.5k\Omega, Ct = 2.2nF unless otherwise specified)$

Characteristics	Symbol	Conditions	Min.	Тур.	Max.	Unit		
REFERENCE SECTION								
Reference Output Voltage	V _{ref}	I _O = 1mA	4.85	5.00	5.15	V		
Line Regulation	ΔV _{ref1}	V _{CC} = 10V ~ 18V	-	10	20	mV		
Load Regulation	ΔVref2	IO = 1mA ~ 10mA	-	20	30	mV		
OSCILLATOR SECTION								
Initial Accuracy	Fosc	-	90	100	110	kHz		
Voltage Stability	STV	V _C C = 10V ~ 18V	-	1.0	1.5	%		
Amplitude	Vocs	Vpin4 peak-to-peak	-	1.25	-	V		
PWM SECTION								
CS/FB Threshold Voltage1	VCS/FB1	-	0.9	1.0	1.1	V		
Maximum Duty Cycle	DMAX	Ta = 25°C	92	95	98	%		
Minimum Duty Cycle	DMIN	-	-	-	0	%		
BURST MODE SECTION			l .	I				
CS/FB Threshold Voltage2 ⁽¹⁾	VCS/FB2	-	0.77	0.97	1.17	V		
CS/FB Threshold Voltage3 ⁽¹⁾	VCS/FB3	-	0.7	0.9	1.1	V		
SOFT START SECTION			l .	I				
Soft Start Current	Iss	Vpin3 = GND	9	12	15	μА		
Soft Start Limit Voltage ⁽²⁾	VsL	ISS = 1μA	1.2	1.5	1.8	V		
PROTECTION SECTION			l .	I				
Latch Voltage	VLATCH	-	2.25	2.5	2.75	V		
Over Voltage Protection	Vove	-	18	19	20	V		
UVLO SECTION								
Start Threshold Voltage	VtH	-	11	12	13	V		
Minimum Operating Voltage	VtL	-	7	8	9	V		
TOTAL CURRENT SECTION			l .	I				
Operating Supply Current	lop	-	-	3	4	mA		
OUTPUT SECTION			l .	I				
Low Output Voltage	VoL	Ta =25°C, I _O = 100mA	-	2	2.5	V		
High Output Voltage	Voн	Ta =25°C, I _O = -100mA	11.5	12	14	V		
Rising Time ⁽¹⁾	Tr	Ta =25°C, CI = 1nF	-	45	150	ns		
Falling Time ⁽¹⁾	T _f	Ta =25°C, CI = 1nF	-	35	150	ns		
START UP SECTION								
VSTR Start-up Current	Istr	V _{str} = 30V, Ta =25°C	0.5	1	1.5	mA		

Note:

^{1.} These parameters, although guaranteed, are not 100% tested in production.

^{2.} It is recommended to connect $1M\Omega$ resistor between the Latch/SS pin and GND to prevent abnormal operation of the latch protection by noise coupling.

Typical Performance Characteristics

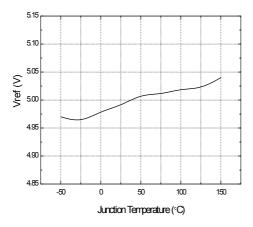


Figure 1. Trimmed Reference Voltage

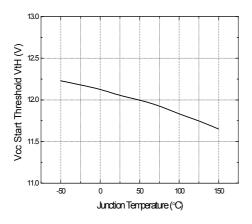


Figure 3. Vcc Start Threshold Voltage

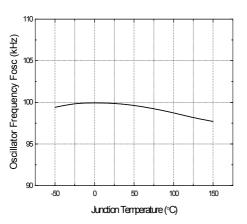


Figure 5. Oscillator Frequency

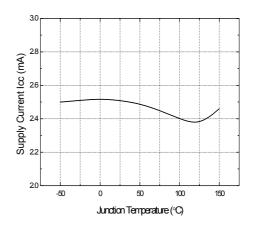


Figure 2. Supply Current

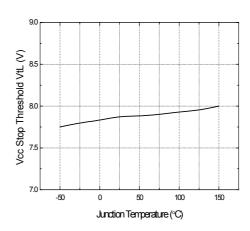


Figure 4. Vcc Stop Threshold Voltage

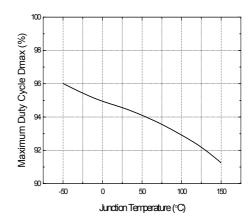


Figure 6. Maximum Duty Cycle

Typical Performance Characteristics (Continued)

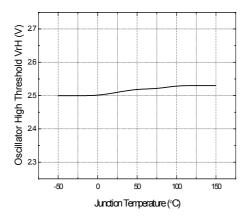


Figure 7. Oscillator High Threshold Voltage

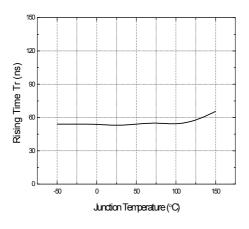


Figure 9. Output Rising Time

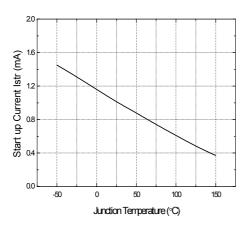


Figure 11. Start-up Current

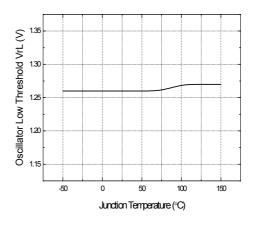


Figure 8. Oscillator Low Threshold Voltage

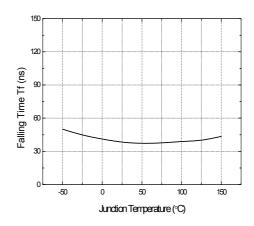


Figure 10. Output Falling Time

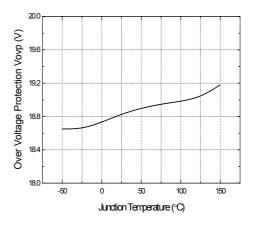
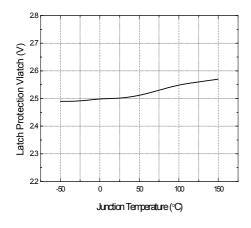


Figure 12. Over Voltage Protection Level

Typical Performance Characteristics (Continued)



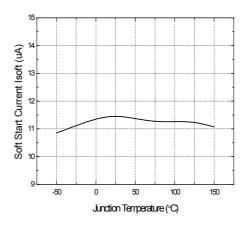


Figure 13. Latch Protection Voltage

Figure 14. Soft Start Current

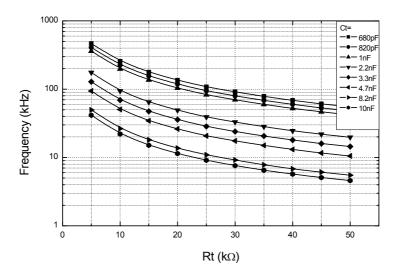
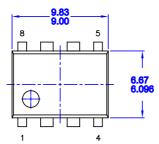
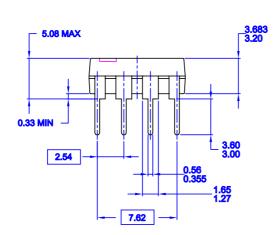


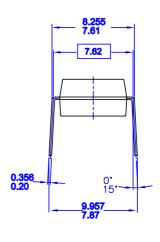
Figure 15. Oscillator Frequency Characteristic

Mechanical Dimensions

8DIP







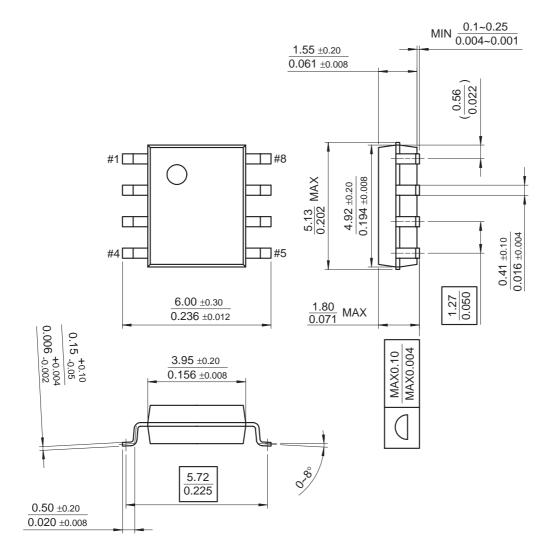
NOTES: UNLESS OTHERWISE SPECIFIED
A) THIS PACKAGE CONFORMS TO
JEDEC MS-001 VARIATION BA
B) ALL DIMENSIONS ARE IN MILLIMETERS.
C) DIMENSIONS ARE EXCLUSIVE OF BURRS,
MOLD FLASH, AND TIE BAR EXTRUSIONS.
D) DIMENSIONS AND TOLERANCES PER
ASME Y14.5M-1994

MKT-N08FrevB

Mechanical Dimensions (Unit: mm) (Continued)

Package

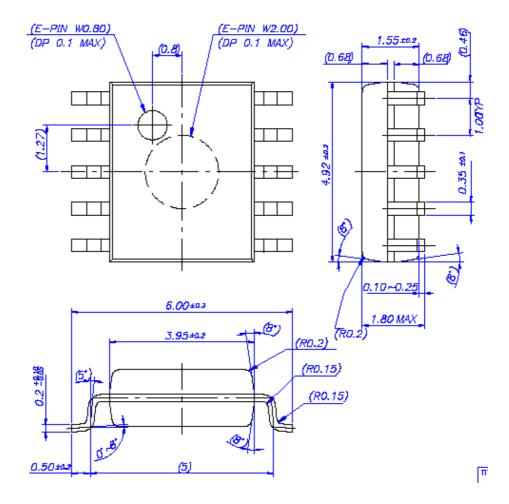
8-SOP



Mechanical Dimensions (Unit: mm) (Continued)

Package

10-SSOP-225



Ordering Information

Device	Package	Operating Temp.
FAN7601N	8-DIP	
FAN7601M	8-SOP	-25°C ~ 125°C
FAN7601G	10-SSOP	

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