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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

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2SC3503/KSC3503 NPN Epitaxial Silicon Transistor

Applications

- Audio, Voltage Amplifier and Current Source
- CRT Display, Video Output
- General Purpose Amplifier

Features

- High Voltage : V_{CEO}= 300V
- Low Reverse Transfer Capacitance : C_{re} = 1.8pF at V_{CB} = 30V
- Excellent Gain Linearity for low THD
- High Frequency: 150MHz
- Full thermal and electrical Spice models are available
- Complement to 2SA1381/KSA1381.

Absolute Maximum Ratings* T_a = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
BV _{CBO}	Collector-Base Voltage	300	V
BV _{CEO}	Collector-Emitter Voltage	300	V
BV _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current(DC)	100	mA
I _{CP}	Collector Current(Pulse)	200	mA
P _C	Total Device Dissipation, $T_C=25^{\circ}C$ $T_C=125^{\circ}C$	7 1.2	W W
T _J , T _{STG}	Junction and Storage Temperature	- 55 ~ +150	°C

* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Thermal Characteristics* T_{a=25°C} unless otherwise noted

Symbol	Parameter	Max.	Units
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case	17.8	°C/W

* Device mounted on minimum pad size

h_{FE} Classification

Classification	С	D	E	F
h _{FE}	40 ~ 80	60 ~ 120	100 ~ 200	160 ~ 320



TO-126

1. Emitter 2.Collector 3.Base

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV_{CBO}	Collector-Base Breakdown Voltage	$I_{C} = 10 \mu A, I_{E} = 0$	300			V
BV _{CEO}	Collecto- Emitter Breakdown Voltage	$I_{\rm C} = 1$ mA, $I_{\rm B} = 0$	300			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_{E} = 10 \mu A, I_{C} = 0$	5			V
I _{CBO}	Collector Cut-off Current	$V_{CB} = 200V, I_E = 0$			0.1	μA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 4V, I_{C} = 0$			0.1	μA
h _{FE}	DC Current Gain	$V_{CE} = 10V, I_{C} = 10mA$	40		320	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_{\rm C} = 20 {\rm mA}, I_{\rm B} = 2 {\rm mA}$			0.6	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_{\rm C} = 20 {\rm mA}, I_{\rm B} = 2 {\rm mA}$			1	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 30V, I_{C} = 10mA$		150		MHz
C _{ob}	Output Capacitance	V _{CB} = 30V, f = 1MHz		2.6		pF
C _{re}	Reverse Transfer Capacitance	V _{CB} = 30V, f = 1MHz		1.8		pF

* Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%

Ordering Information

Part Number*	Marking	Package	Packing Method	Remarks
2SC3503CSTU	2SC3503C	TO-126	TUBE	hFE1 C grade
2SC3503DSTU	2SC3503D	TO-126	TUBE	hFE1 D grade
2SC3503ESTU	2SC3503E	TO-126	TUBE	hFE1 E grade
2SC3503FSTU	2SC3503F	TO-126	TUBE	hFE1 F grade
KSC3503CSTU	C3503C	TO-126	TUBE	hFE1 C grade
KSC3503DSTU	C3503D	TO-126	TUBE	hFE1 D grade
KSC3503ESTU	C3503E	TO-126	TUBE	hFE1 E grade
KSC3503FSTU	C3503F	TO-126	TUBE	hFE1 F grade

* 1. Affix "-S-" means the standard TO126 Package (see package dimensions). If the affix is "-STS-" instead of "-S-", that mean the short-lead TO126 package. 2. Suffix "-TU" means the tube packing, The Suffix "TU" could be replaced to other suffix character as packing method.

Typical Characteristics

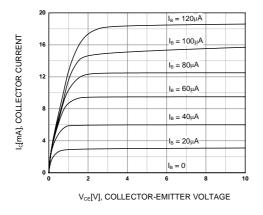


Figure 1. Static Characteristic

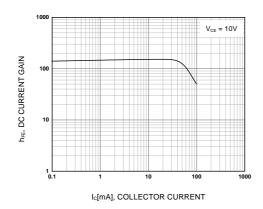


Figure 3. DC current Gain

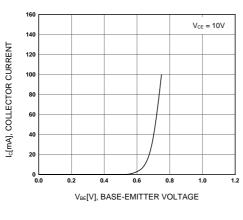


Figure 5. Base-Emitter On Voltage

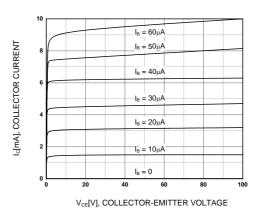


Figure 2. Static Characteristic

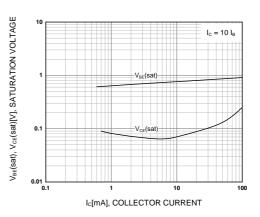
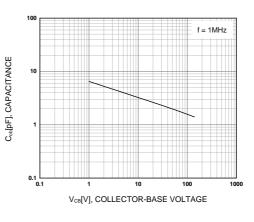
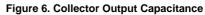


Figure 4. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage





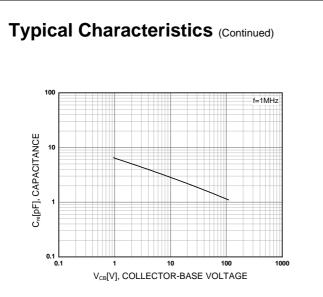


Figure 7. Reverse Transfer Capacitance

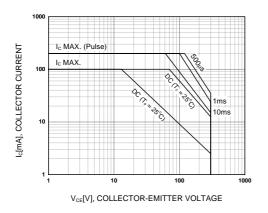


Figure 9. Safe Operating Area

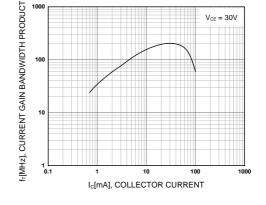


Figure 8. Current Gain Gandwidth Product

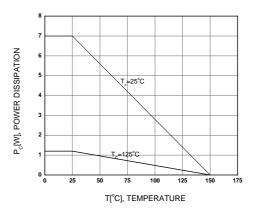
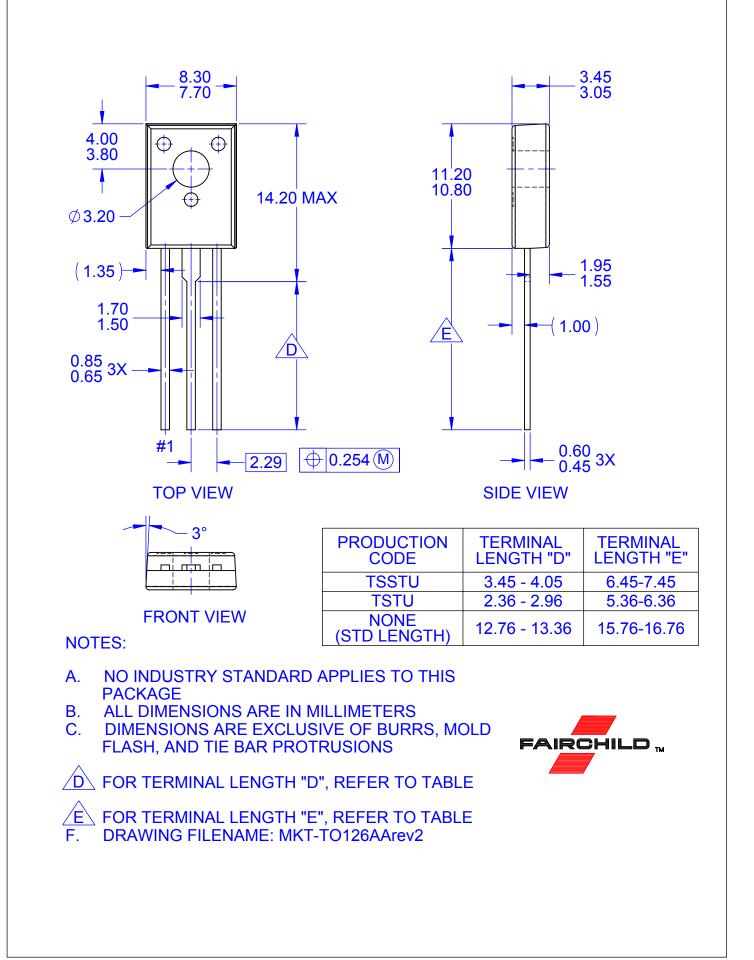


Figure 10. Power Derating



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