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September 2015

KSC5019 NPN Epitaxial Silicon Transistor

Features

- · Low Saturation
- $V_{CE}(sat) = 0.5 \text{ V at } I_{C} = 2 \text{ A}, I_{B} = 50 \text{ mA}$



Ordering Information

Part Number	Top Mark	Package	Packing Method
KSC5019MTA	KSC5019MTA C5019		Ammo

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage	30	V
V _{CES}	Collector-Emitter Voltage	30	V
V _{CEO}	Collector-Emitter Voltage	10	V
V _{EBO}	Emitter-Base Voltage	6	V
I _C	Collector Current (DC)	2	Α
I _{CP}	Collector Current (Pulse) ⁽¹⁾	5	Α
I _B	Base Current	2	Α
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 to 150	°C

Note:

1. PW \leq 10 ms, duty cycle \leq 30%

Thermal Characteristics(2)

Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Value	Unit
P _C	Collector Power Dissipation	750	mW
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	160	°C/W

Note:

2. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

Electrical Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I _{CBO}	Collector Cut-Off Current	$V_{CB} = 30 \text{ V}, I_{E} = 0$			100	nA
I _{EBO}	Emitter Cut-Off Current	$V_{EB} = 6 \text{ V}, I_{C} = 0$			100	nA
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = 10 mA, I _B = 0	10			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 1 \text{ mA}, I_C = 0$	6			V
h _{FE1}	- DC Current Gain	$V_{CE} = 1 \text{ V}, I_{C} = 0.5 \text{ A}$	140		600	
h _{FE2}	DC Current Gain	V _{CE} = 1 V, I _C = 2 A	70	200		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 2 \text{ A}, I_B = 50 \text{ mA}$		0.2	0.5	V
V _{BE} (on)	Base-Emitter On Voltage	V _{CE} = 1 V, I _C = 2 A		0.86	1.50	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 1 \text{ V}, I_{C} = 0.5 \text{ A}$		150		MHz
C _{ob}	Output Capacitance	V _{CB} = 10 V, I _E = 0, f = 1 MHz		27		pF

h_{FE} Classification

Classification	L	M	N	Р
h _{FF1}	140 ~ 240	200 ~ 330	300 ~ 450	420 ~ 600

Typical Performance Characteristics

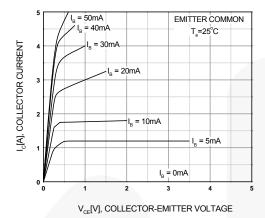


Figure 1. Static Characteristic

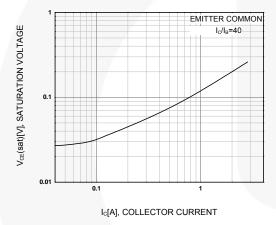


Figure 3. Collector-Emitter Saturation Voltage

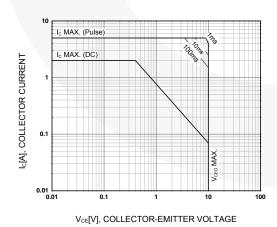


Figure 5. Safe Operating Area

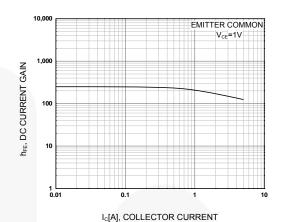


Figure 2. DC Current Gain

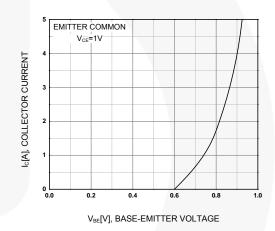


Figure 4. Base-Emitter On Voltage

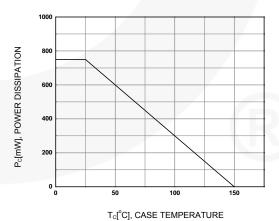
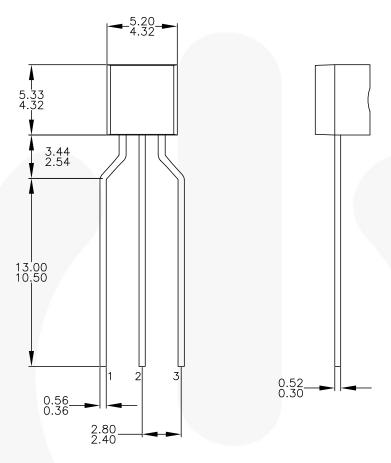
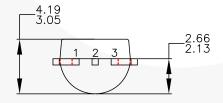


Figure 6. Power Derating

Physical Dimensions





NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC. ALL DIMENSIONS ARE IN MILLIMETERS. DRAWING CONFORMS TO ASME Y14.5M-2009. DRAWING FILENAME: MKT-ZAO3FREV3. FAIRCHILD SEMICONDUCTOR.

Figure 7. 3-Lead, TO-92, Molded, 0.2 In Line Spacing Lead Form, Ammo Type





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Definition of Terms

Definition of Terms				
Datasheet Identification	Product Status	Definition		
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
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