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FAIRCHILD

SEMICONDUCTOR TM

KSD363

B/W TV Horizontal Deflection Output

- Collector-Base Voltage : V_{CBO}=300V
 Collector Current : I_C=6A
- Collector Dissipation : $P_C=40W(T_C=25^{\circ}C)$



1.Base 2.Collector 3.Emitter

NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

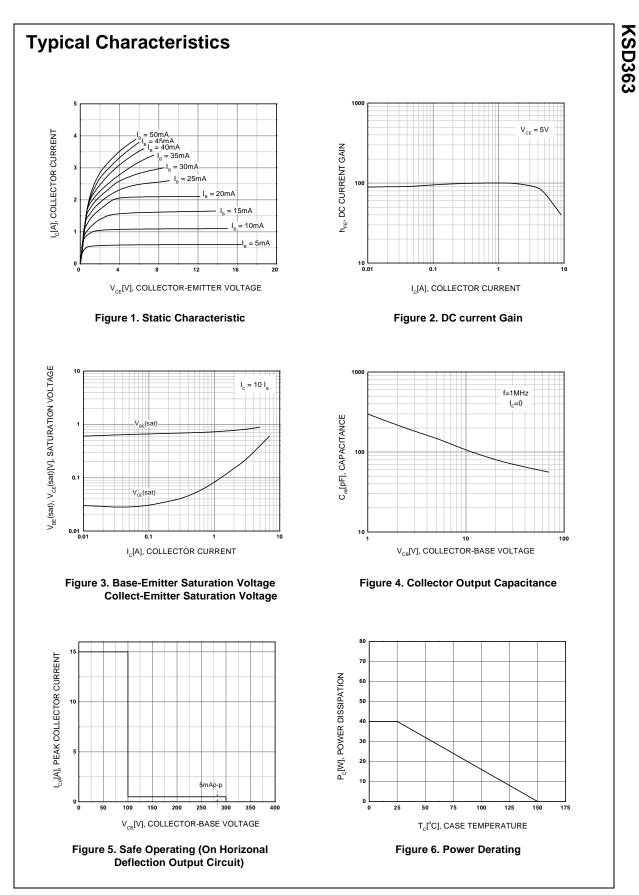
Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	300	V
V _{CEO}	Collector-Emitter Voltage	120	V
V _{EBO}	Emitter-Base Voltage	8	V
I _C	Collector Current	6	A
P _C	Collector Dissipation (T _C =25°C)	40	W
TJ	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

Electrical Characteristics $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{C} = 1 \text{ mA}, I_{E} = 0$	300			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_{\rm C} = 20 {\rm mA}, I_{\rm B} = 0$	120			V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E = 1mA, I _C = 0	8			V
I _{CBO}	Collector Cut-off Current	V _{CB} = 250V, I _E = 0			1	mA
h _{FE}	DC Current Gain	$V_{CE} = 5V, I_{C} = 1A$	40		240	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 1A, I _B = 0.1A			1	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 1A, I _B = 0.1A			1.5	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 5V, I_{C} = 0.5A$		10		MHz

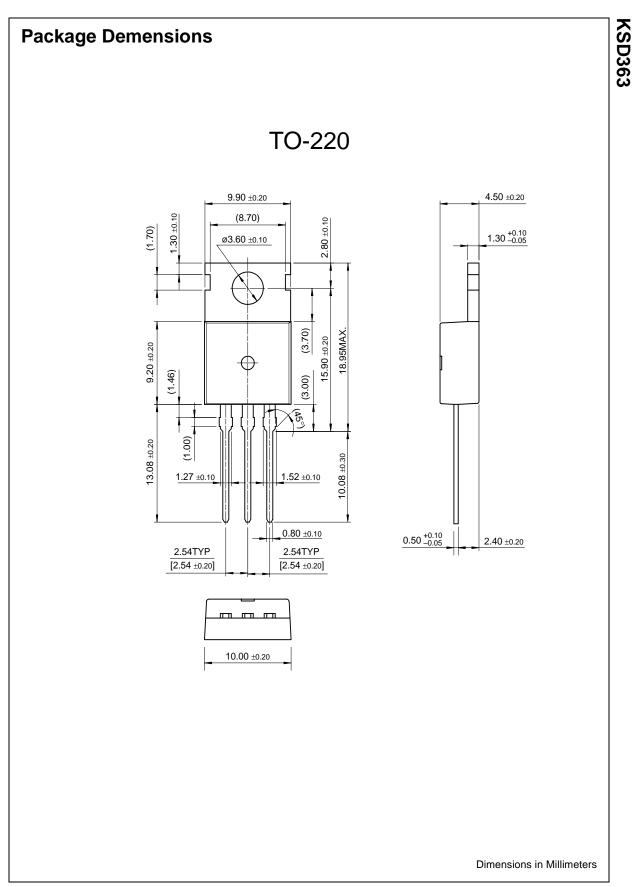
h_{FE} Classification

Classification	R	0	Y
h _{FE}	40 ~ 80	70 ~ 140	120 ~ 240



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Rev. A, February 2000



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Definition of Terms

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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