Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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H7N1002LD, H7N1002LS, H7N1002LM

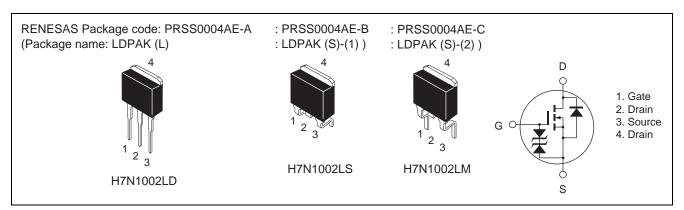
Silicon N Channel MOS FET High Speed Power Switching

REJ03G1131-0800 Rev.8.00 Nov 13, 2009

Features

- Low on-resistance $R_{DS (on)} = 8 \text{ m}\Omega \text{ typ.}$
- Low drive current
- Available for 4.5 V gate drive

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	100	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	75	Α
Drain peak current	I _{D (pulse)} Note 1	300	Α
Body to drain diode reverse drain current	I _{DR}	75	Α
Avalanche current	I _{AP} Note 3	50	Α
Avalanche energy	E _{AR} Note 3	166	mJ
Channel dissipation	Pch Note 2	100	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at Tc = 25°C

3. Value at Tch = 25°C, Rg \geq 50 Ω

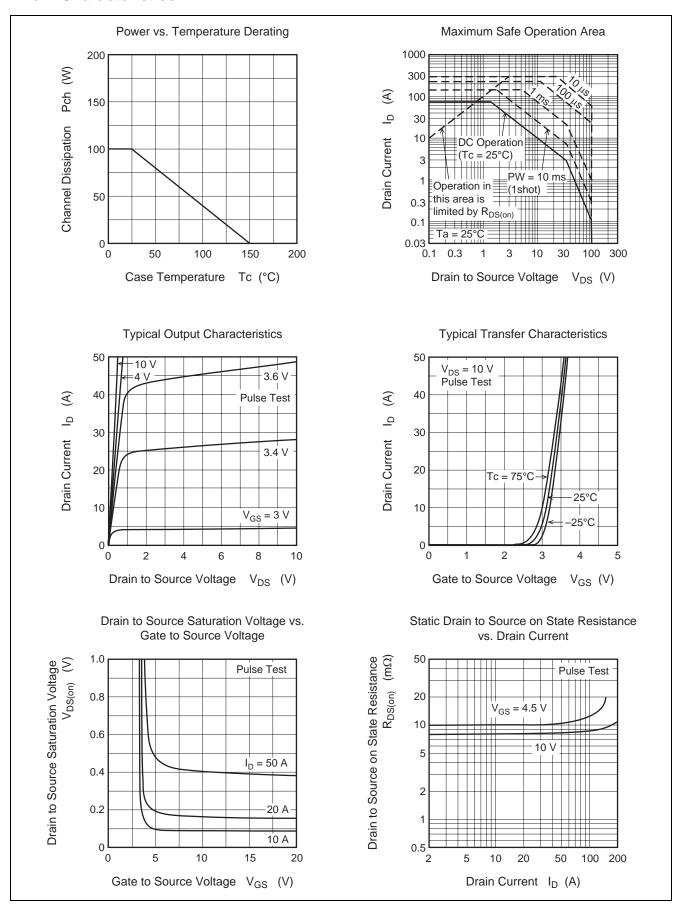
Electrical Characteristics

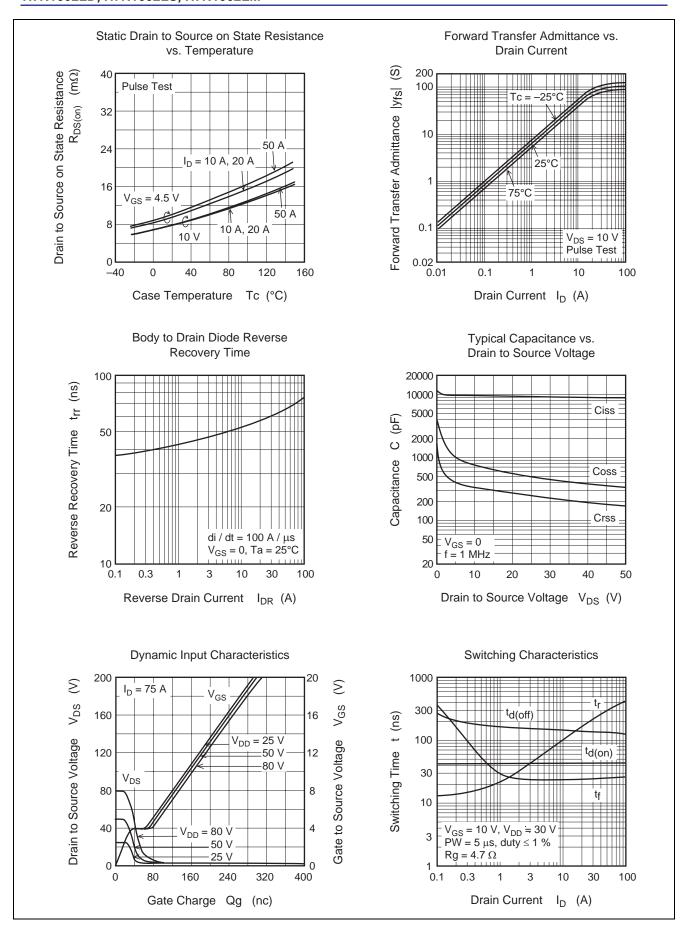
 $(Ta = 25^{\circ}C)$

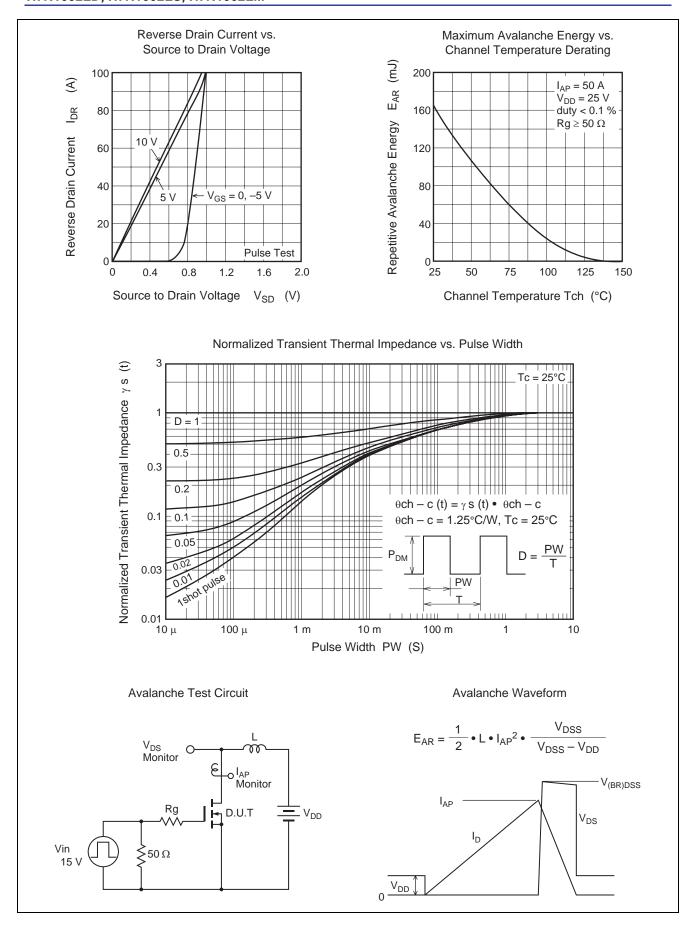
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	100	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V _{(BR) GSS}	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	10	μΑ	$V_{DS} = 100 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS (off)}	1.5	_	2.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$
Static drain to source on state	R _{DS (on)}	_	8	10	mΩ	$I_D = 37.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 4}}$
resistance		_	10	15	mΩ	$I_D = 37.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note 4}}$
Forward transfer admittance	y _{fs}	57	95	_	S	$I_D = 37.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	_	9700	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	740	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	330	_	pF	f = 1 MHz
Total gate charge	Qg	_	155	_	nC	V _{DD} = 50 V
Gate to source charge	Qgs	_	35	_	nC	V _{GS} = 10 V
Gate to drain charge	Qgd	_	33	_	nC	I _D = 75 A
Turn-on delay time	t _{d (on)}	_	43	_	ns	$V_{GS} = 10 \text{ V}, I_D = 37.5 \text{ A}$
Rise time	t _r	_	245	_	ns	$R_L = 0.8 \Omega$
Turn-off delay time	t _{d (off)}	_	130	_	ns	$Rg = 4.7 \Omega$
Fall time	t _f	_	25	_	ns	
Body to drain diode forward voltage	V_{DF}	_	0.93	_	V	I _F = 75 A, V _{GS} = 0
Body to drain diode reverse recovery	t _{rr}	_	70	_	ns	I _F = 75 A, V _{GS} = 0
time						$di_F/dt = 100 A/\mu s$

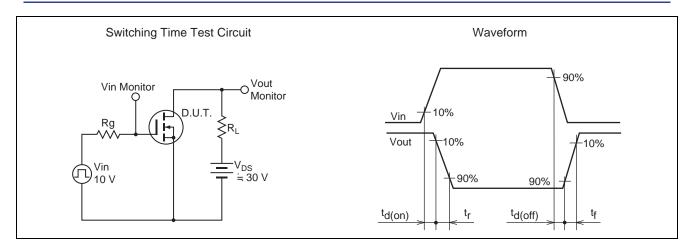
Note: 4. Pulse test

Main Characteristics

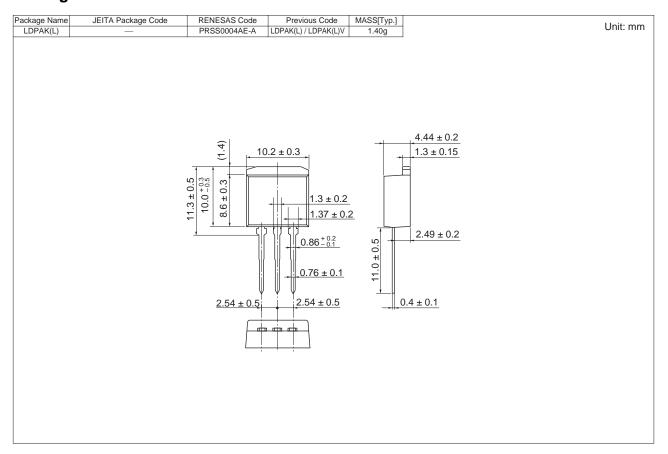


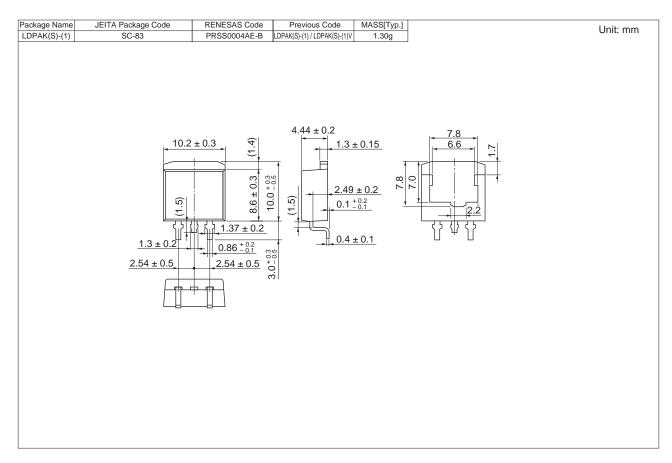


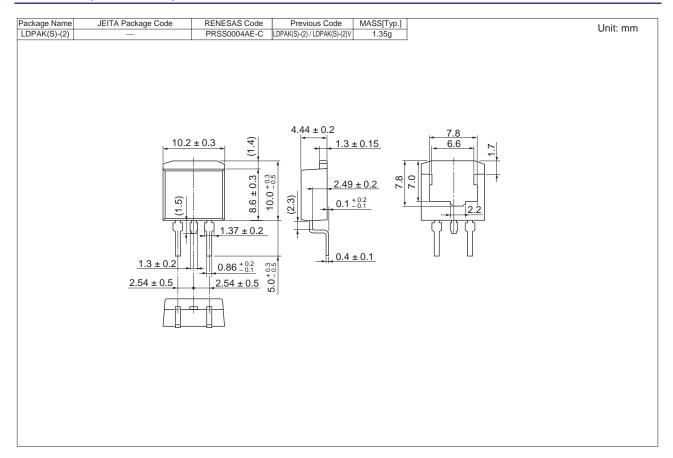




Package Dimensions







Ordering Information

Part Name	Quantity	Shipping Container
H7N1002LD-E	500 pcs	Box (Conductive Sack)
H7N1002LSTL-E	1000 pcs	Taping
H7N1002LMTL-E	1000 pcs	Taping

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