

## ST13003DN

### High voltage fast-switching NPN power transistor

Preliminary data

#### **Features**

- High voltage capability
- Low spread of dynamic parameters
- Very high switching speed
- Integrated free-wheeling diode

### **Application**

■ Compact fluorescent lamps (CFLs)

#### **Description**

The device is manufactured using high voltage multi epitaxial planar technology for high switching speeds and high voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

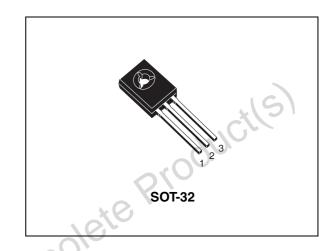


Figure 1. Internal schematic diagram

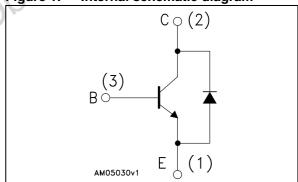


Table 1. Device summary

Order code	Marking	Package	Packaging
ST13003DN	13003DN	SOT-32	BAG

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Electrical ratings ST13003DN

# 1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CES</sub>	Collector-emitter voltage (V <sub>BE</sub> = 0)	700	V
V <sub>CEO</sub>	Collector-emitter voltage (I <sub>B</sub> = 0)	400	V
V <sub>EBO</sub>	Collector-base voltage (I <sub>C</sub> = 0)	9	V
I <sub>C</sub>	Collector current	1	Α
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5 ms)	2	Α
I <sub>B</sub>	Base current	0.5	SA
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 5 ms)	1 G	Α
P <sub>TOT</sub>	Total dissipation at T <sub>c</sub> = 25 °C	20	W
T <sub>STG</sub>	Storage temperature	-55 to 150	°C
TJ	Max. operating junction temperature	150	O

Table 3. Thermal data

	Symbol Parameter		Value	Unit	
	R <sub>thJC</sub>	Thermal resistance junction-case	6.25	°C/W	
Obsole	je P'	coducils			

# 2 Electrical characteristics

 $T_{case} = 25$  °C; unless otherwise specified.

Table 4. Electrical characteristics

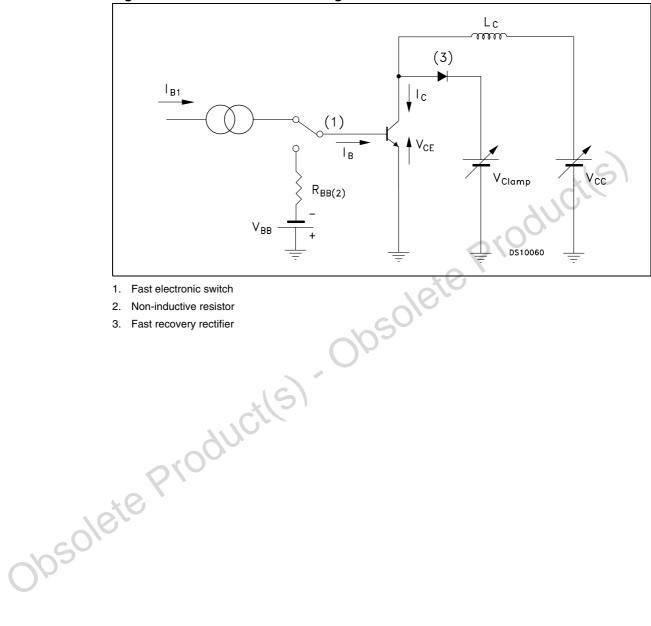
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I <sub>CES</sub>	Collector cut-off current (V <sub>BE</sub> = 0)	$V_{CE} = 700 \text{ V}$ $V_{CE} = 700 \text{ V}$ $T_{C} = 125 ^{\circ}\text{C}$			1 5	mA mA
I <sub>EBO</sub>	Emitter cut-off current (I <sub>C</sub> = 0)	V <sub>EB</sub> = 9 V			1	mA
V <sub>CEO(sus)</sub>	Collector-emitter sustaining voltage (I <sub>B</sub> = 0)	I <sub>C</sub> = 10 mA	400	X	S	V
V <sub>CE(sat)</sub> (1)	Collector-emitter saturation voltage	$I_C = 0.5 \text{ A}$ $I_B = 125 \text{ mA}$ $I_C = 1 \text{ A}$ $I_B = 330 \text{ mA}$	201	70	0.7 1.2	V V
V <sub>BE(sat)</sub> (1)	Base-emitter saturation voltage	$I_C = 0.5 \text{ A}$ $I_B = 125 \text{ mA}$ $I_C = 1 \text{ A}$ $I_B = 330 \text{ mA}$			1.2 1.3	V V
h <sub>FE</sub>	DC current gain	$I_{C} = 0.5 \text{ A}, \qquad V_{CE} = 2 \text{ V}$ $I_{C} = 1 \text{ A}, \qquad V_{CE} = 10 \text{ V}$	6 5		18 15	
t <sub>s</sub>	Inductive Load Storage time Fall time	$I_C = 0.4 \text{ A}$ $V_{clamp} = 300 \text{ V}$ $I_{B(on)} = -I_{B(off)} = 80 \text{ mA}$ $V_{BB(off)} = -5 \text{ V}$ Figure 2		2.5 180		μs ns
V <sub>F</sub>	Diode forward voltage	I <sub>F</sub> = 350 mA		1.5	_	V

<sup>1.</sup> Pulse test: pulse duration ≤ 300 μs, duty cycle ≤ 2 %

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#### 2.1 **Test circuit**

Figure 2. Inductive load switching test circuit



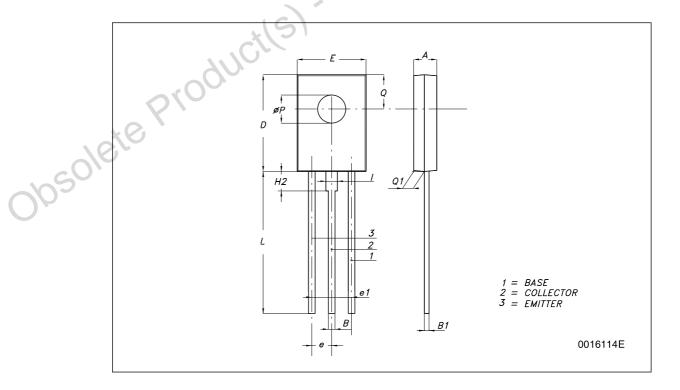
## 3 Package mechanical data

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Obsolete Product(s). Obsolete Product(s)

#### **SOT-32 (TO-126) MECHANICAL DATA**

DIM.	mm.				
DIWI.	MIN.	TYP	MAX.		
Α	2.4		2.9		
В	0.64		0.88		
B1	0.39		0.63		
D	10.5		11.05		
E	7.4		7.8		
е	2.04	2.29	2.54		
e1	4.07	4.58	5.08		
L	15.3		16		
Р	2.9	Q	3.2		
Q		3.8			
Q1	1	18,	1.52		
H2		2.15			
<u> </u>		1.27			



ST13003DN Revision history

# 4 Revision history

Table 5. Document revision history

Date	Revision	Changes
25-Feb-2010	1	First release.



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