TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type (U-MOSIII)

2SJ681

Relay Drive, DC-DC Converter and Motor Drive Applications

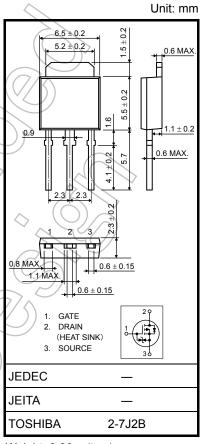
- 4-V gate drive
- Low drain-source ON-resistance: R_{DS (ON)} = 0.12 Ω (typ.)

 $(V_{GS} = -10 \text{ V})$

- High forward transfer admittance: |Y_{fs}| = 5.0 S (typ.)
- Low leakage current: I_{DSS} = -100 μA (max) (V_{DS} = -60 V)
- Enhancement mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V } (V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	-60	V
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	-60	V
Gate-source voltage		V_{GSS}	±20	V
Drain current	DC (Note 1)	I _D	-5	A
	Pulse(Note 1)	I _{DP}	-20	<< <u>A</u>
Drain power dissipation	١	P _D	20	W
Single pulse avalanche	e energy (Note 2)	EAS	40.5	mJ
Avalanche current		IAR	-5	JA
Repetitive avalenche e	nergy (Note 3)	EAR	2	mJ
Channel temperature		Tch	150	→°C
Storage temperature ra	nge	T _{stg}	-55 to 150	°C



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	6.25	°C / W
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = -25 V, T_{ch} = 25°C (initial), L = 2.2 mH, R_G = 25 Ω , I_{AR} = -5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

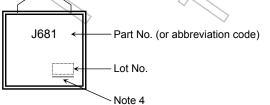
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V	_	_	-100	μΑ
Drain-source breakdown voltage		V (BR) DSS	I _D = -10 mA, V _{GS} = 0 V	-60	_	_	V
		V (BR) DSX	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	-35	/	_	V
Gate threshold	voltage	V_{th}	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$	-0.8) /_	-2.0	V
Drain-source ON resistance		Pro (OV)	V _{GS} = -4 V, I _D = -2.5 A) <u> </u>	0.16	0.25	Ω
		R _{DS} (ON)	$V_{GS} = -10 \text{ V}, I_D = -2.5 \text{ A}$	\mathcal{C}	0.12	0.17	7.2
Forward transfe	r admittance	Y _{fs}	V _{DS} = -10 V, I _D = -2.5 A	2.5	5.0	_	S
Input capacitance		C _{iss}		_	700	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	60	_	
Output capacitance		Coss		_ /	90	\nearrow	
Switching time	Rise time	t _r	V_{GS} V_{GS} $V_{DD} \approx -30 \text{ V}$	-	14	> _	ns
	Turn-on time	t _{on}		7	24	_	
	Fall time	t _f		2	14	_	
	Turn-off time	t _{off}	Duty ≤ 1%, t _w ≠ 10 μs) —	95	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	15	_	nC
Gate-source charge		Q _{gs}	$V_{DD} \approx -48 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -5 \text{ A}$	_	11	_	
Gate-drain ("miller") charge		Q _{gd}		_	4	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR	<u> </u>	_	_	-5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	-	_	_	-20	А
Forward voltage (diode)	V _{DSF}	$I_{DR} = -5 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.7	٧
Reverse recovery time	t _{rr}	I _{DR} = -5 A, V _{GS} = 0 V	_	40	_	ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 50 A / μs	_	32	_	nC

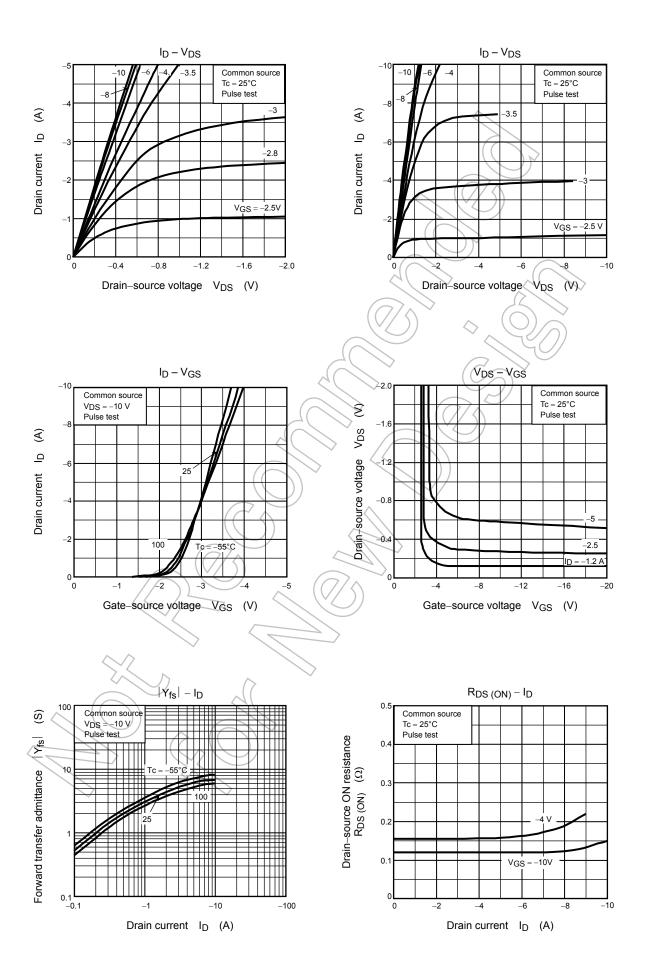




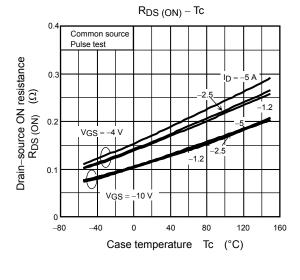
Note 4: A line under a Lot No. identifies the indication of product Labels.

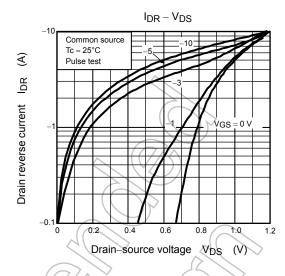
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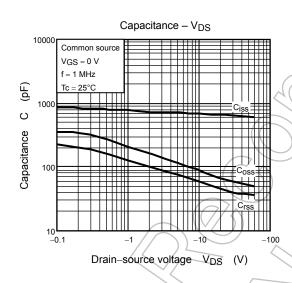
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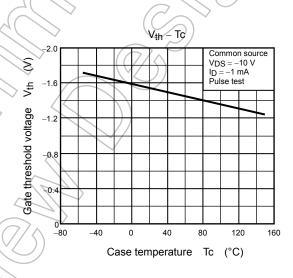


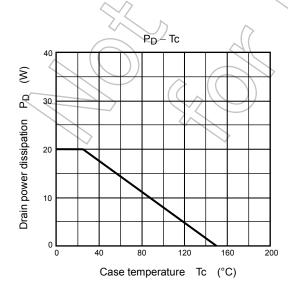
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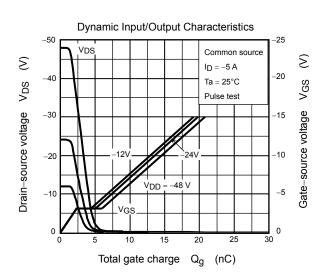


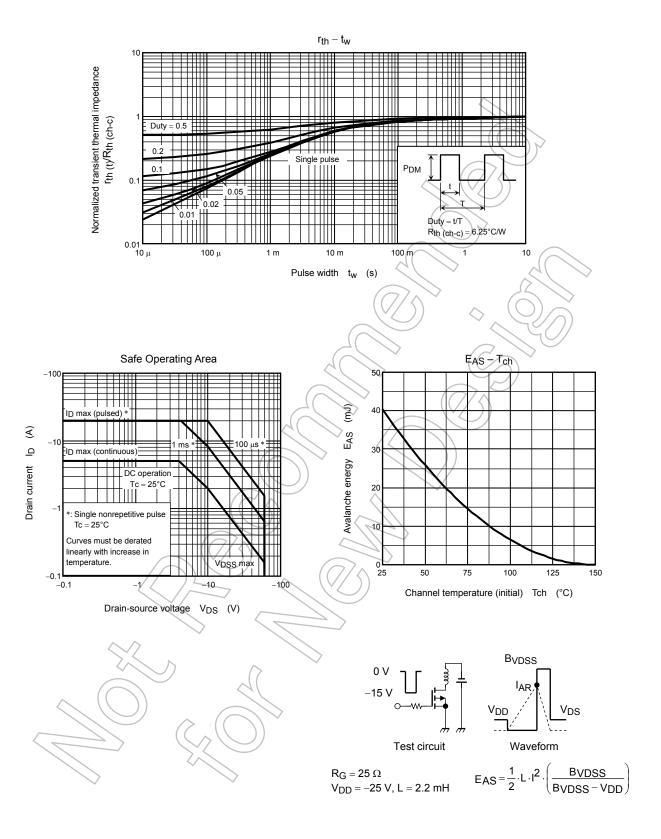












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6