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TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSII⁻⁵)

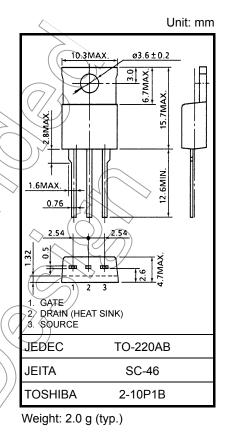
2SK1119

DC-DC Converter and Motor Drive Applications

- Low drain-source ON resistance $R_{DS}(ON) = 3.0 \Omega$ (typ.)
- High forward transfer admittance $|Y_{fs}| = 2.0 \text{ S (typ.)}$
- Low leakage current $I_{DSS} = 300 \ \mu A \ (max) \ (V_{DS} = 800 \ V)$
- Enhancement mode $: V_{th} = 1.5 \text{ to } 3.5 \text{ V} (V_{DS} = 10 \text{ V}, \text{ID} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

	_			$\langle \vee \rangle $
Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	1000	> V
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	1000	^v v
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	ID <	4	
	Pulse (Note 1)	IDP	12	$\langle \langle A \rangle$
Drain power dissipation (Tc = 25°C)		PD	100	Ŵ
Channel temperature		Tch	150	°C
Storage temperature range			-55 to 150	°C
			11	11



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	1.25	°C / W
Thermal resistance, channel to	Rth (ch-a)	83.3	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

This transistor is an electrostatic-sensitive device. Please handle with caution.

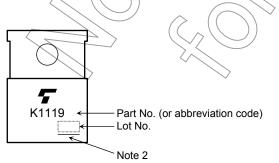
Electrical Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I _{GSS}	V _{GS} = ±20 V, V _{DS} = 0 V	—	—	±100	nA
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 800 V, V _{GS} = 0 V	_	—	300	μA
Drain−source bi voltage	reakdown	V (BR) DSS	I_D = 10 mA, V _{GS} = 0 V	1000	_	_	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	(1.5	4	3.5	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 2 A	$\langle \rangle$	3.0	3.8	Ω
Forward transfe	r admittance	Y _{fs}	$V_{DS} = 20 V, I_D = 2 A$	1.0	2.0	_	S
Input capacitand	ce	C _{iss}		2	700	_	
Reverse transfe	er capacitance	C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	> —	55	_	pF
Output capacita	nce	Coss		_	100	_	
Switching time	Rise time	tr	$v_{\rm GS}^{10V}$	_ (18	\rightarrow	
	Turn-on time	t _{on}	$V_{GS} = \frac{V_{OUT}}{200 \Omega}$	_((30) —	
	Fall time	t _f			12		ns
	Turn-off time	t _{off}	$V_{DD} = 400V$ $Duty \leq 1\%, t_W = 10\mu s$	P	70	_	
Total gate char plus gate-drain	rge (Gate-source)	Qg		_	60	_	
Gate-source ch	arge	Q _{gs}	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$	_	35	_	nC
Gate-drain ("mi	ller") charge	Q _{gd}		_	25	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current	IPR	- (V)-	_	_	4	А
Pulse drain reverse current (Note 1)			_	_	12	A
Forward voltage (diode)	V _{DSF}	I _{DR} = 4 A, V _{GS} = 0 V		_	-1.9	V
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Marking



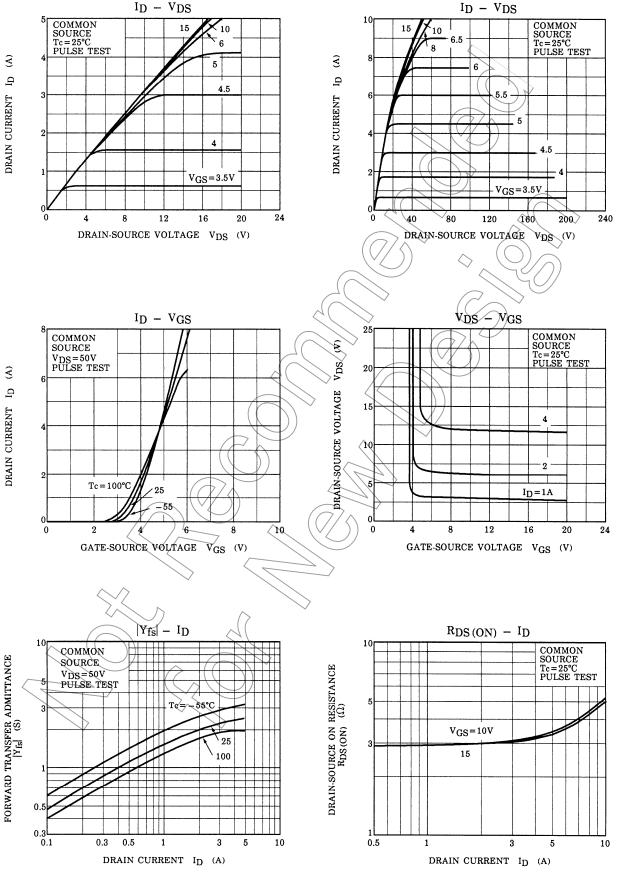
Note 2: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

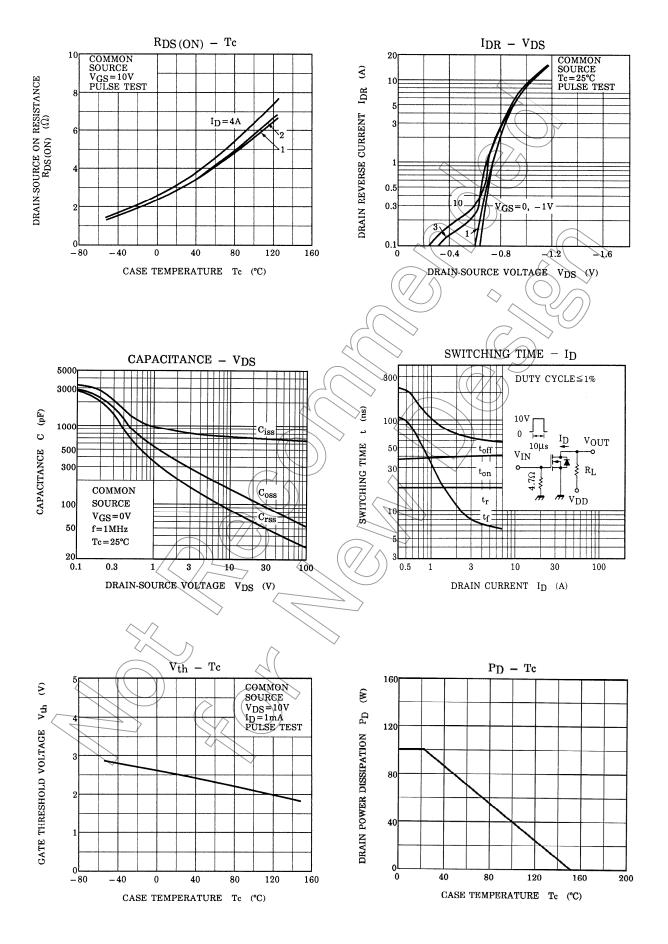
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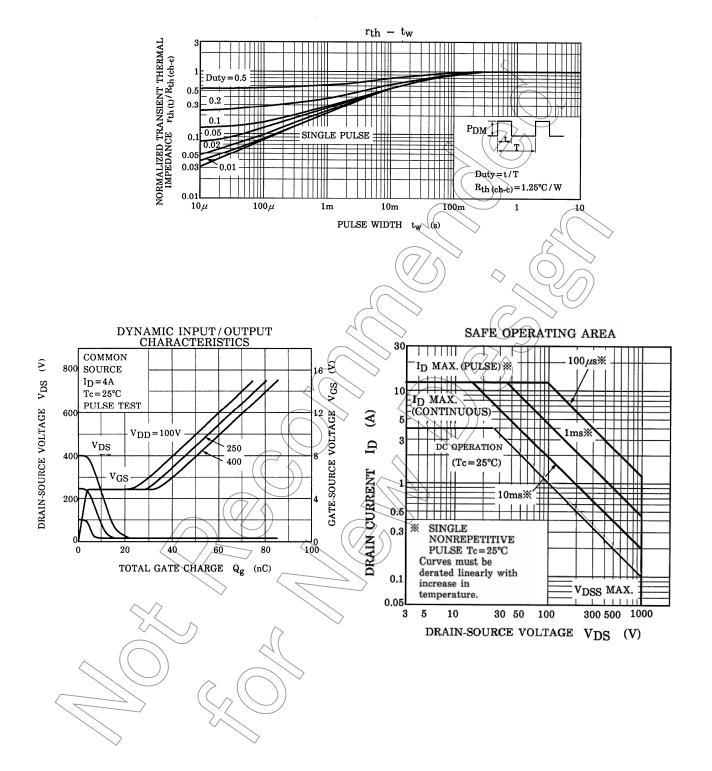
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