# WINset-VL R4x00
# WINset-PCI R4x00

Mentor ARC

## Standard Features

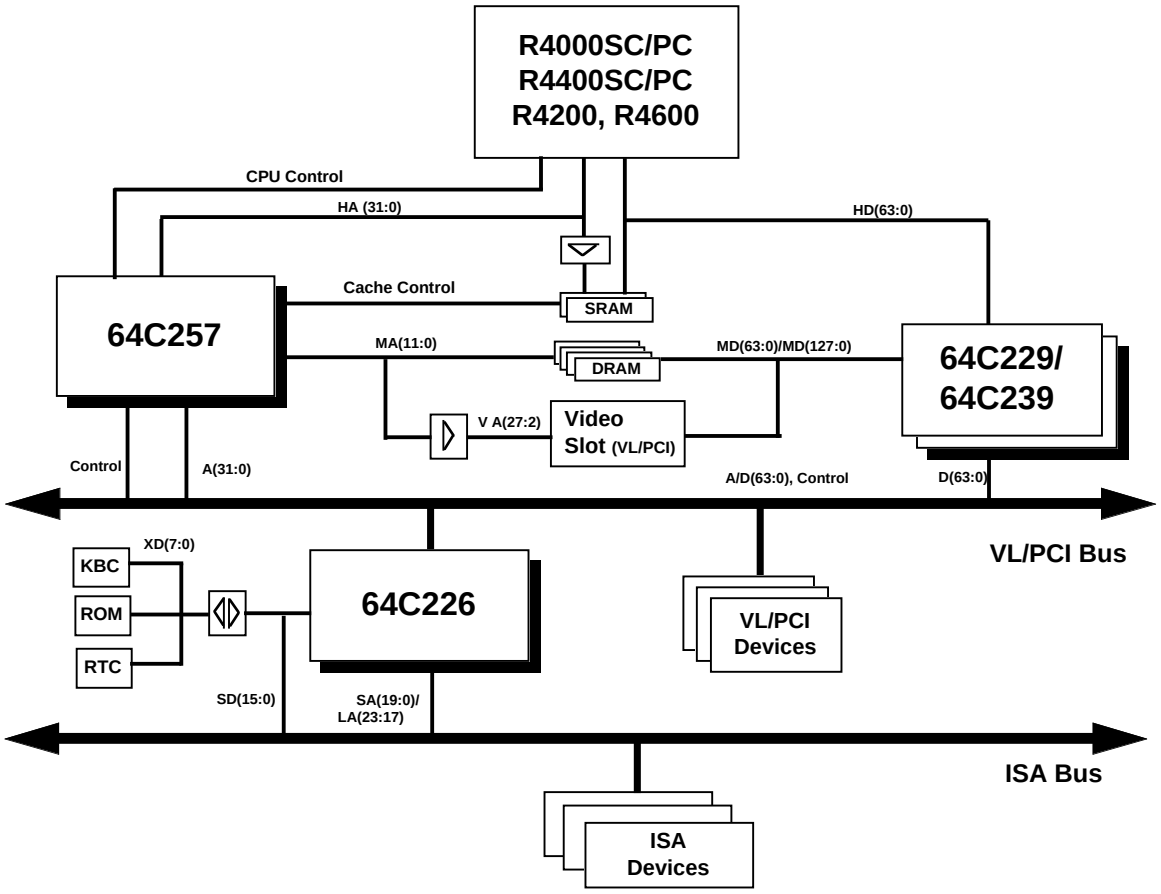
- ❑ Wide range CPU support R4000SC/PC, R4600 and R4400SC/PC
- ❑ VESA/PCI specification fullyCompatible
- ❑ High integration and high performance
- ❑ Separate VL/PCI and CPU bus to perform concurrent operation
- ❑ L2 write-back/write-through cache controller for PC package
- ❑ 64-bit VL/PCI bus with optional 128 bit memory bus
- ❑ Green PC and APM compliant

Mentor ARC WINset R4x00 Chipset is designed to enable the high-performance workstation-level Green PC design. Its architecture is designed to support all R4x00 family processors under Windows™ NT environments. WINset R4x00 is comprised of three major components: the 64C257 Cache, Memory and VL local bus control unit, the 64C226 VL/PCI and ISA bridge unit, and the 64C229 or 64C239 Data Path unit. The 64C257 integrates a high-performance secondary write-back cache controller supporting up to 1M cache memory, a high-performance memory controller supporting up to 256MB main memory with flexible programmability of burst timing, and a VL arbiter supporting three VL master devices. The 64C226 integrates VL to ISA bridge, 82C206 (excluding RTC), and power management. The power management is designed to combine with APM compliant specifications to support Green PCs. The 64C239 and 64C229 integrate three port data buffers. Two 86C229's support full 64 bit systems including 64 bit VL/PCI bus. Two 86C239s can be used to design a 128 bit memory bus. As the block diagram shows, the highly integrated WINset-R4x00 chipset provides the combinations of high performance, low-cost and power efficient features for Windows and green desktops.

## CPUs Supported

R4000SC/PC, R4400SC/PC, R4600, R4700

## Host Platforms




**WinSET-PCI R4x00**
**System Block Diagram**

## Contact List

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